



CMOS OCTAL BUS TRANSCEIVER AND 3.3V TO 5V SHIFTER WITH 3-STATE OUT- PUTS AND 5 VOLT TOLERANT I/O

IDT74LVC4245A

FEATURES:

- 0.5 MICRON CMOS Technology
- $V_{CCA} = 5V \pm 0.5V$
- $V_{CCB} = 2.7V$ to $3.6V$
- CMOS power levels ($0.4\mu W$ typ. static)
- Rail-to-Rail output swing for increased noise margin
- All inputs, outputs and I/O are 5 Volt tolerant
- Supports hot insertion
- Available in SOIC, SSOP, QSOP, and TSSOP packages

Drive Features for LVC4245A:

- High Output Drivers: $\pm 24mA$
- Reduced system switching noise

APPLICATIONS:

- 5V and 3.3V mixed voltage systems
- Data communication and telecommunication systems

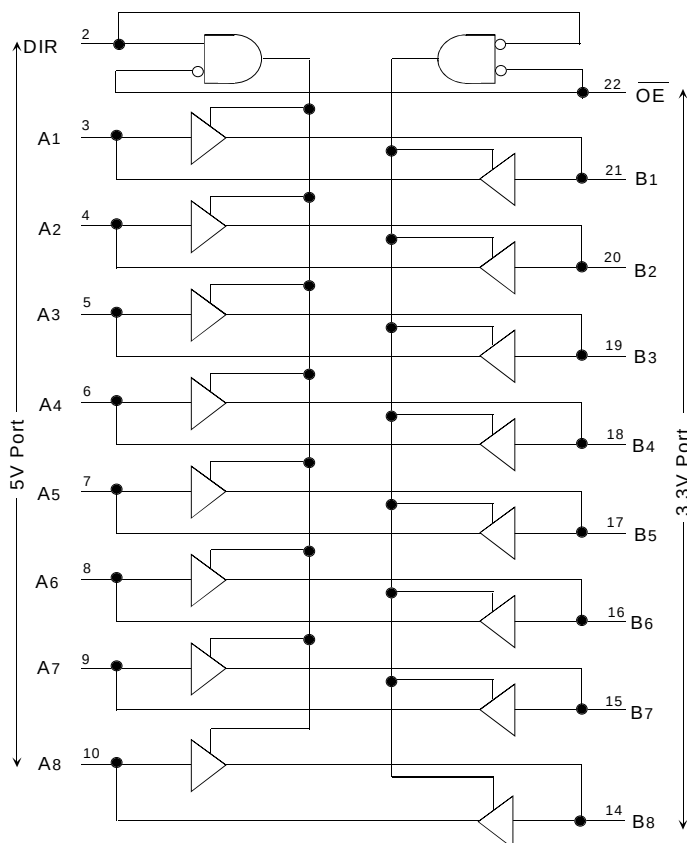
DESCRIPTION:

The LVC4245A is manufactured using advanced dual metal CMOS technology. This octal noninverting bus transceiver contains two separate supply rails; B port has V_{CCB} , which is set at 3.3V, and A port has V_{CCA} , which is set at 5V. This allows for translation from a 3.3V to a 5V environment, and vice-versa.

This device is ideal for asynchronous communication between two buses (A and B). The direction control pin (DIR) controls the direction of data flow. The output enable pin ($\overline{OE}$) overrides the direction control and disables both ports. All inputs are designed with hysteresis for improved noise margin.

The LVC4245A has been designed with a $\pm 24mA$ output driver. This driver is capable of driving a moderate to heavy load while maintaining speed performance.

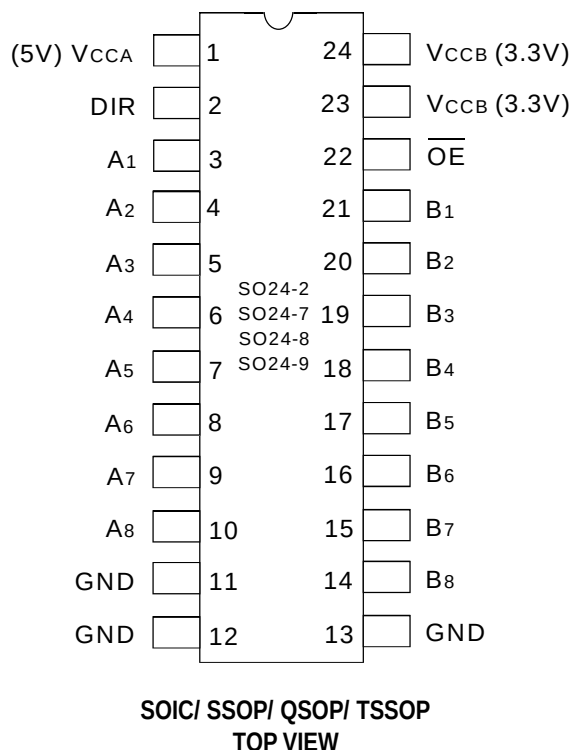
FUNCTIONAL BLOCK DIAGRAM



INDUSTRIAL TEMPERATURE RANGE

JULY 2000

PIN CONFIGURATION



PIN DESCRIPTION

Pin Names	Description
$\overline{OE}$	Output Enable Input (Active LOW)
DIR	Direction Control Input
Ax	Port A Inputs or 3-State Outputs
Bx	Port B Inputs or 3-State Outputs

FUNCTION TABLE (1)

Inputs		Outputs
$\overline{OE}$	DIR	
L	L	Bus B Data to Bus A
L	H	Bus A Data to Bus B
H	X	High Z State

NOTE:

- H = HIGH Voltage Level
L = LOW Voltage Level
X = Don't Care

ABSOLUTE MAXIMUM RATINGS

FOR VCCA (1)

Symbol	Description	Max.	Unit
VTERM	Terminal Voltage with Respect to GND	– 0.5 to +6.5	V
TSTG	Storage Temperature	– 65 to +150	°C
IOUT	DC Output Current	– 50 to +50	mA
I _{IK} I _{OK}	Continuous Clamp Current, V _I < 0 or V _O < 0	– 50	mA
I _{CC} I _{SS}	Continuous Current through each V _{CC} or GND	±100	mA

ABSOLUTE MAXIMUM RATINGS

FOR VCCB (1)

Symbol	Description	Max.	Unit
VTERM	Terminal Voltage with Respect to GND	– 0.5 to +6.5	V
TSTG	Storage Temperature	– 65 to +150	°C
IOUT	DC Output Current	– 50 to +50	mA
I _{IK} I _{OK}	Continuous Clamp Current, V _I < 0 or V _O < 0	– 50	mA
I _{CC} I _{SS}	Continuous Current through each V _{CC} or GND	±100	mA

NOTE:

- Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

CAPACITANCE (T_A = +25°C, f = 1.0MHz)

Symbol	Parameter ⁽¹⁾	Conditions	Typ.	Max.	Unit
C _{IN}	Input Capacitance	V _{IN} = 0 or VCCA VCCA = Open	5	—	pF
C _{I/O}	I/O Port Capacitance ⁽²⁾	V _{OUT} = VCCA or GND, VCCA = 5V	11	—	pF
C _{I/O}	I/O Port Capacitance ⁽³⁾	V _{OUT} = VCCB or GND, VCCB = 3.3V	11	—	pF

NOTES:

- As applicable to the device type.
- For A port only.
- For B port only.

DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE (A PORT)

Following Conditions Apply Unless Otherwise Specified:

Operating Condition: $T_A = -40^{\circ}\text{C}$ To $+85^{\circ}\text{C}$; $V_{CCA} = 5V \pm 0.5V$ ⁽¹⁾

Symbol	Parameter	Test Conditions		Min.	Typ. ⁽²⁾	Max.	Unit
V_{IH}	Input HIGH Voltage Level	$V_{CCA} = 4.5V$ to $5.5V$		2	—	—	V
V_{IL}	Input LOW Voltage Level	$V_{CCA} = 4.5V$ to $5.5V$		—	—	0.8	V
I_{IH} I_{IL}	Input Leakage Current (control inputs)	$V_{CCA} = 5.5V$	$V_I = 0$ to $5.5V$	—	—	± 1	μA
I_{OZH} I_{OZL}	High Impedance Output Current (3-State Output pins)	$V_{CCA} = 5.5V$	$V_O = 0$ to $5.5V$	—	—	± 5	μA
V_H	Input Hysteresis	$V_{CCA} = 5.0V$		—	100	—	mV
I_{CCL} I_{CCH} I_{CCZ}	Quiescent Power Supply Current	$V_{CCA} = 5.5V$	$V_{IN} = GND$ or V_{CCA}	—	—	80	μA
ΔI_{CC}	Quiescent Power Supply Current Variation	One input at $3.4V$, other inputs at V_{CCA} or GND $V_{CCA} = 4.5V$ to $5.5V$		—	—	1.5	mA

NOTES:

- $V_{CCB} = 2.7V$ to $3.6V$
- Typical values are at $V_{CCA} = 5.0V$, $+25^{\circ}\text{C}$ ambient.

DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE (B PORT)

Following Conditions Apply Unless Otherwise Specified:

Operating Condition: $T_A = -40^{\circ}\text{C}$ To $+85^{\circ}\text{C}$; $V_{CCB} = 2.7V$ To $3.6V$ ⁽¹⁾

Symbol	Parameter	Test Conditions		Min.	Typ. ⁽²⁾	Max.	Unit
V_{IH}	Input HIGH Voltage Level	$V_{CCB} = 2.7V$ to $3.6V$		2	—	—	V
V_{IL}	Input LOW Voltage Level	$V_{CCB} = 2.7V$ to $3.6V$		—	—	0.8	V
I_{OZH} I_{OZL}	High Impedance Output Current (3-State Output pins)	$V_{CCB} = 3.6V$	$V_O = 0$ to V_{CCB}	—	—	± 5	μA
V_H	Input Hysteresis	$V_{CCB} = 3.3V$		—	100	—	mV
I_{CCL} I_{CCH} I_{CCZ}	Quiescent Power Supply Current	$V_{CCB} = 3.6V$	$V_{IN} = GND$ or V_{CCB}	—	—	50	μA
ΔI_{CC}	Quiescent Power Supply Current Variation	One input at $V_{CCB} - 0.6V$, other inputs at V_{CCB} or GND $V_{CCB} = 2.7V$ to $3.6V$		—	—	500	μA

NOTES:

- $V_{CCA} = 5V \pm 0.5V$
- Typical values are at $V_{CCB} = 3.3V$, $+25^{\circ}\text{C}$ ambient.

OUTPUT DRIVE CHARACTERISTICS, $V_{CCA} = 5V \pm 0.5V$ (A PORT)

Symbol	Parameter	Test Conditions ⁽¹⁾		Min.	Max.	Unit
VOH	Output HIGH Voltage (B port to A port)	VCCA = 4.5V	IOH = -0.1mA	4.3	—	V
		VCCA = 5.5V		5.3	—	
		VCCA = 4.5V	IOH = -24mA	3.7	—	
		VCCA = 5.5V		4.7	—	
VOL	Output LOW Voltage (B port to A port)	VCCA = 4.5V	IOL = 0.1mA	—	0.2	V
		VCCA = 5.5V		—	0.2	
		VCCA = 4.5V	IOL = 24mA	—	0.55	
		VCCA = 5.5V		—	0.55	

NOTE:

1. VIH and VIL must be within the min. or max. range shown in the DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE table for the appropriate VCC range. TA = -40°C to +85°C, VCCB = 2.7V to 3.6V.

OUTPUT DRIVE CHARACTERISTICS, $V_{CCB} = 2.7V$ TO $3.6V$ (B PORT)

Symbol	Parameter	Test Conditions ⁽¹⁾		Min.	Max.	Unit
VOH	Output HIGH Voltage (A port to B port)	VCCB = 2.7V to 3.6V	IOH = -0.1mA	VCC - 0.2	—	V
		VCCB = 2.7V	IOH = -12mA	2.2	—	
		VCCB = 3.0V		2.4	—	
		VCCB = 3.0V	IOH = -24mA	2	—	
VOL	Output LOW Voltage (A port to B port)	VCCB = 2.7V to 3.6V	IOL = 0.1mA	—	0.2	V
		VCCB = 2.7V	IOL = 12mA	—	0.4	
		VCCB = 3.0V	IOL = 24mA	—	0.55	

NOTE:

1. VIH and VIL must be within the min. or max. range shown in the DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE table for the appropriate VCC range. TA = -40°C to +85°C, VCCA = 5V ± 0.5V.

OPERATING CHARACTERISTICS, TA = 25°C

Symbol	Parameter	Test Conditions	VCCA = 5V, VCCB = 3.3V	Unit
			Typical	
CPD	Power dissipation capacitance per transceiver Outputs enabled	CL = 0pF, f = 10Mhz	39.5	pF
CPD	Power dissipation capacitance per transceiver Outputs disabled		5	pF

SWITCHING CHARACTERISTICS ⁽¹⁾

		V _{CCA} = 5V±0.5V, V _{CCB} = 2.7V to 3.6V		Unit
Symbol	Parameter	Min.	Max.	
t _{PHL}	Propagation Delay	1	6.3	ns
t _{PLH}	Ax to Bx	1	6.7	
t _{PHL}	Propagation Delay	1	6.1	ns
t _{PLH}	Bx to Ax	1	5	
t _{PZL}	Output Enable Time	1	8.8	ns
t _{PZH}	$\overline{OE}$ to Bx	1	9.8	
t _{PZL}	Output Enable Time	1	9	ns
t _{PZH}	$\overline{OE}$ to Ax	1	8.1	
t _{PLZ}	Output Disable Time	1	7.7	ns
t _{PHZ}	$\overline{OE}$ to Bx	1	7.8	
t _{PLZ}	Output Disable Time	1	7	ns
t _{PHZ}	$\overline{OE}$ to Ax	1	5.8	

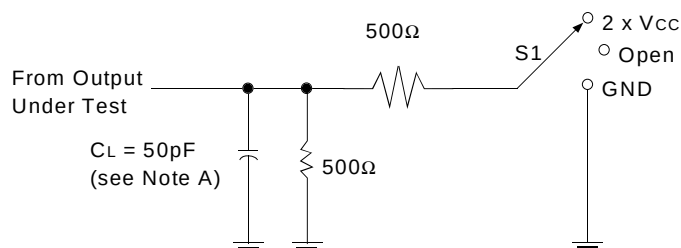
NOTE:

1. See test circuits and waveforms. T_A = – 40°C to + 85°C.

LOAD CIRCUIT AND VOLTAGE WAVEFORMS

PARAMETER MEASUREMENT INFORMATION (A PORT)

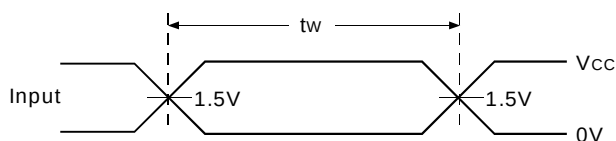
LOAD CIRCUIT



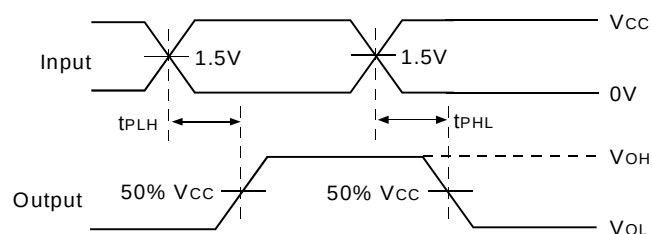
TEST CONDITIONS

TEST	S1
t_{PLH} / t_{PHL}	Open
t_{PLZ} / t_{PZL}	$2 \times V_{CC}$
t_{PHZ} / t_{PZH}	GND

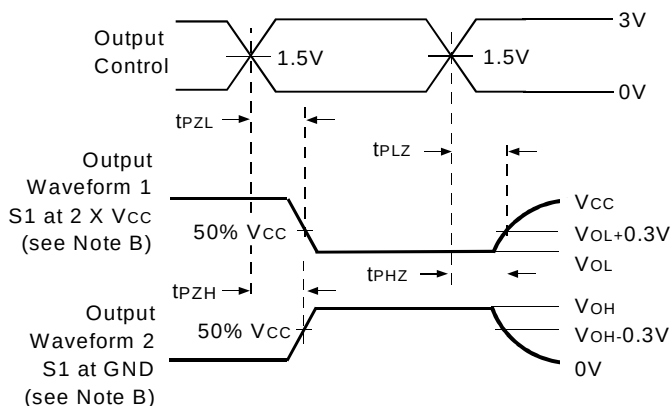
VOLTAGE WAVEFORMS PULSE DURATION



VOLTAGE WAVEFORMS PROPAGATION DELAY TIMES NONINVERTING OUTPUTS



VOLTAGE WAVEFORMS ENABLE AND DISABLE TIMES LOW- AND HIGH-LEVEL ENABLING



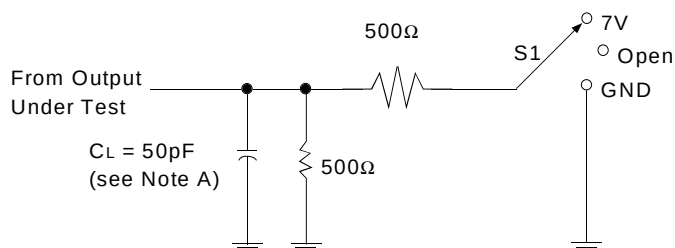
NOTES:

- C_L includes probe and jig capacitance.
- Waveform 1 is for an output with internal conditions such that the output is LOW except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is HIGH except when disabled by the output control.
- All input pulses are supplied by generators having the following characteristics: $PRR \leq 10\text{MHz}$; $Z_o = 50\Omega$; $t_r \leq 2.5\text{ns}$; $t_f \leq 2.5\text{ns}$.
- The outputs are measured one at a time with one transition per measurement.

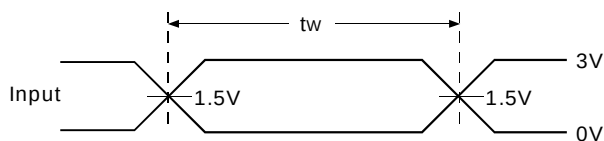
LOAD CIRCUIT AND VOLTAGE WAVEFORMS

PARAMETER MEASUREMENT INFORMATION (B PORT)

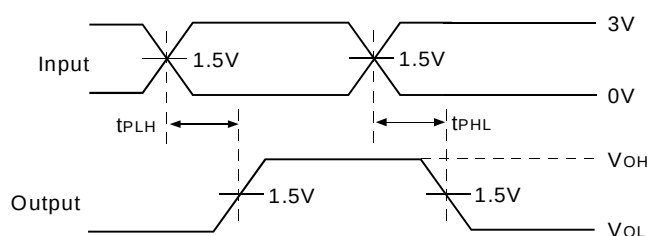
LOAD CIRCUIT



VOLTAGE WAVEFORMS PULSE DURATION



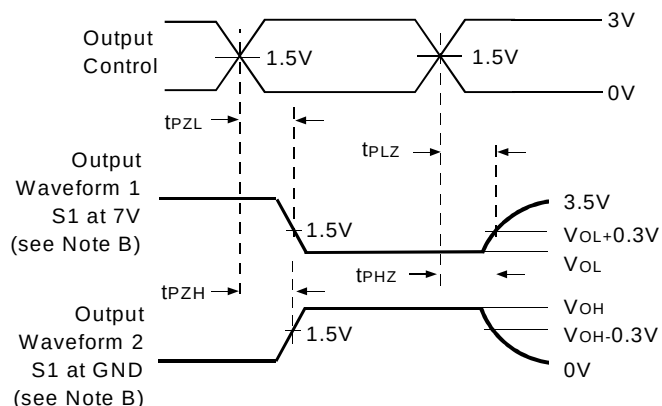
VOLTAGE WAVEFORMS PROPAGATION DELAY TIMES NONINVERTING OUTPUTS



TEST CONDITIONS

TEST	S1
tPLH / tPHL	Open
tPLZ / tPZL	7V
tPHZ / tPZH	GND

VOLTAGE WAVEFORMS ENABLE AND DISABLE TIMES LOW- AND HIGH-LEVEL ENABLING



NOTES:

- C_L includes probe and jig capacitance.
- Waveform 1 is for an output with internal conditions such that the output is LOW except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is HIGH except when disabled by the output control.
- All input pulses are supplied by generators having the following characteristics: PRR $\leq 10\text{MHz}$; $Z_o = 50\Omega$; $t_r \leq 2.5\text{ns}$; $t_f \leq 2.5\text{ns}$.
- The outputs are measured one at a time with one transition per measurement.

ORDERING INFORMATION

IDT XX LVC X XXXX XX
 Temp. Range Bus-Hold Device Type Package

				SO	Small Outline IC (gull wing) (SO24-2)
				PY	Shrink Small Outline Package (SO24-7)
				Q	Quarter Size Small Outline Package (SO24-8)
				PG	Thin Shrink Small Outline Package (SO24-9)
			4245A		Octal Bus Transceiver and 3.3V to 5V Shifter with 3-State Outputs, $\pm 24\text{mA}$
		Blank			No Bus-hold
74					-40°C to $+85^{\circ}\text{C}$



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