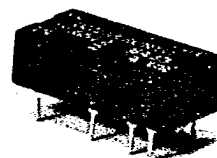


SERIES DL6570 DYNAMIC RAM TIMING MODULES

- 14 Pin Package • Specifically Designed for Dynamic Ram Timing
- Used with Multiplex Address Rams

Specifications

- Supply Voltage : 4.75 to 5.25VDC
- Logic 1 Input Current : 50 μ A Max
- Logic 0 Input Current : -2mA Max
- Logic 1 Volt Out : 2.5v Min
- Logic 0 Volt Out : 0.5v Max
- Logic 1 Fan-Out : 20/Tap Max
- Logic 0 Fan-Out : 10/Tap Max
- Power Dissipation : 240mW Total
- Operating Temp Range : 0°C to 70°C
- Temp Coefficient : 100PPM/°C

**Input Test Conditions**

- Input Pulse Voltage : 3.2v
- Input Rise Time : ≤ 3 NS
- Input Current : 60mA Typ.
- Pulse Width : Min 40% of Total Delay

Electrical Specifications at 25°C (measured with no loads on Taps)

Part Number	Total Delay at Each Tap NSEC ⁽¹⁾				Output	Rise Time NS Max ⁽²⁾
	Tap 1	Tap 2	Tap 3	Tap 4		
DL6571	25 $\pm$ 5	45 $\pm$ 5	160 $\pm$ 8	185 $\pm$ 9	340 $\pm$ 17	4
DL6572	30 $\pm$ 5	80 $\pm$ 5	220 $\pm$ 11	250 $\pm$ 12.5	400 $\pm$ 20	4
DL6573	40 $\pm$ 5	100 $\pm$ 5	265 $\pm$ 13	290 $\pm$ 14.5	440 $\pm$ 22	4
DL6574	85 $\pm$ 5	140 $\pm$ 5	320 $\pm$ 16	345 $\pm$ 17	515 $\pm$ 25.5	5

Note: (1) Measured at 1.5v level leading edge.

(2) Measured from 0.75v to 2.4v.

