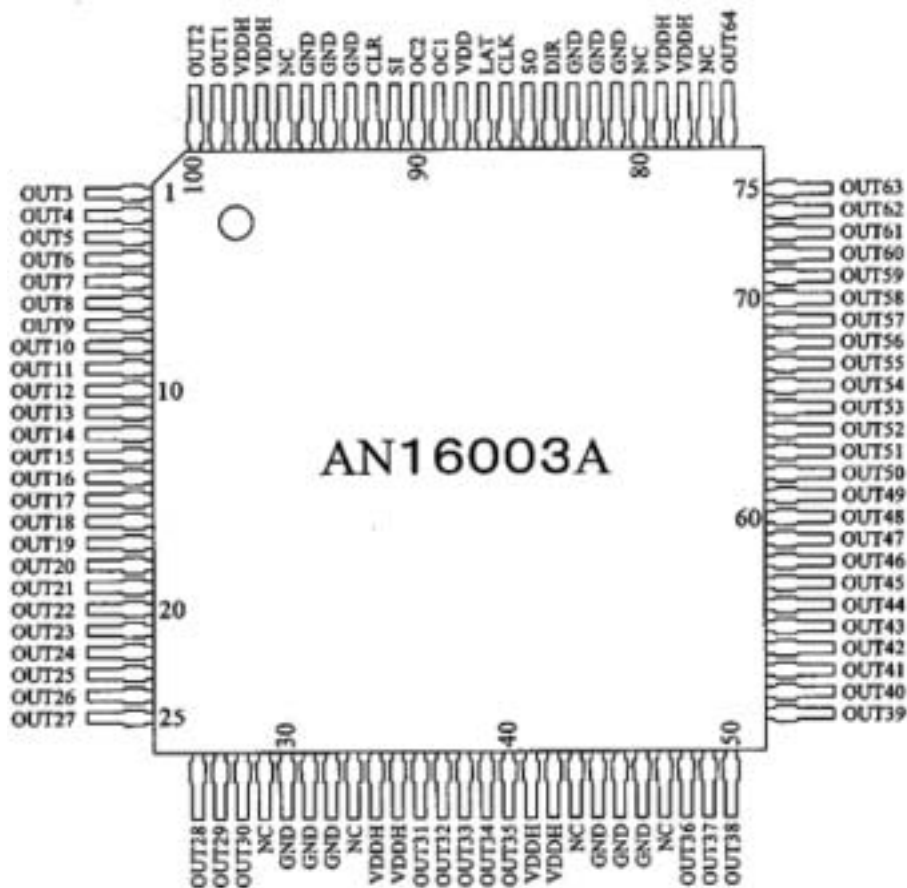


■ AN16003A-K (50 SCAN A ASSY : IC3001 - IC3006)
 (50 SCAN B ASSY : IC3201 - IC3206)

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● Pin Arrangement (Top view)



**AN16003A-K (50 SCAN A ASSY : IC3001 - IC3006)
(50 SCAN B ASSY : IC3201 - IC3206)**

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Plasma Display Panel IC

● Pin Function (1/3)

Pin No.	Pin Name	Type	Discription
1	OUT3	Output	High-voltage push-pull output pin
2	OUT4		
3	OUT5		
4	OUT6		
5	OUT7		
6	OUT8		
7	OUT9		
8	OUT10		
9	OUT11		
10	OUT12		
11	OUT13		
12	OUT14		
13	OUT15		
14	OUT16		
15	OUT17		
16	OUT18		
17	OUT19		
18	OUT20		
19	OUT21		
20	OUT22		
21	OUT23		
22	OUT24		
23	OUT25		
24	OUT26		
25	OUT27		
26	OUT28		
27	OUT29		
28	OUT30		
29	N.C	-	Not connected
30	GND	Ground	GND pin
31	GND	Ground	GND pin
32	GND	Ground	GND pin
33	N.C	-	Not connected
34	V _{DDH}	Supply	High-voltage circuit supply pin
35	V _{DDH}	Supply	High-voltage circuit supply pin
36	OUT31	Output	High-voltage push-pull output pin
37	OUT32		
38	OUT33		
39	OUT34		
40	OUT35		
41	V _{DDH}	Supply	High-voltage circuit supply pin
42	V _{DDH}	Supply	High-voltage circuit supply pin
43	N.C	-	Not connected
44	GND	Ground	GND pin
45	GND	Ground	GND pin
46	GND	Ground	GND pin
47	N.C	-	Not connected
48	OUT36	Output	High-voltage push-pull output pin
49	OUT37		
50	OUT38		

**AN16003A-K (50 SCAN A ASSY : IC3001 - IC3006)
(50 SCAN B ASSY : IC3201 - IC3206)**

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● Pin Function (2/3)

Pin No.	Pin Name	Type	Discription			
51	OUT39	Output	High-voltage push-pull output pin			
52	OUT40					
53	OUT41					
54	OUT42					
55	OUT43					
56	OUT44					
57	OUT45					
58	OUT46					
59	OUT47					
60	OUT48					
61	OUT49					
62	OUT50					
63	OUT51					
64	OUT52					
65	OUT53					
66	OUT54					
67	OUT55					
68	OUT56					
69	OUT57					
70	OUT58					
71	OUT59					
72	OUT60					
73	OUT61					
74	OUT62					
75	OUT63					
76	OUT64					
77	N.C	-	Not connected			
78	V _{DDH}	Supply	High-voltage circuit supply pin			
79	V _{DDH}	Supply	High-voltage circuit supply pin			
80	N.C	-	Not connected			
81	GND	Ground	GND pin			
82	GND	Ground	GND pin			
83	GND	Ground	GND pin			
84	DIR	Input	Setup pin of sift register sift direction L: Shift into reverse (SO → SI) H: Shift forward (SI → SO)			
85	SO	Input/Output	Serial data input/output pin			
86	CLK	Input	Serial clock input pin Fetch SI or SO data to sift register by CLK rise edge			
87	LAT	Input	LAT data input pin L: Transfer shft register data to output latch H: Hold data to output latch			
88	VDD	Supply	Logic supply pin			
89	OC1	Input	Output control pin Control output according to the right truth value table	OC1	OC2	OUT
				L	L	ALL HiZ
				L	H	DATA
				H	L	ALL L
				H	H	ALL H
90	OC2					

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■ **AN16003A-K (50 SCAN A ASSY : IC3001 - IC3006)**
(50 SCAN B ASSY : IC3201 - IC3206)

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Plasma Display Panel IC

● **Pin Function (3/3)**

Pin No.	Pin Name	Type	Discription
91	SI	Input/Output	Serial data input/output pin
92	CLK	Input	All output reset pin CLK pin: L → Normal operation CLK pin: H → All output High
93	GND	Ground	GND pin
94	GND	Ground	GND pin
95	GND	Ground	GND pin
96	N.C	-	Not connected
97	V _{DDH}	Supply	High-voltage circuit supply pin
98	V _{DDH}	Supply	High-voltage circuit supply pin
99	OUT1	Output	High-voltage push-pull output pin
100	OUT2	Output	High-voltage push-pull output pin