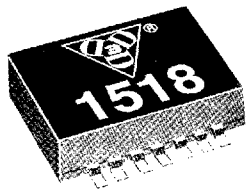
5 Taps — Delay Lines

SERIES: 1518

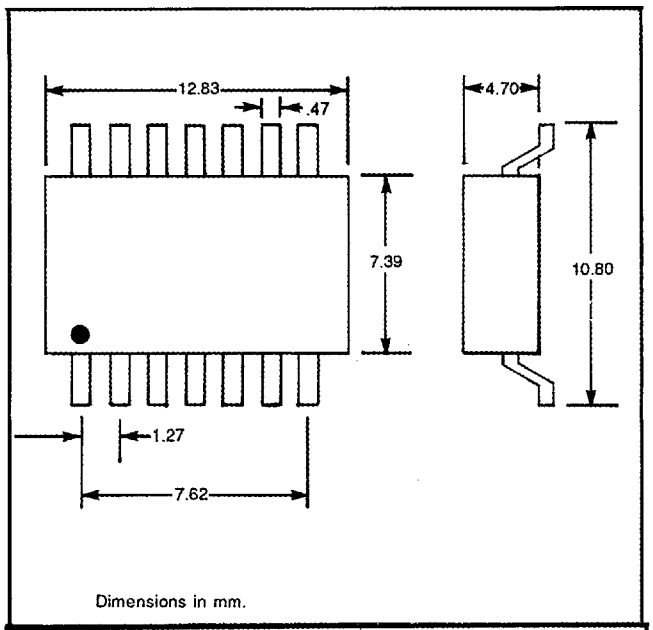
$T_D/T_R = 3/1$

data delay devices, inc.



FEATURES:

- **200 ns delay.**
- **5 taps.**
- **Low D.C. resistance.**
- **Fast delivery.**



Dimensions in mm.

PART NUMBER MAKE-UP

1518 101 151 A

SERIES: 1518

DELAY TIME: Expressed in nanoseconds (ns)
First two digits are significant figures.
Last digit specifies number of zeros to follow. 101

IMPEDANCE: Expressed in ohms. First two digits are significant. Last digit specifies number of zeros to follow. 151

PIN SEQUENCE CODE: (SEE Table II). "STANDARD" Pin sequence if omitted. A

SPECIFICATIONS:

- **Delay accuracy:** ± 5% (others on request)
- **No. taps:** 5 equally spaced taps
- **Taps accuracy:** ± 5% of tap delay
- **Impedance tolerance:** ± 10% (others on request)
- **Rise-time:** 30% of time delay
- **Withstanding voltage:** 50 Vdc min.
- **Temperature coefficient:** 100 PPM/°C.
- **Environment:** Meets or exceeds MIL-D-23859C.

TABLE I

Td (ns)[1]	Td/tap (ns)[2] (±5%)	Tr (ns) Max.	ATTENUATION (%) TYPICAL PER IMPEDANCE 50	100	200	300	500
5	1	3		5			
10	2	4	3	5	5		
15	3	5	3	5	5		
20	4	6	3	5	5	5	
25	5	7	3	5	5	5	7
30	6	10	3	5	5	5	7
40	8	13	3	5	5	5	7
50	10	15	3	5	5	7	7
60	12	20	3	5	6	7	8
75	15	25	3	5	6	7	8
80	16	26	4	5	6	7	8
100	20	30	4	5	6	7	8
110	22	32	4	5	6	7	8
125	25	40	4	5	6	7	8
150	30	50		5	8	10	10
180	36	60		7	8	10	10
200	40	70		8	10	12	12

[1] Or ± 2 ns, whichever is greater.
[2] Or ± 1 ns, whichever is greater, referenced from input.

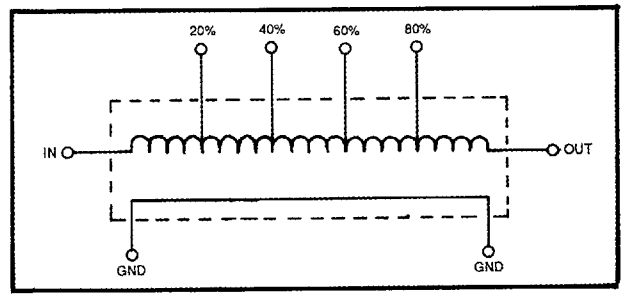


TABLE II

CODE	IN	20%	40%	60%	80%	OUT	GROUND
STANDARD	1	13	3	11	5	6	7
A	1	12	4	10	6	7	8, 14

Other pin sequences available.

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