

AWS5504-S14

GaAs IC High Power Positive Control SPDT Reflective Switch DC-2.0 GHz

Advanced Product Information
Rev. 1

FEATURES

- High Linearity (IP3 55 dBm @ 0.9 GHz)
- Low Insertion Loss (0.35 dB @ 0.9 GHz)
- +3 V to +5 V Operation
- Low DC Power Consumption
- Ultra Miniature SOT-6 Package

DESCRIPTION

The AWS5504 is a Single Pole Double Throw (SPDT) GaAs MMIC switch assembled in a SOT-6 plastic package. The AWS5504 is designed for analog and digital applications that require low insertion loss, high linearity, and small size. The switch can be controlled with positive, negative, or a combination of both voltages.

Typical applications include: transmit/receive switch, diversity switching, and antenna selection.



S14 SOT-6 6 Pin Plastic Package

ELECTRICAL SPECIFICATIONS AT 25 °C (0, +5V)

Parameter ¹	Frequency ²	Min	Тур	Max	Unit
Insertion Loss ³	DC - 0.5 GHz DC - 1.0 GHz DC - 2.0 GHz	-	0.3 0.4 1.0	0.4 0.6 1.2	dB
Isolation	DC - 0.5 GHz DC - 1.0 GHz DC - 2.0 GHz	20 15 8	23 17 10	-	dB
VSWR ⁴	DC - 1.0 GHz DC - 2.0 GHz	-	1.3:1 1.3:1	1.4:1 1.8:1	dB

OPERATING CHARACTERISTICS AT 25° C (0, +5V)

Parameter	Condition	Frequency	Min	Тур	Max	Unit
Switching Characteritics ⁵	Rise, Fall (10/90% or 90/10% RF) On, Off (50% CTL to 90%/10% RF) Video Feedthru	-	-	60 100 50	-	ns ns mV
Intermodulation Intercept Point (IP3)	For Two-tone Input Power +10 dBm	0.9 GHz	-	+55	-	dBm
Input Power for 1dB Compression		0.9 GHz	-	+38	-	dBm
Control Voltage	V _{LOW} = 0 to 0.2 V @ 20 uA Max V = +3 V @ 100 uA Max to +5 V @	@ 200 uA Max				

- 1. All measurements made in a 50 ohm system, unless other specified.
- 2. DC = 300 kHz.
- 3. Insertion loss changes by 0.003 dB/°C.
- 4. Insertion loss state.
- 5. Video feedthru measured with 1 ns rise time pulse and 500 MHz bandwidth.

 $V_S = V_{HIGH} + 0.2V$

ABSOLUTE MAXIMUM RATINGS

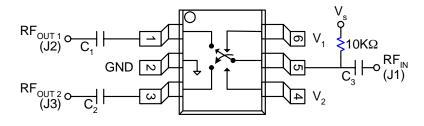
Characteristics	Value
RF Input Power	6 W Max > 900 MHz, 0/-5 V Control
Control Voltage	-0.2 V, +8 V
Operating Temperature	-40° C to +85°C
Storage Temperature	-65°C to +150°C
$\theta_{\sf JC}$	25° C/W

TRUTH TABLE

V ₁	V ₂	J ₁ - J ₂	J ₁ - J ₃
0	V_{High}	Isolation	Insertion Loss
V_{High}	0	Insertion Loss	Isolation

$$V_{High}$$
 = +3 to +5 V (V_{S} = Vhigh \pm 0.2 V)

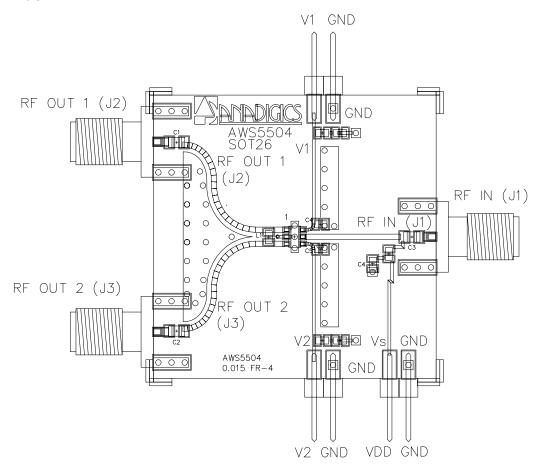
PIN OUT



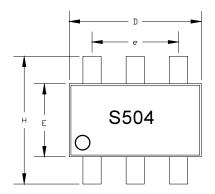
DC block capacitors $C_{1,2,3}$ must be supplied externally. $C_{1,2,3}$ = 100 pF for operation >500 MHz

Pin	Function	Description
1	RF _{OUT 1} (J2)	RF port can be used as an input or as an output.
2	GND	Ground connection. Keep as short as possible.
3	RF _{OUT 2} (J3)	RF port can be used as an input or an ouput.
4	V2	Control Voltage 2, Low 0V , High 3V to 5V
5	RF _N (J1)/Vs	RF common port and bias voltage for positive control (3V to 5V).
6	V1	Control Voltage 1, Low 0V , High 3V to 5V

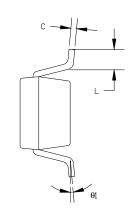
TEST CIRCUIT LAYOUT



PACKAGE OUTLINE DRAWING

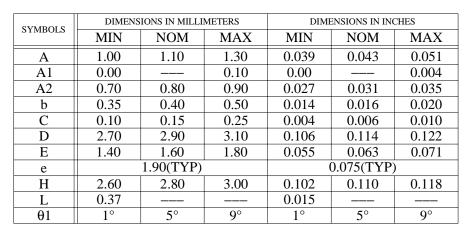






NOTES:

- 1. Package body sizes exclude mold flash and gate burrs.
- 2. Dimension L is measured in gage plane
- 3. Coplanarity: 0.1000 mm
- 4. Tolerance ± 0.1000 mm (4 mil) unless otherwise specified.



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