

# FERRITE CHIP BEADS 4 LINE ARRAY (NORMAL USAGE)

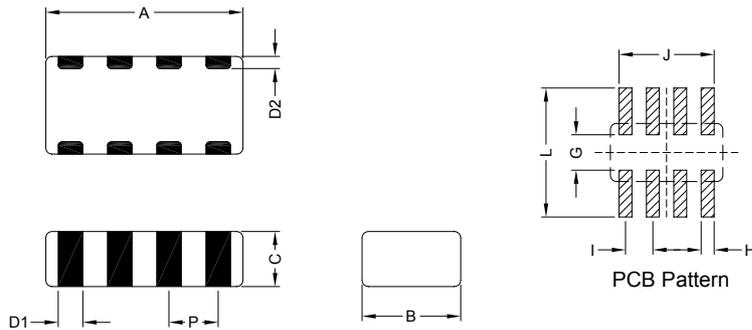
**A4K SERIES**

## 1. PART NO. EXPRESSION :

A 4 K 3 0 0 - R E - 1 0  
 (a)(b)(c) (d) (e)(f) (g)

- (a) Series code
- (b) Dimension code
- (c) Material code
- (d) Impedance code : 300 = 30Ω
- (e) R : Reel
- (f) Current code : E = 500mA
- (g) 10 : Lead Free

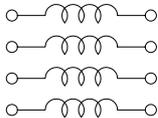
## 2. CONFIGURATION & DIMENSIONS :



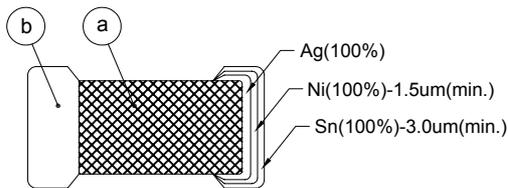
Unit:m/m

A	B	C	D1	D2	P	G	H	I	J	L
3.20±0.20	1.60±0.20	0.90±0.20	0.40±0.15	0.20±0.10	0.80±0.10	0.80 Ref.	0.40 Ref.	0.80 Ref.	2.80 Ref.	2.20~2.60

## 3. SCHEMATIC :



## 4. MATERIALS :



- (a) Body : Ferrite
- (b) Termination : Ag/Ni/Sn

## 5. GENERAL SPECIFICATION :

- a) Temp. rise : 30°C Max.
- b) Rated current : Base on temp. rise
- c) Storage temp. : -55°C to +125°C
- d) Operating temp. : -55°C to +125°C
- e) Resistance to solder heat : 260°C.10secs



**RoHS Compliant**

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### 6. ELECTRICAL CHARACTERISTICS :

Part Number	Impedance ( $\Omega$ )	Test Frequency ( MHz )	DC Resistance ( $\Omega$ ) Max.	Rated Current ( mA ) Max.
A4K300-RE-10	30 $\pm$ 25%	100	0.20	500
A4K600-RD-10	60 $\pm$ 25%	100	0.25	400
A4K121-RD-10	120 $\pm$ 25%	100	0.30	350
A4K301-RC-10	300 $\pm$ 25%	100	0.40	250
A4K601-RB-10	600 $\pm$ 25%	100	0.50	200
A4K102-RB-10	1000 $\pm$ 25%	100	0.75	150



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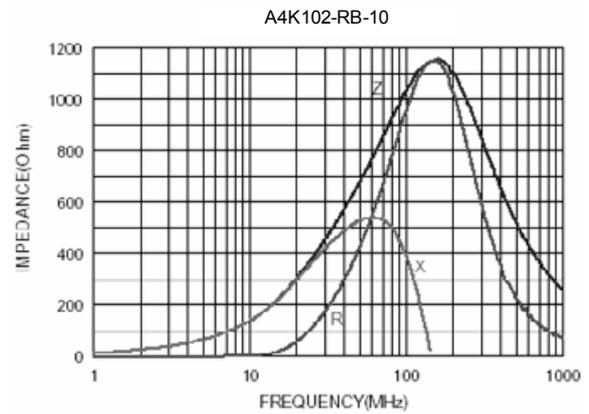
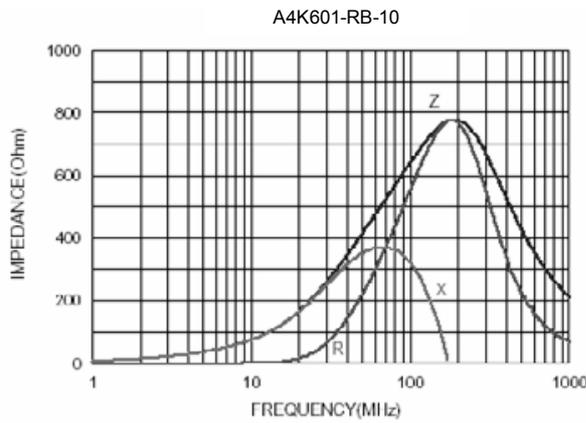
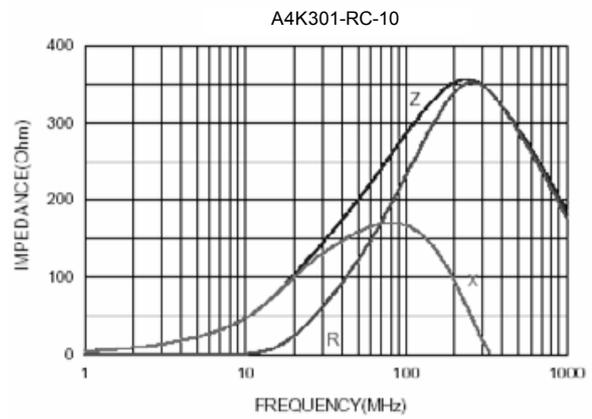
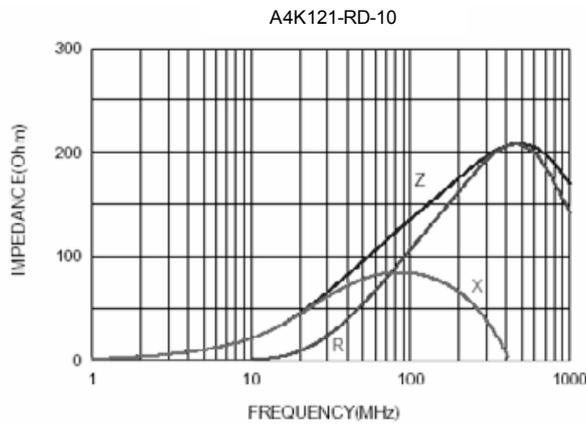
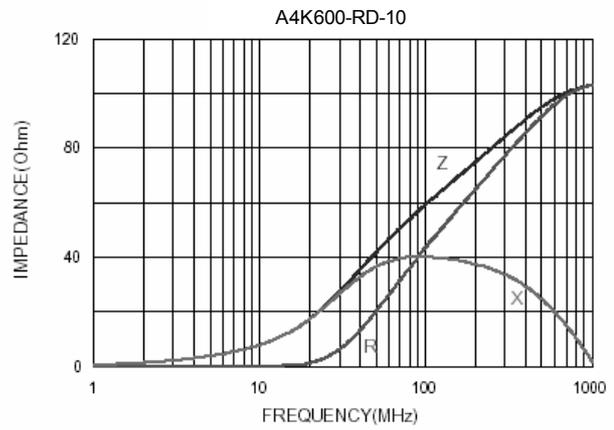
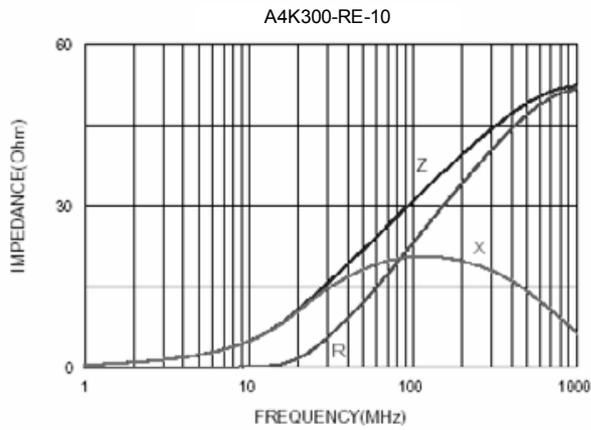
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### 7. IMPEDANCE VS. FREQUENCY CURVES :



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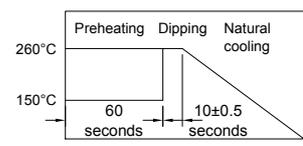
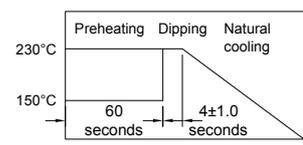
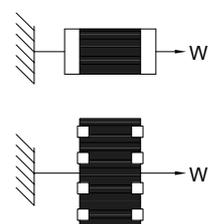
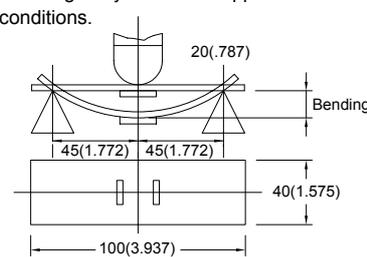


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### 8. RELIABILITY & TEST CONDITION :

ITEM	PERFORMANCE	TEST CONDITION						
Electrical Characteristics Test								
Impedance	Refer to standard electrical characteristics list	HP4291A, HP4287A+16192A						
DC Resistance		HP4338B						
Rated Current								
Temperature Rise Test	30°C max. ( $\Delta t$ )	1. Applied the allowed DC current. 2. Temperature measured by digital surface thermometer.						
Solder Heat Resistance	Appearance : No significant abnormality Impedance change : Within $\pm 30\%$ No mechanical damage Remaining terminal electrode : 70% Min.	Preheat : 150°C, 60sec. Solder : Sn-Ag3.0-Cu0.5 Solder Temperature : 260 $\pm$ 5°C Flux for lead free : rosin Dip Time : 10 $\pm$ 0.5sec. 						
Solderability	More than 90% of the terminal electrode should be covered with solder.	Preheat : 150°C, 60sec. Solder : Sn-Ag3.0-Cu0.5 Solder Temperature : 230 $\pm$ 5°C Flux for lead free : rosin Dip Time : 4 $\pm$ 1sec. 						
Terminal Strength	The terminal electrode & the dielectric must not be damaged by the forces applied on the right conditions. 	For A Series : <table border="1" data-bbox="909 1086 1252 1153"> <thead> <tr> <th>Size</th> <th>Force (Kfg)</th> <th>Time (sec)</th> </tr> </thead> <tbody> <tr> <td>4</td> <td>0.5</td> <td>&gt; 25</td> </tr> </tbody> </table>	Size	Force (Kfg)	Time (sec)	4	0.5	> 25
Size	Force (Kfg)	Time (sec)						
4	0.5	> 25						
Flexture Strength	The terminal electrode & the dielectric must not be damaged by the forces applied on the right conditions. 	Solder a chip on a test substrate, bend the substrate by 2mm (0.079in) and return.						



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### 8. RELIABILITY & TEST CONDITION :

ITEM	PERFORMANCE	TEST CONDITION									
Loading at High Temperature	Appearance : No damage. Impedance : Within $\pm 30\%$ of initial value.	Temperature : $125\pm 5^{\circ}\text{C}$ Applied Current : rated current Duration : $500\pm 12\text{hrs}$ Measured at room temperature after placing for 2 to 3hrs.									
Humidity		Humidity : 90~95% RH. Temperature : $40\pm 2^{\circ}\text{C}$ Duration : $500\pm 12\text{hrs}$ Measured at room temperature after placing for 2 to 3hrs.									
Thermal Shock	Appearance : No damage. Impedance : Within $\pm 30\%$ of initial value. <table border="1" data-bbox="470 683 874 772"> <thead> <tr> <th>Phase</th> <th>Temperature (<math>^{\circ}\text{C}</math>)</th> <th>Times (min.)</th> </tr> </thead> <tbody> <tr> <td>1</td> <td><math>-55\pm 2^{\circ}\text{C}</math></td> <td><math>30\pm 3</math></td> </tr> <tr> <td>2</td> <td><math>+125\pm 5^{\circ}\text{C}</math></td> <td><math>30\pm 3</math></td> </tr> </tbody> </table> Measured : 5 times	Phase	Temperature ( $^{\circ}\text{C}$ )	Times (min.)	1	$-55\pm 2^{\circ}\text{C}$	$30\pm 3$	2	$+125\pm 5^{\circ}\text{C}$	$30\pm 3$	For A Series : Condition for 1 cycle Step1 : $-55\pm 2^{\circ}\text{C}$ $30\pm 3$ min. Step2 : $+125\pm 5^{\circ}\text{C}$ $30\pm 3$ min. Number of cycles : 5 Measured at room temperature after placing for 2 to 3hrs.
Phase	Temperature ( $^{\circ}\text{C}$ )	Times (min.)									
1	$-55\pm 2^{\circ}\text{C}$	$30\pm 3$									
2	$+125\pm 5^{\circ}\text{C}$	$30\pm 3$									
Low temperature storage test		Temperature : $-55\pm 2^{\circ}\text{C}$ Duration : $500\pm 12\text{hrs}$ Measured at room temperature after placing for 2 to 3hrs.									



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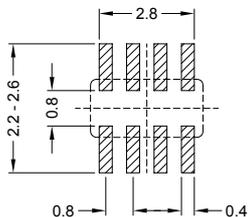


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### 9. SOLDERING AND MOUNTING :

#### 9-1. Recommended PC Board Pattern



PC board should be designed so that products are not sufficient under mechanical stress as warping the board. Products shall be positioned in the sideways direction against the mechanical stress to prevent failure.

#### 9-2. Soldering

Mildly activated rosin fluxes are preferred. The minimum amount of solder can lead to damage from the stresses caused by the difference in coefficients of expansion between solder, chip and substrate. The terminations are suitable for all wave and re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

##### 9-2.1 Lead Free Solder Re-flow :

Recommended temperature profiles for re-flow soldering in Figure 1.

##### 9-2.2 Solder Wave :

Wave soldering is perhaps the most rigorous of surface mount soldering processes due to the steep rise in temperature seen by the circuit when immersed in the molten solder wave, typical at 230°C. Due to the risk of thermal damage to products, wave soldering of large size products is discouraged. Recommended temperature profile for wave soldering is shown in Fig. 2

##### 9-2.3 Soldering Iron (Figure 3) :

Products attachment with soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

Note :

- a) Preheat circuit and products to 150°C.
- b) 350°C tip temperature (max)
- c) Never contact the ceramic with the iron tip
- d) 1.0mm tip diameter (max)
- e) Use a 20 watt soldering iron with tip diameter of 1.0mm
- f) Limit soldering time to 3 secs.

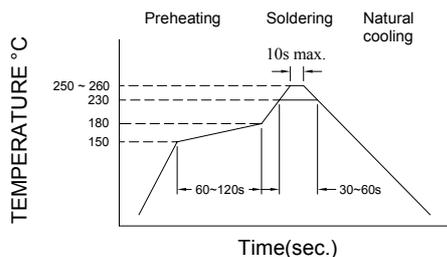


Figure 1. Re-flow Soldering

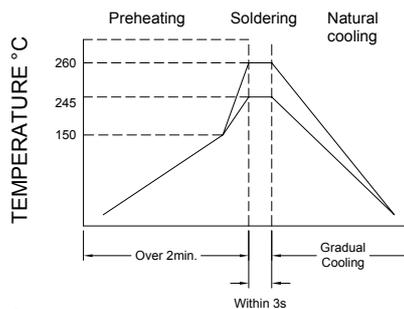


Figure 2. Wave Soldering

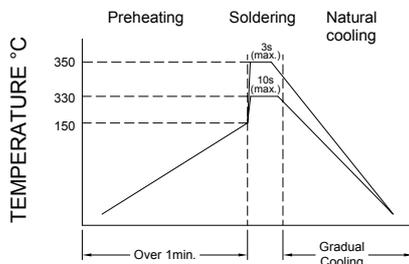


Figure 3. Hand Soldering



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### 9-3. Solder Volume

Accordingly increasing the solder volume, the mechanical stress to product is also increased. Exceeding solder volume may cause the failure of mechanical or electrical performance. Solder shall be used not to be exceed as shown in Fig. 4.

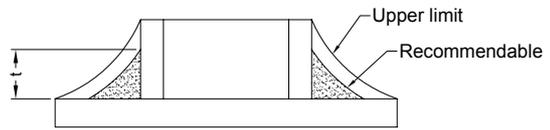


Figure 4



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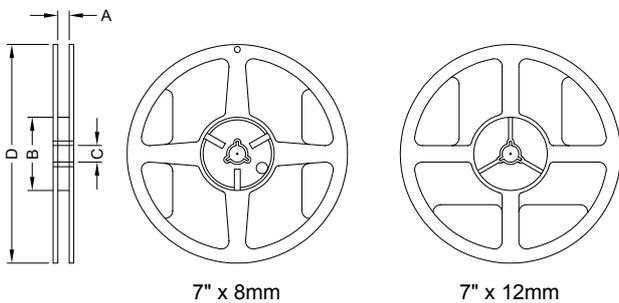
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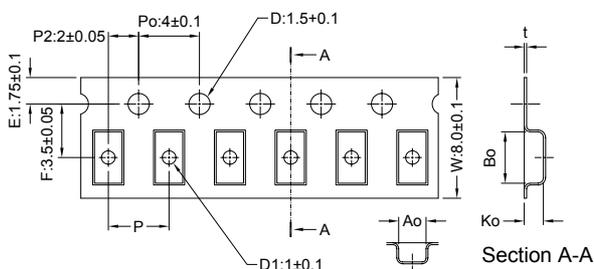
### 10. PACKAGING INFORMATION :

#### 10-1. Reel Dimension



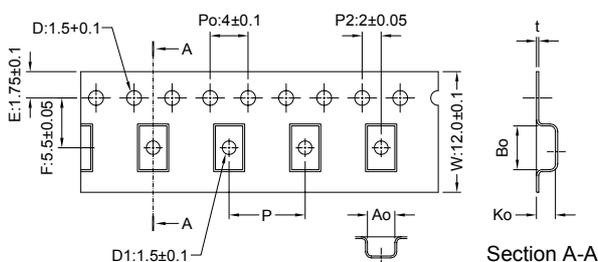
Type	A(mm)	B(mm)	C(mm)	D(mm)
7" x 8mm	9.0±0.5	60.0±2.0	13.5±0.5	178.0±2.0
7" x 12mm	13.5±0.5	60.0±2.0	13.5±0.5	178.0±2.0

#### 10-2 Tape Dimension / 8mm



Series	Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	D1(mm)
Z	1	1.12±0.05	0.67±0.05	0.54±0.05	2.0±0.1	0.23±0.05	none
	2	1.80±0.10	1.01±0.10	1.02±0.10	4.0±0.1	0.22±0.05	none
	3 (09)	2.25±0.10	1.42±0.10	1.04±0.10	4.0±0.1	0.22±0.05	1.0±0.1
	3 (12)	2.35±0.10	1.50±0.10	1.45±0.10	4.0±0.1	0.22±0.05	1.0±0.1
	4	3.50±0.10	1.88±0.10	1.27±0.10	4.0±0.1	0.22±0.05	1.0±0.1
A	4	3.40±0.10	1.77±0.10	1.04±0.10	4.0±0.1	0.22±0.05	1.0±0.1

#### 10-2.1 Tape Dimension / 12mm



Series	Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	D1(mm)
Z	6	4.95±0.1	1.93±0.1	1.93±0.1	4.0±0.1	0.24±0.05	1.5±0.1
	7	4.95±0.1	3.66±0.1	1.85±0.1	8.0±0.1	0.24±0.05	1.5±0.1
	8	6.10±0.1	5.40±0.1	2.00±0.1	8.0±0.1	0.30±0.05	1.5±0.1

#### 10-3. Packaging Quantity

Chip Size	8	7	6	5	4	3 (12)	3 (09)	2	1
Chip / Reel	1000	1000	2000	2500	3000	2000	4000	4000	10000
Inner Box	4000	4000	8000	12500	15000	10000	20000	20000	50000
Middle Box	20000	20000	40000	62500	75000	50000	100000	100000	250000
Carton	40000	40000	80000	125000	150000	100000	200000	200000	500000
Bulk (Bags)	7000	12000	20000	30000	50000	100000	150000	200000	300000



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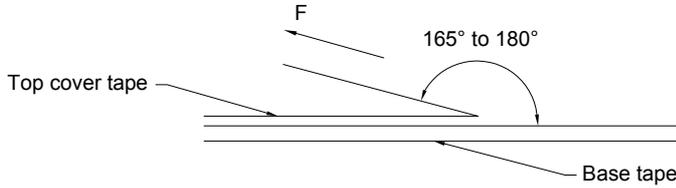
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**10-4. Tearing Off Force**



The force for tearing off cover tape is 15 to 60 grams in the arrow direction under the following conditions.

Room Temp. (°C)	Room Humidity (%)	Room atn (hPa)	Tearing Speed (mm/min)
5~35	45~85	860~1060	300

**Application Notice**

1. Storage Conditions :

To maintain the solderability of terminal electrodes :

- a) Temperature and humidity conditions : -10 ~ 40°C and 30 ~ 70% RH.
- b) Recommended products should be used within 6 months from the time of delivery.
- c) The packaging material should be kept where no chlorine or sulfur exists in the air.

2. Transportation :

- a) Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- b) The use of tweezers or vacuum pick up is strongly recommended for individual components.
- c) Bulk handling should ensure that abrasion and mechanical shock are minimized.



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