

HDSL2 TRANSFORMER

For Level One Chipset



- Matched to Level One SK70740/SK70741/SK70742 HDSL2 Chipset
- Excellent THD performance
- Maximum height .500" (12,70 mm)

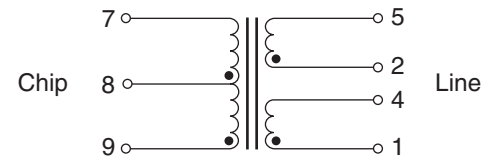
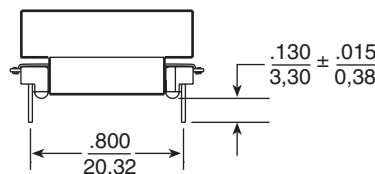
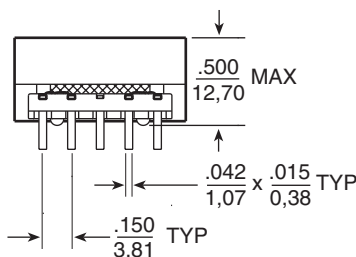
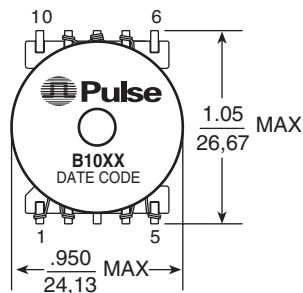
Electrical Specifications @ 25°C — Operating Temperature -40°C to +85°C

Part Number	Turns Ratio Chip-Line (±2%)	OCL @ 10 KHz, 100 mV (1-5) with (2-4) shorted	Longitudinal Balance	THD (10 KHz @ 4.95 Vrms)	DC Resistance (7-9), (4-1), (5-2)	Isolation Voltage (Vrms)
B1019	1:2	1.76 mH (±10%)	>50 dB @ 20 KHz to 600 KHz	77 dB MIN	0.60 Ω MAX	2000
B1042	1:2.3	1.76 mH (±10%)	>43 dB @ 20 KHz to 400 KHz	77 dB MIN	0.60 Ω MAX	2000

Mechanical

Schematic

B1019/B1042



Weight.....14.0 grams
Tray45/tray

Dimensions: Inches
mm
Unless otherwise specified,
all tolerances are ± .010
0,25

For More Information :

UNITED STATES (Worldwide)

12220 World Trade Drive
San Diego, CA 92128
U.S.A.
<http://www.pulseeng.com>
TEL: 858 674 8100
FAX: 858 674 8262

UNITED KINGDOM (Northern Europe)

1 & 2 Huxley Road
The Surrey Research Park
Guildford, Surrey GU2 5RE
United Kingdom
TEL: 44 1483 401700
FAX: 44 1483 401701

FRANCE (Southern Europe)

Zone Industrielle
F-39270
Orgelet
France
TEL: 33 3 84 35 04 04
FAX: 33 3 84 25 46 41

SINGAPORE (Southern Asia)

150 Kampong Ampat
#07-01/02
KA Centre
Singapore 368324
TEL: 65 287 8998
FAX: 65 280 0080

TAIWAN, R.O.C. (Northern Asia)

3F-4, No. 81, Sec. 1
HsinTai Wu Road
Hsi-Chih, Taipei Hsien
Taiwan, R.O.C.
Tel: 886 2 2698 0228
FAX: 886 2 2698 0948

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