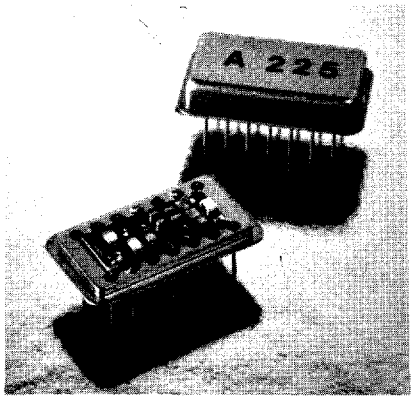
AMP TEK®

CHARGE SENSITIVE PREAMPLIFIER + SHAPING AMPLIFIER

A 225



ULTRA LOW NOISE: <280 electrons r.m.s.

Model A-225 is a high performance thin film hybrid charge sensitive preamplifier and shaping amplifier developed especially for high resolution systems employing solid state detectors, proportional counters, photomultiplier tubes and other charge producing detectors in the pulse height analysis or A/D mode of operation.

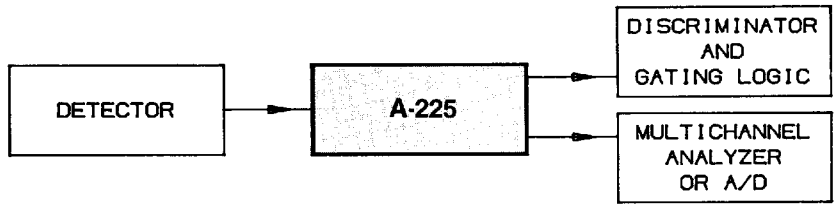
While these units were specifically designed for satellite instrumentation the following unique characteristics make them equally useful for space, laboratory and commercial applications:

- Operates from −55°C to +125°C.
- Small size (14 pin hybrid DIP) allows mounting close to the detector.
- Ultra low power (as low as 10 milliwatts)
- Wide range single supply voltage (+4 to +25 VDC)
- Pole-zero cancellation (internal)
- Two outputs available (timing pulse and shaped unipolar)
- High reliability screening
- One year warranty

DIRECT APPLICATIONS WILL BE IN:

- Aerospace
- Portable instrumentation
- Nuclear monitoring
- Particle, x-ray and gamma detection
- Imaging
- Research experiments
- Medical and nuclear electronics
- Electro-optical systems

TYPICAL APPLICATION



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SPECIFICATIONS

($V_S = 5V$, $T = 25°C$, $R_L = 10K$)

INPUT CHARACTERISTICS

SENSITIVITY:	240 mv/Mev (Si) 300 mv/Mev (Ge) 195 mv/Mev (CdTe) 206 mv/Mev (HgI_2) 5.2×10^{12} v/coulomb 0.83 μv/electron
NOISE:	2.5 Kev FWHM (Si) 2.0 Kev FWHM (Ge) 3.1 Kev FWHM (CdTe) 2.9 Kev FWHM (HgI_2) 4.5×10^{-17} coulomb rms 280 electrons rms
NOISE SLOPE:	50 ev/pf (Si) 40 ev/pf (Ge) 62 ev/pf (CdTe) 58 ev/pf (HgI_2) 9×10^{-19} coulomb/pf 5.6 electrons/pf
DYNAMIC INPUT CAPACITANCE:	>7,000 pf
POLARITY:	Negative
DETECTOR CAPACITANCE:	Up to 1,000 pf

OUTPUT CHARACTERISTICS

1) Shaping Amplifier (Pin 8)

Polarity:	Positive
Peaking Time:	2.4 μsec
Integral Nonlinearity:	<0.04% for 0-10V output pulse
Pole-Zero Compensation:	Internal
Dynamic Range:	(V_S-1.25) Volt
DC Level:	0.8V nominal

2) Timing Pulse (Pin 12)

Polarity:	Positive
Sensitivity:	44 mv/Mev (Si) 55 mv/Mev (Ge) 1.0 v/picocoulomb
Risetime:	18 nsec (unloaded)

GENERAL

Operating Voltage:	V_S = +4 to +25 VDC
Operating Current:	2.3 ma independent of V_S
Variation of Sensitivity with Supply Voltage:	<0.07%/volt, 4 to 10 volts <0.005% volt, 10 to 25 volts
Temperature:	−55° to +125°C Operational
Temperature Stability:	0.02%/°C at 25°C typical ±2% from −25°C to +75°C
Screening:	AMPTEK HIGH RELIABILITY
Package:	14 Pin Hybrid DIP (Metal)
Radiation Resistance:	10^5 Rads
Warranty:	One year
Test Board:	PC-25

AMPTEK HIGH RELIABILITY SCREENING

1. PRECAP VISUAL:	MIL-STD-883, method 2017. Low Magnification, High Magnification.
2. SEALING:	Welded, hermetic seal.
3. STAMPING:	Date code and serial number.
4. STABILIZATION BAKE:	MIL-STD-883, method 1008, Condition C. +150°C, 24 hours minimum.
5. TEMPERATURE CYCLE:	MIL-STD-883, method 1010, Condition C min. T = −65°C to +150°C. 10 minutes each extreme, 5 minutes maximum transfer time.
6. CENTRIFUGE:	MIL-STD-883, method 2001, Condition B. Yl Axis; 10,000 G's.
7. ELECTRICAL TEST:	As per specifications.
8. BURN-IN TEST:	MIL-STD-883, method 1015, 160 hours at +125°C.
9. FINE LEAK TEST:	MIL-STD-883, method 1014, Condition A. Rejection if leak rate in excess of 5×10^{-7} cc/sec.
10. GROSS LEAK TEST:	MIL-STD-883, method 1014, Condition C. Fluoro Carbon; Rejection if stream of bubbles is present.
11. ELECTRICAL TEST:	As per specifications.
12. EXTERNAL VISUAL:	MIL-STD-883, method 2009.

OPERATING NOTES

CIRCUIT LAYOUT

Due to the high sensitivity and ultra low noise of the A-225, care should be taken in circuit layout. The PC-25 Test Board may be used as an example of appropriate layout technique. In general, ground plane construction is recommended. Input and output lines should be kept well separated and in most cases shielding will be necessary. Particular attention should be paid to the detector ground connection to avoid oscillation due to feedback. The supply voltage is internally decoupled. While this is normally adequate, in some applications external bypassing may be helpful.

To facilitate noise minimization in certain applications, two separate ground connections are provided. Pin 4 is the ground connection for the input stage and is also connected to the case. Pin 5 is ground for the remainder of the circuit. In most applications Pins 4 and 5 may be connected to the same ground (preferably a ground plane under the unit,) along with Pins 2, 3, 6, 7, 9, 10 and 13.

The A-225 may also be used in applications where the input from the detector is positive by connecting Pin 5, output ground, to a negative supply voltage instead of ground. This voltage can range from 0 to −10V. A negative output pulse at Pin 8 will result from a positive input. The dynamic range in this case will be approximately 0.5 volts greater than Pin 5. For example, with Pin 5 operated at −5 volts, negative pulses of up to 5.5 volts amplitude can be obtained. This mode of operation will result in increased operating current. The A-225 is not specified in this mode and critical parameters should be verified by the user.

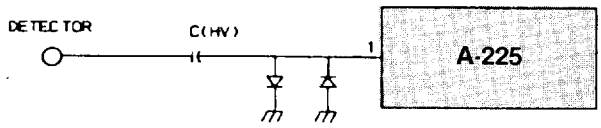
POWER SUPPLY

While specifications are given for operation at +5V, the characteristics of the A-225 are relatively unaffected by changes in supply voltage from 4 to +25 VDC. Parameters critical to a particular application should be checked at the actual operating voltage.

INPUT

In order to minimize noise, the A-225 input (Pin 1) has no internal protection. An external protection however may be added to Pin 1 by connecting two back to back diodes to ground.

TYPICAL PROTECTION CIRCUIT



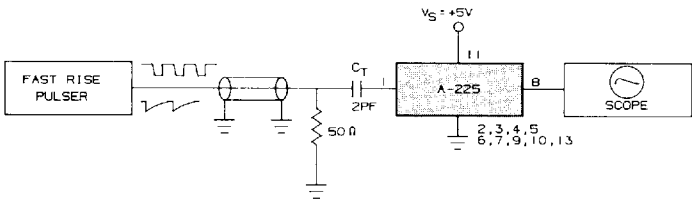
NOTE: The input protection circuit provides a limited amount of protection against transients generated in the detector and bias network. Any circuit capable of providing absolute protection would cause intolerable degradation of noise performance. For this reason, care must be exercised in the use of any preamp with high voltage detectors. Specifically, damage may result from detector breakdown, breakdown of the high voltage coupling capacitor or other component, excessively rapid rise or fall of detector bias voltage, or the addition of uncharged capacitance across the input with bias voltage applied.

In some applications, increased protection will justify an increased noise level. In this case, a resistor may be added in series with the input—normally a few hundred ohms will suffice.

TEST CIRCUIT

The A-225 can be tested with a pulser by using a small capacitor (usually 1 to 2pf) to inject a test charge into the input. The unit will respond to the negative-going edge of the test pulse, which should have a transition time of less than 20 ns. This negative going edge should be followed by a relatively flat part of the waveform so that it appears as a step function. For example, a square wave is a good test waveform. (Keep the square wave frequency low enough that the response to the positive-going edge can be ignored). Alternately, a "sawtooth" waveform or a tail pulse with long fall time ($>100\ \mu s$) may be used. Charge transfer to the input is according to $Q = C_T V$, where Q = total charge, C_T = value of test capacitor, and V = amplitude of voltage step. DO NOT connect the test pulser to the input directly or through a large capacitor ($>100pf$) as this can produce a large current in the input FET and cause irreversible damage.

TYPICAL TEST CIRCUIT



Negative going $T_r < 20ns$, $T_F > 10\ \mu s$, or square wave.

Amplitude: $V = Q/C_T = 500$ mv per picocoulomb for $C_T = 2pf$

EXAMPLE: To simulate 1 Mev in silicon detector:
1 Mev (Si) = .044 pc
500 mv/pc x .044 pc = 22mv
A 22 mv step into 2pf test capacitor
simulates 1 Mev in silicon

OUTPUTS

PIN 8

The shaping amplifier of the A-225 produces a unipolar pulse at Pin 8 suitable for high resolution, high rate pulse height analysis. This output has an AC impedance of approximately 30 ohms and will drive 1K loads as well as several feet of unterminated cable. In applications requiring highest linearity, load resistance should be greater than 5K. This output has a quiescent D.C. output level, or baseline, of approximately 0.8V. In most applications the pulse should be capacitively coupled to external circuitry.

PIN 12

The output at PIN 12 is a timing pulse with an unloaded risetime of approximately 20 ns and a falltime of $2.8\mu sec$ capable of driving a load of 500Ω. This output has approximately the same linearity characteristics as the shaped output at Pin 8. Grounding this pin could damage the unit.

COMPENSATION

The A-225 is internally optimized for detector capacitance up to approximately 50 pf. In applications with large detector capacitance and requiring short timing pulse risetime, a compensation capacitor from 0 to 250 pf may be connected from Pin 14 to ground. The exact value should be experimentally determined with the detector connected to the input. Note that this compensation will not normally be necessary if the timing pulse is not used, or its risetime is not critical.

BIPOLAR PULSE

In applications requiring a bipolar pulse, the unipolar output can be differentiated with an RC differentiator. The time constant should be approximately $1.8\mu sec$, suggested values are: $C = 1nf$, $R = 1.8K$. In most cases this pulse should be buffered in order to drive subsequent circuitry.

GENERAL

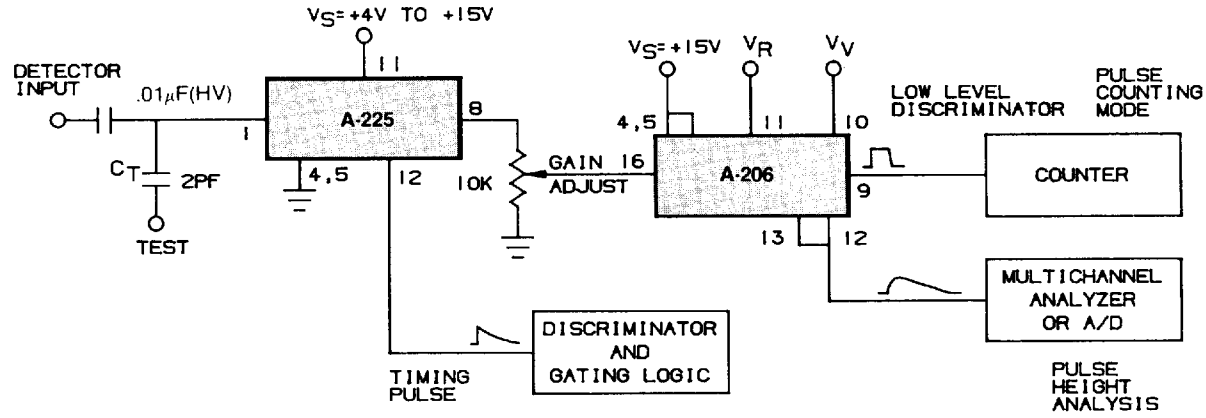
Due to its hermetic seal and small size, the A-225 is well suited to use in space or within a vacuum chamber. In such applications care should be taken to avoid electrical discharge near the input which can damage the unit and VOID WARRANTY. Use care in soldering leads — avoid overheating.

For laboratory usage, the A-225 can be mounted on its PC-25 test board and enclosed in a small metal box with appropriate cable connectors. A high voltage coupling capacitor should be added at the input as well as a small hole in the metal box in order to provide screwdriver adjustment to the gain control potentiometer.

PIN CONFIGURATION

Pin 1 Input
Pin 2, 9 Case
Pin 3, 6, 7, 10, 13 NC
Pin 4 Input GND and Case
Pin 5 Output GND
Pin 8 Output
Pin 11 $V_S = 4$ to $+25$ VDC
Pin 12 Timing Pulse
Pin 14 Compensation (see operating notes)

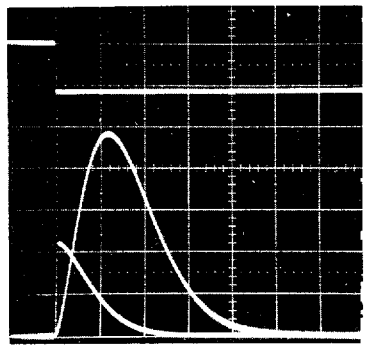
THE A-225/A-206 COMPLETE HIGH RESOLUTION SYSTEM AND TYPICAL WAVEFORMS

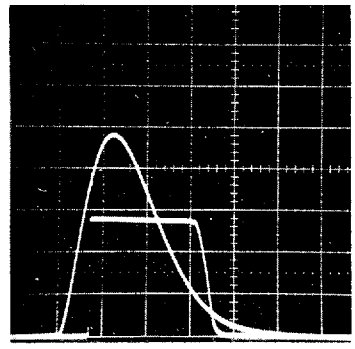


Upper:
Input through 2pf test capacitor, A-225, Pin 1.
(Vertical: 20 mv/div.: Horizontal: 2 μs/div.)

Middle:
A-225, Pin 8.
(Vertical: 50 mv/div.: Horizontal: 2μs/div.)

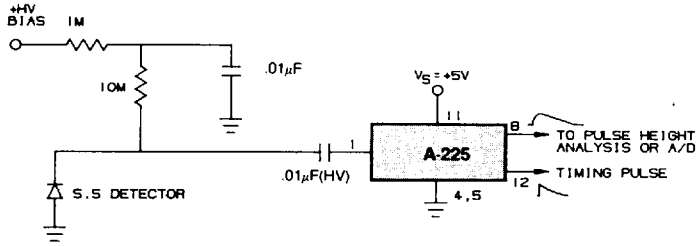
Lower:
A-225, Pin 12.
(Vertical: 20 mv/div.: Horizontal: 2μs/div.)



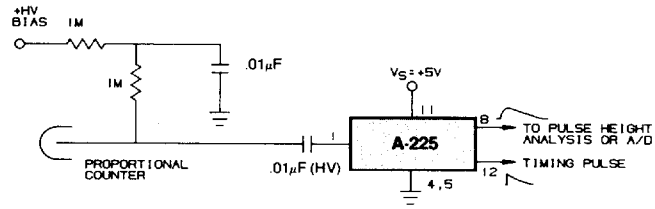


Upper:
Amplifier Output, A-206, Pin 13.
(Vertical: 500 mv/div.; Horizontal: 2μs/div.)

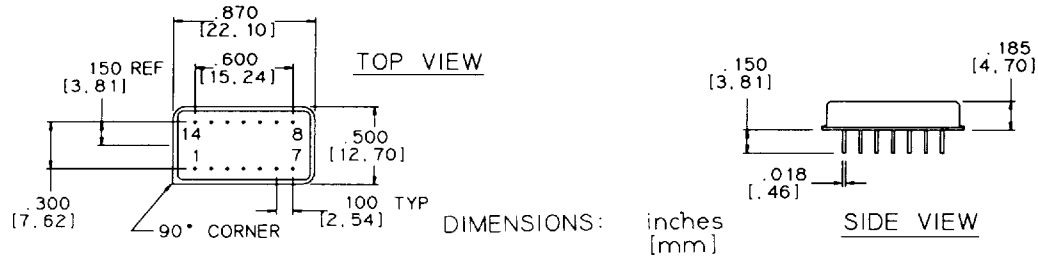
Lower:
Discriminator Output, A206, Pin 9.
(Vertical 5v/div.; Horizontal: 2μs/div.)



CONNECTION OF THE A-225 TO A SOLID STATE DETECTOR



CONNECTION OF THE A-225 TO A PROPORTIONAL COUNTER



14 PIN DUAL IN-LINE HYBRID PACKAGE

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