

7997076 SANYO SEMICONDUCTOR CORP

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LA5665

monolithic linear IC

CIRCUIT DRAWING
No.2109**MULTIFUNCTION MULTIPLE VOLTAGE
REGULATOR**

3050

The LA5665 is a multi-output voltage regulator intended for use in microcomputer-controlled tuners, receivers, preamp and the like. It delivers 2 regulated outputs of 15.5V and 5.6V and contains a reset circuit and a muting circuit.

Features

- Voltage regulator function
 - 2-output voltage regulator
 - a: 15.5V - 350mA, b: 5.6V - 100mA
 - All outputs contain an overcurrent limiter.
 - On-chip thermal shutdown circuit
- Reset function
 - Resettable when power is turned ON/OFF
 - Reset time securing function against short break of power
- An external resistor, capacitor can be used to set the reset signal pulse width.
- Muting function
 - On-chip muting circuit that detects the 15.5V voltage and reset signal to deliver the muting signal.

LA5700

monolithic linear IC

CIRCUIT DRAWING
No.2110**VOLTAGE REGULATOR FOR ELECTRONIC
TUNING SYSTEM**

3005A

Use

Suitable for voltage regulator for electronic tuning system LSI LC7200 or digital frequency displayer LSI LC7250.

LA6082D,6082S

monolithic linear IC

CIRCUIT DRAWING
No.2111**J-FET INPUT DUAL OPERATIONAL AMP**

3001A(LA6082D) 3017B(LA6082S)

The LA6082D/S are J-FET input dual OP amps having such features as high-speed response, high-input impedance, low-input bias current. They are suited for use in general-purpose control equipment, measuring equipment (very low current measurement, long-integrating circuit, sample & hold circuit, impedance converter, etc.).

Features

- High slew rate
- High input impedance
- Low input bias current
- Low input offset current
- No phase compensation required

LA6083D

monolithic linear IC

CIRCUIT DRAWING
No.2112**J-FET INPUT DUAL OPERATIONAL AMP**

3003A

The LA6083D is a J-FET input dual OP amp having such features as high-speed response, high-input impedance, low-input bias current. It is suited for use in general-purpose control equipment, measuring equipment (very low current measurement, long-integrating circuit, sample & hold circuit, impedance converter, etc.).

Features

- High slew rate
- High input impedance
- Low input bias current
- Low input offset current
- No phase compensation required