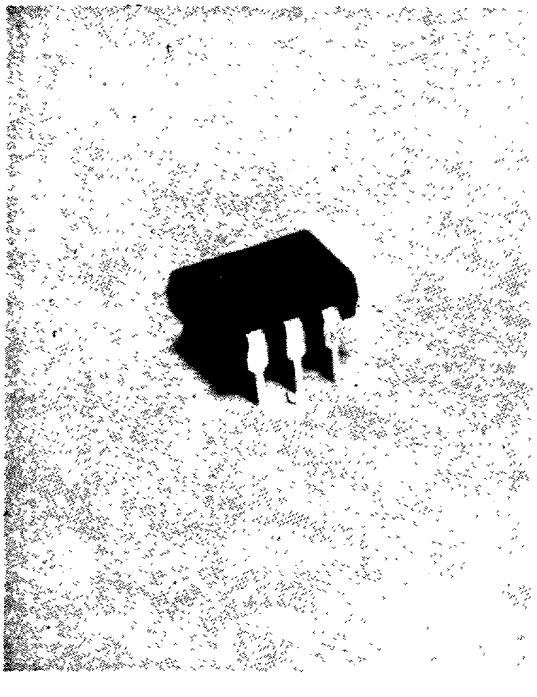




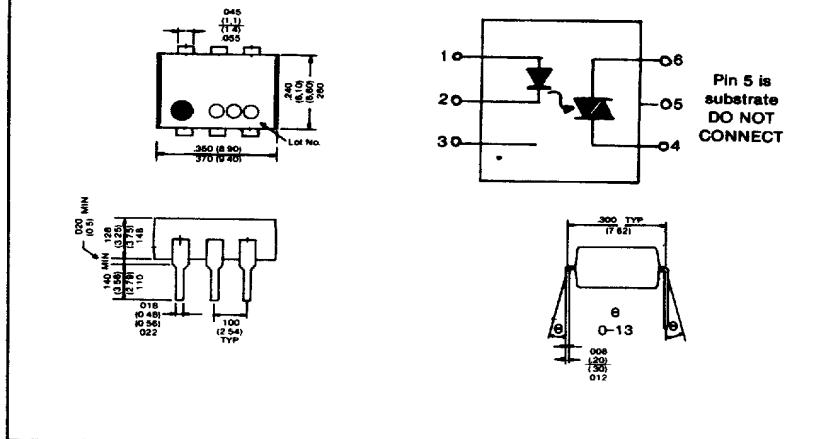
IS 3020, IS 3021

OPTICALLY COUPLED BILATERAL SWITCH

Light Activated TRIAC



PACKAGE DIMENSIONS IN INCHES (MM)



ABSOLUTE MAXIMUM RATINGS (25°C unless otherwise noted)

Storage Temperature	-55°C to + 150°C
Operating Temperature	-55°C to + 100°C
Lead Soldering Temperature (1/16 inch (1.6 mm) from case for 10 seconds)	260°C
Input-to-Output Isolation Voltage	±2500 V (Steady State)

Input Diode

Forward D.C. Current	60 mA
Reverse D.C. Voltage	3 V
Peak forward current (1 µs p.w. 800 pps)	3 A
Power Dissipation (derate linearly 1.33 mW/°C above 25°C)	100 mW

Output Photo Triac

Off-State Output Terminal Voltage	400 V
RMS Forward Current.....	100 mA
Forward Current (Peak)	1.2 A

Total Power Dissipation

(derate linearly 4.0 mW/°C above 25°C)..... 300 mW

FEATURES

- Photo-Triac Output
- 2500 V Isolation
- 400 V Peak Blocking Voltage
- Low cost dual-in-line package

DESCRIPTION

The IS3020 and IS3021 are optically coupled isolators consisting of a Gallium Arsenide infrared emitting diode and light activated silicon bilateral switch mounted in a standard 6-pin dual-in-line package.

All electrical parameters are 100% tested. Specifications are guaranteed to a cumulative 0.65% AQL.

ELECTRICAL CHARACTERISTICS (25°C unless otherwise noted)

Parameter		Min.	Typ	Max.	Units	Test Condition
Input	Forward Voltage (VF)			1.5	Volt	IF = 10 mA
	Reverse Current (IR)			10	µA	VR = 3.0 V
	Reverse Breakdown Voltage (VR)	3.0			Volt	IR = 10 µA
Output Photo-Triac	Peak Off-State Current (IDRM)			100	nA	VDRM = 400 V
	Peak Blocking Voltage	400			Volt	IDRM = 100 nA
	On-State Voltage (VTM)		2.3	3.0	Volt	ITM = 100 mA
Critical rate of rise of commutating Off-state voltage dv/dt (C)			0.15		V/µ sec	Iload = 15 mA, Vin = 30 V (RMS) (Fig. 1)
Coupled	Input Current to Trigger	IS3020		30	mA	Main Terminal Voltage = 3 V RL = 150Ω
		IS3021		15	mA	Main Terminal Voltage = 3 V Initiating Current = 10 mA
	Holding current, either direction		100		µA	(Note 1)
	Input-to-Output Isolation Voltage	2500			V(RMS)	

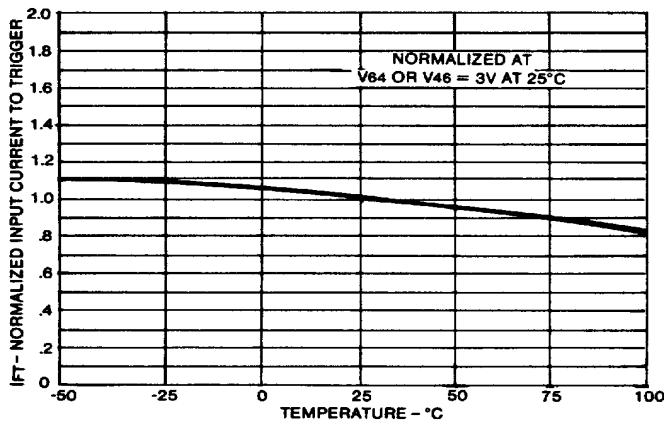
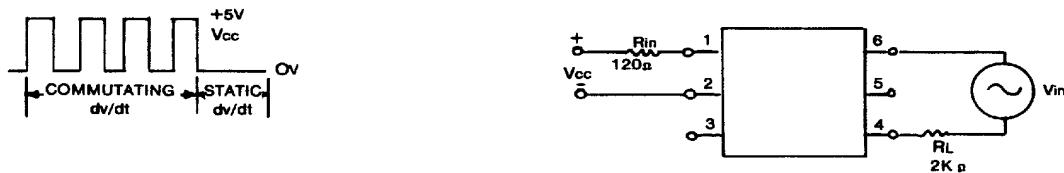
FIGURE 1
COUPLED dv/dt - TEST CIRCUIT

FIGURE 2 INPUT CURRENT TO TRIGGER VS TEMPERATURE

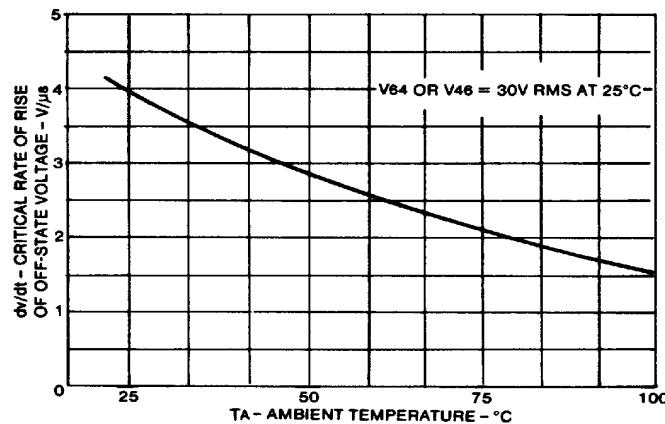


FIGURE 3 dv/dt VS TEMPERATURE

Note 1. Measured with input leads shorted together and output leads shorted together