

Low-Cost Three-Way Power Splitter/Combiner 1510 - 1660 MHz DS53-0002

Features

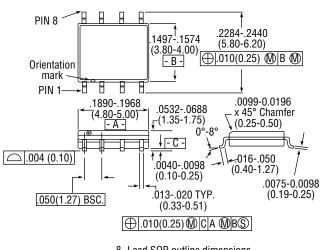
- Small Size and Low Profile •
- Industry Standard SOIC-8 SMT Plastic Package
- Superior Repeatability •
- Excellent Insertion Loss 0.6 dB Typical
- Low Cost
- Frequency coverage for GPS and LEOS Programs

Description

M/A-COM's DS53-0002 is an IC based monolithic power splitter in a low cost SOIC-8 plastic package. This 3-way power splitter is ideally suited for applications where small size, low profile and low cost, without sacrificing performance are required. Typical applications include base stations, portables and PCMCIA cards for cellular applications. Available in tape and reel.

The DS53-0002 is fabricated using a passive integrated circuit process. The process features full chip passivation for increased performance and reliability.

SO-8



8- Lead SOP outline dimensions Narrow body .150 (All dimensions per JEDEC No. MS-012-AA, Issue C) Dimensions in () are in mm. Unless Otherwise Noted: .xxx = \pm 0.010 (.xx = \pm 0.25) .xx = \pm 0.02 (.x = \pm 0.5)

Ordering Information

Part Number	Description
DS53-0002	SOIC 8-Lead Plastic Package
DS53-0002-TR	Forward Tape & Reel*
DS53-0002-RTR	Reverse Tape & Reel*

*If specific reel size is required, consult factory for part number assignment.

Typical Electrical Specifications¹, $T_{A} = +25^{\circ}C$

Parameter	Unit	Min.	Тур.	Max.
Insertion Loss above 4.78 dB	dB	—	0.6	0.7
Isolation	dB	15	18	—
VSWR	—	—	1.4:1	1.6:1
Amplitude Balance	dB	—	0.4	0.6
Phase Balance	0	—	6°	10°

1. All specifications apply with a 50 ohm source and load impedance.

The Preliminary Specifications Data Sheet Contains Typical Electrical Specifications Which May Change Prior to Final Introduction.

M/A-COM, Inc. North America:

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1

V1.00

Absolute Maximum Ratings¹

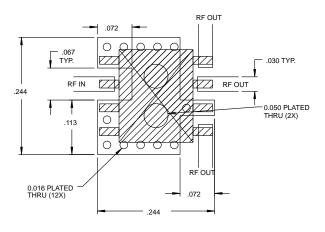
Parameter	Absolute Maximum		
Input Power ²	1 W CW		
Operating Temperature	-40°C to +85°C		
Storage Temperature	-65°C to +150°C		

1. Exceeding these limits may cause permanent damage.

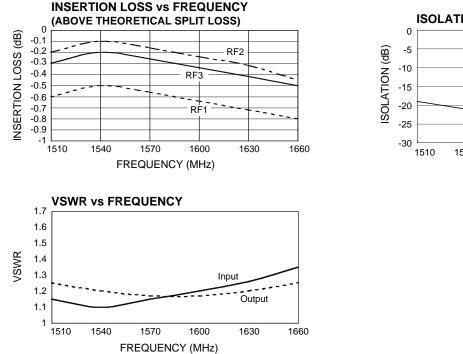
2. With internal load dissipation of 0.125 W Maximum.

Recommended PCB Configuration

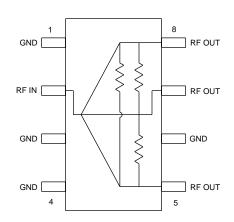
(Dimensions in Inches)



Typical Performance @ +25°C



Functional Diagram



		ISOL	ATIC	N vs	FREG	QUENCY		
	0							
ISOLATION (dB)	-5							
	-10							
	-15							
	-20		<u> </u>					
	-25							
	-30							
		1510	154	C	1570	1600	1630	1660
				FR	EQUE	NCY (MHz)		

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2

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