Active Delay Lines

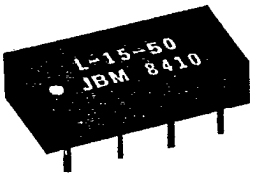
T-47-13

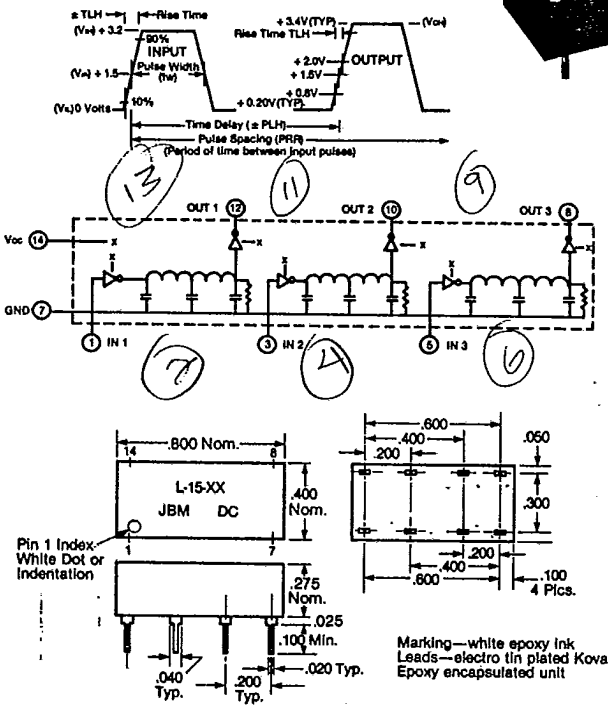
NEW
Low Power/High Drive

Delay Range 5-200 nsec

STYLE L-15 (14-pin DIP)

untapped, multi delay





Marking—white epoxy ink
Leads—electro tin plated Kovar
Epoxy encapsulated unit

FEATURES

- Low power
- Fast rise time
- Low supply current
- High drive capability
- Excellent noise margin

Delay Line Characteristics

Input Characteristics:

Vih logic "1" input voltage	2.0V min.
Vil logic "0" input voltage	0.8V max.
Iih logic "1" input current	.20μA max.
Iil logic "0" input current	−0.6mA max.

Output Characteristics:

Voh logic "1" output voltage	2.7V min.
Vol logic "0" output voltage	0.5V max.
Ioh logic "1" output current	−1.0mA max.
Iol logic "0" output current	20mA max.

Test Conditions:

Ambient temperature	25C
Supply voltage (Vcc)	5.0 ± .05Vdc
Input pulse amplitude	3.2V nom.
Input pulse width (tw)	2 x TD min.
Input pulse duty cycle	< 50%
Input pulse rise time	3 nsec (10% − 90%)
Output loads	500 ohms in parallel with 50pF

Environment:

Operating temperature	0C to +70C
Storage temperature	−55C to +125C
Operating supply voltage	4.75 to 5.25Vdc

DELAY SPECIFICATIONS (all times in nanoseconds):

JBM Part #	Line 1 In 1—out 12	Line 2 In 3—out 10	Line 3 In 5—out 8
L-15-050	5 ± 2	5 ± 2	5 ± 2
L-15-051	6 ± 2	6 ± 2	6 ± 2
L-15-052	7 ± 2	7 ± 2	7 ± 2
L-15-053	8 ± 2	8 ± 2	8 ± 2
L-15-054	9 ± 2	9 ± 2	9 ± 2
L-15-055	10 ± 2	10 ± 2	10 ± 2
L-15-056	15 ± 2	15 ± 2	15 ± 2
L-15-057	20 ± 2	20 ± 2	20 ± 2
L-15-058	30 ± 2	30 ± 2	30 ± 2
L-15-059	40 ± 3	40 ± 3	40 ± 3
L-15-060	50 ± 3	50 ± 3	50 ± 3
L-15-061	60 ± 3	60 ± 3	60 ± 3
L-15-062	70 ± 3.5	70 ± 3.5	70 ± 3.5
L-15-063	75 ± 4	75 ± 4	75 ± 4
L-15-064	80 ± 4	80 ± 4	80 ± 4
L-15-065	90 ± 4.5	90 ± 4.5	90 ± 4.5
L-15-066	100 ± 5	100 ± 5	100 ± 5
L-15-067	125 ± 6.5	125 ± 6.5	125 ± 6.5
L-15-068	150 ± 7.5	150 ± 7.5	150 ± 7.5
L-15-069	175 ± 9	175 ± 9	175 ± 9
L-15-070	200 ± 10	200 ± 10	200 ± 10

Delays may change 2% or 1ns for a respective 5% increase or decrease in supply voltage.

Consult factory for other delays, tolerances, and logic families.