

## GENERAL DESCRIPTION

This Trench MOSFET has better characteristics, such as fast switching time, low on resistance, low gate charge and excellent avalanche characteristics. It is mainly suitable for DC/DC Converter and Battery pack..

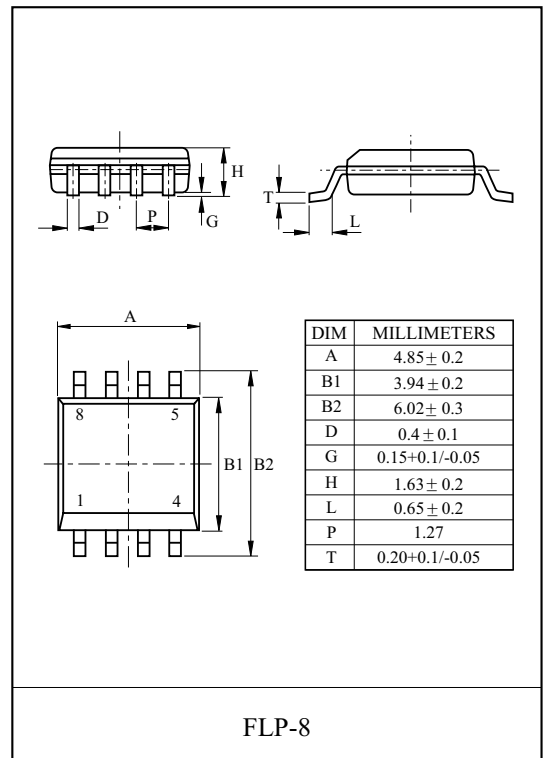
## FEATURES

- $V_{DSS}=30V$ ,  $I_D=16A$ .
- Drain to Source On Resistance.
  - $R_{DS(ON)}=6.3m$  (Max.) @  $V_{GS}=10V$
  - $R_{DS(ON)}=10.7m$  (Max.) @  $V_{GS}=4.5V$

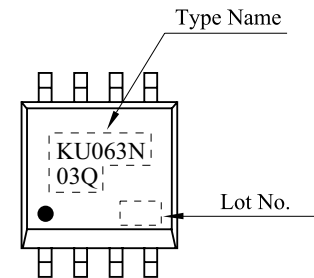
## MOSFET Maximum Ratings (Ta=25 Unless otherwise noted)

CHARACTERISTIC		SYMBOL	RATING	UNIT
Drain to Source Voltage		$V_{DSS}$	30	V
Gate to Source Voltage		$V_{GSS}$	$\pm 20$	V
Drain Current	DC@Ta=25 (Note 1)	$I_D$	16	A
	Pulsed	$I_{DP}$	64	A
Drain Power Dissipation	@Ta=25 (Note 1)	$P_D$	2.5	W
Maximum Junction Temperature		$T_j$	150	
Storage Temperature Range		$T_{stg}$	-55~150	
Thermal Resistance, Junction to Ambient (Note 1)		$R_{thJA}$	50	/W

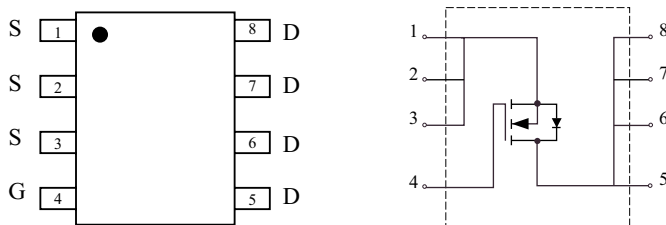
Note1) Surface Mounted on 1 × 1 FR4 Board, t 10sec.



## Marking



## PIN CONNECTION (TOP VIEW)



## ELECTRICAL CHARACTERISTICS (Ta=25 ) UNLESS OTHERWISE NOTED

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT		
<b>Static</b>								
Drain to Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250 μA	30	-	-	V		
Drain Cut-off Current	I <sub>DSS</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =30V	-	-	1	μA		
Gate to Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = ± 20V, V <sub>DS</sub> =0V	-	-	± 100	nA		
Gate to Source Threshold Voltage	V <sub>th</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250 μA	1.0	-	3.0	V		
Drain to Source On Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =16A (Note2)	-	5.3	6.3	m		
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =13A (Note2)	-	8.9	10.7			
Forward Transconductance	g <sub>fs</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =16A (Note2)	-	52	-	S		
<b>Dynamic</b>								
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, f=1MHz (Note2)	-	1751	-	pF		
Output Capacitance	C <sub>oss</sub>		-	350	-			
Reverse Transfer Capacitance	C <sub>rss</sub>		-	253	-			
Gate Resistance	R <sub>g</sub>	f=1MHz	-	2.8	-			
Total Gate Charge	V <sub>GS</sub> =10V	V <sub>DS</sub> =15V, V <sub>GS</sub> =10V, I <sub>D</sub> =16A (Note2)	-	39.7	-	nC		
	V <sub>GS</sub> =4.5V		-	20.1	-			
Gate to Source Charge	Q <sub>gs</sub>		-	6.8	-			
Gate to Drain Charge	Q <sub>gd</sub>		-	8.2	-			
Turn-On Delay Time	t <sub>d(on)</sub>		V <sub>DS</sub> =15V, V <sub>GS</sub> =10V I <sub>D</sub> =16A, R <sub>G</sub> =1.6 (Note2)	-	10.1		-	ns
Turn-On Rise Time	t <sub>r</sub>			-	10.5		-	
Turn-Off Delay Time	t <sub>d(off)</sub>	-		31.2	-			
Turn-Off Fall Time	t <sub>f</sub>	-		11.0	-			
<b>Source to Drain Diode Ratings</b>								
Source to Drain Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =16A (Note2)	-	0.8	1.2	V		
Reverse Recovery Time	t <sub>rr</sub>	I <sub>S</sub> =16A, dI/dt=100A/μs	-	22.5	-	ns		
Reverse Recovered Charge	Q <sub>rr</sub>	I <sub>S</sub> =16A, dI/dt=100A/μs	-	9.5	-	nC		
Note2) Pulse Test : Pulse Width 300μs, Duty Cycle 2%								

Fig1.  $I_D - V_{DS}$

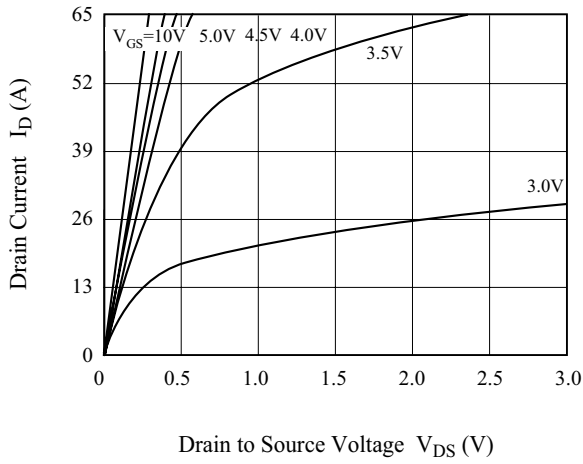


Fig2.  $R_{DS(on)} - I_D$

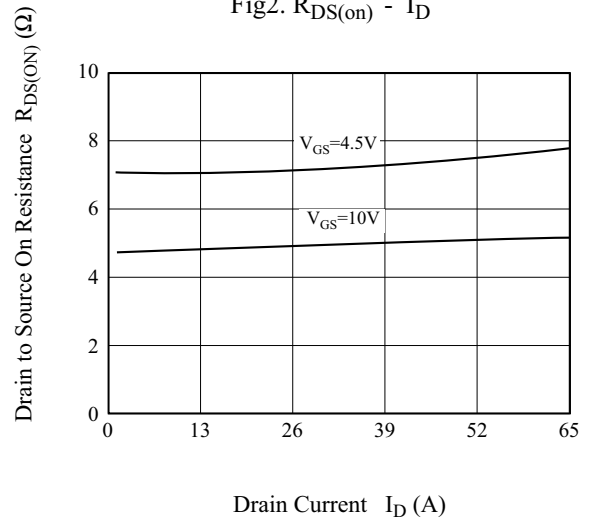


Fig3.  $I_D - V_{GS}$

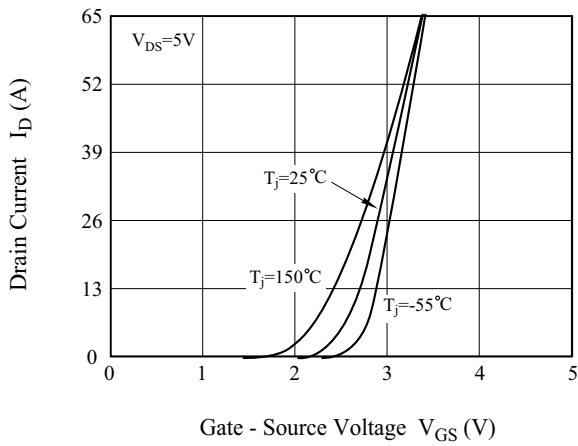


Fig4.  $R_{DS(ON)} - T_j$

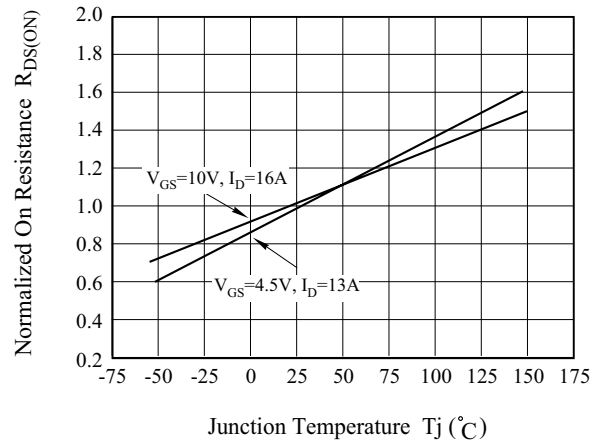


Fig5.  $V_{th} - T_j$

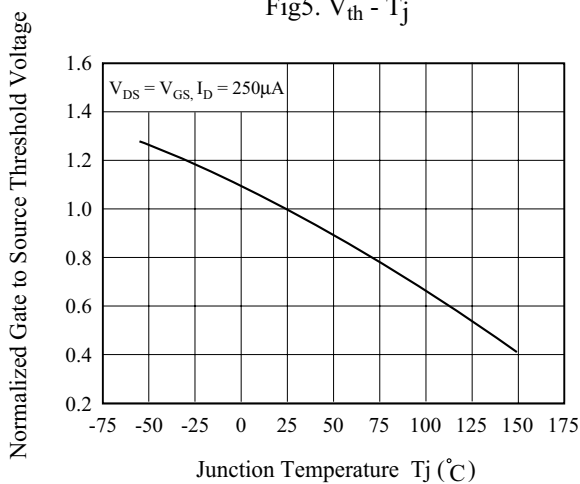


Fig6.  $I_S - V_{SD}$

