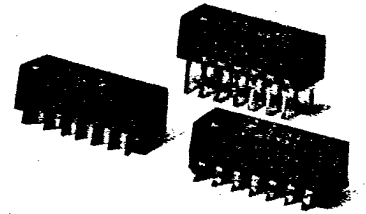


SERIES 625 10-TAPS 14 PIN H-CMOS DIGITAL DELAY MODULES-REDUCED SIZE

- 14 Pin DIP and Surface Mount Packages • H-Cmos Logic • 10 Equally Spaced Taps

Specifications:

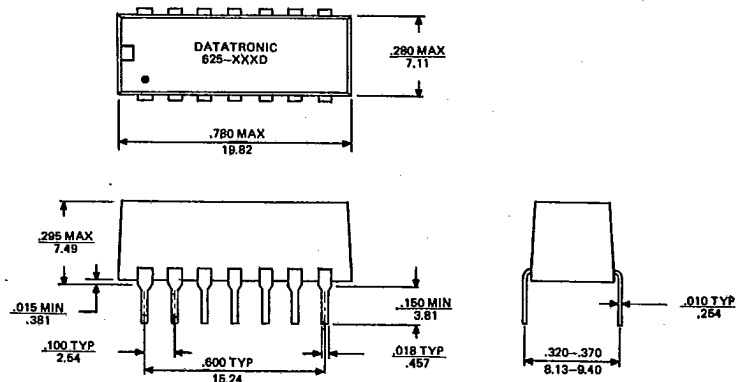
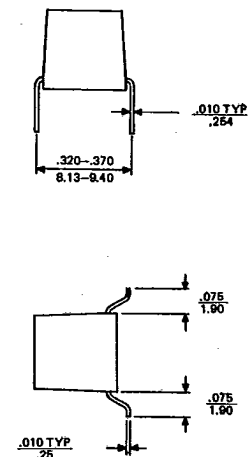
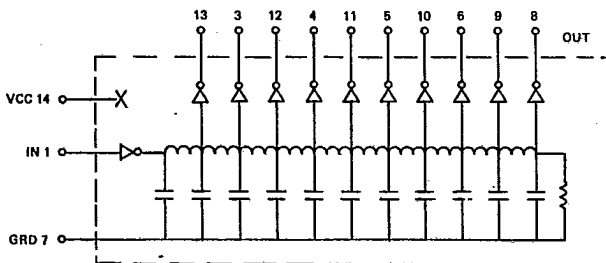
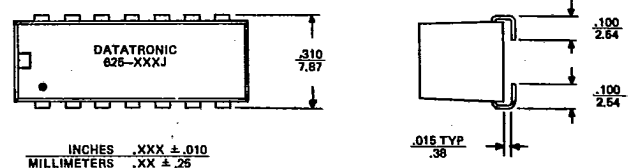
- Supply Voltage : 4.75 to 5.25VDC
- Logic 1 Input Voltage : 3.2V
- Logic 0 Input Voltage : 0.9V
- Logic 1 Output Voltage : 4.5V
- Logic 0 Output Voltage : 0.1V
- Input Current : 1 μ A Max
- Supply Current : ICCH = 5mA, ICCL = 20 μ A
- Fan-Out : 10 LSTTL Loads Min
- Rise-Time : 8NS TYP. (measured from 0.8V to 2.0V)
- Operating Temp Range : 0°C to 70°C
- Temperature Coefficient : 300 PPM/°C



Electrical Specifications at 25°C (Measured with no Loads on Taps)

Part Number			Total Delay Nanosecond (1)	Tap To Tap Delay NSEC (1)
Type D	Type W	Type J		
625-10D*	625-10W*	625-10J*	9 $\pm$ 1.0	1 $\pm$ 0.4
625-15D*	625-15W*	625-15J*	13.5 $\pm$ 1.2	1.5 $\pm$ 0.5
625-20D*	625-20W*	625-20J*	18 $\pm$ 1.5	2 $\pm$ 0.5
625-25D*	625-25W*	625-25J*	22.5 $\pm$ 2	2.5 $\pm$ 0.7
625-50D*	625-50W*	625-50J*	50 $\pm$ 2.5	5 $\pm$ 1.5
625-100D*	625-100W*	625-100J*	100 $\pm$ 5	10 $\pm$ 2
625-150D*	625-150W*	625-150J*	150 $\pm$ 7.5	15 $\pm$ 2
625-200D	625-200W	625-200J	200 $\pm$ 10	20 $\pm$ 2
625-250D	625-250W	625-250J	250 $\pm$ 15.5	25 $\pm$ 2.5
625-300D	625-300W	625-300J	300 $\pm$ 15	30 $\pm$ 3
625-400D	625-400W	625-400J	400 $\pm$ 20	40 $\pm$ 4
625-500D	625-500W	625-500J	500 $\pm$ 25	60 $\pm$ 5

Note: (1) Measured at 1.5v level leading edge.
*Time delay referenced to 1st Tap.

TYPE D**TYPE W****TYPE J****SCHEMATIC**