

SERIES 615 14 PIN TWO OUTPUTS FAST LOGIC DELAY MODULES

- 14 Pin DIP Auto-Insertable • TTL and DTL Compatible • Two Outputs

Specifications:

- Supply Voltage : 4.75 to 5.25VDC
- Logic 1 Input Current : 100 μ A Max
- Logic 0 Input Current : -1.6mA Max
- Logic 1 Volt Out : 2.5V Min
- Logic 0 Volt Out : 0.5V Max
- Logic 1 Fan-Out : 25 Max
- Logic 0 Fan-Out : 12.5 Max
- Output Rise Time⁽²⁾ : 2NSEC TYP.
- Operating Temp Range : 0°C to 70°C
- Temperature Coefficient : 100 PPM/°C

Input Test Conditions:

- Input Pulse Voltage : 3.2V
- Input Rise Time : ≤ 2 NS
- Pulse Width : 40% of Total Delay
- Supply Current ICCL* : 54mA
- ICCH* : 12mA

Electrical Specifications at 25°C (Measured with no Loads on Taps)

Part Number	Total Delay Nanosecond (1)	Tap Delay NSEC (1)
615-10	10 $\pm$ 2	5
615-12	12 $\pm$ 2	6
615-16	16 $\pm$ 2	8
615-25	25 $\pm$ 2	12.5
615-30	30 $\pm$ 2	15
615-35	35 $\pm$ 2	17.5
615-40	40 $\pm$ 2	20
615-45	45 $\pm$ 2.2	22.5
615-50	50 $\pm$ 2.5	25
615-60	60 $\pm$ 3	30
615-75	75 $\pm$ 3.5	37.5
615-100	100 $\pm$ 5.0	50
615-125	125 $\pm$ 5.5	62.5
615-150	150 $\pm$ 6.0	75
615-175	175 $\pm$ 8.7	87.5
615-200	200 $\pm$ 10	100
615-250	250 $\pm$ 12.5	125

Note: (1) Measured at 1.5v level leading edge. Tot. $\pm 5\%$ or ± 2 NS, whichever is greater.
(2) Measured from 0.75v to 2.4v.

