

# JT9648-AS

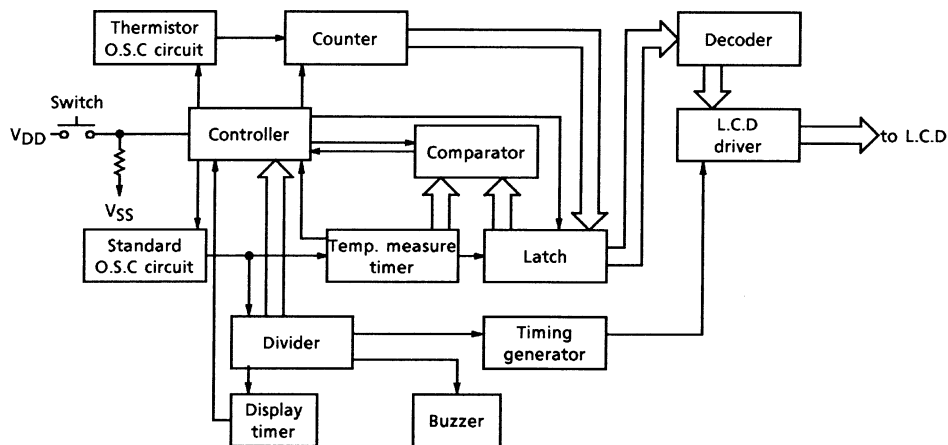
## Liquid Crystal Display Body Thermometer LSI

The JT9648-AS is a single chip CMOS LSI for electrical body thermometer capable of directly driving a 4-1/2 digit liquid crystal using a thermistor as a body heat sensor.

### Features

- Display temperature range: 32.00°C to 43.00°C (90.00 to 110.00°F)
- Resolution: 0.01°C (0.01°F)
- Accuracy:  $\pm 0.1^\circ\text{C}$  (35.0°C~38.0°C,  $V_{SS1} = -1.55\text{ V} \sim -1.40\text{ V}$ ,  $T_a = 25^\circ\text{C}$ )
- Max value holding function
- Memorize last value
- Voltage drop detecting function
- Measured temperature stable detecting function
- Power ON/OFF operation by a single switch
- Automatic power OFF function (approx. 16 min)
- Full display function
- A single 1.5 V silver oxide battery
- 4-1/2 digit, 1/3 duty, 1/2 bias liquid crystal direct driving
- Buzzer output (time elapsed or stable detection reporting)
- °C/°F measurements selectable by bonding option

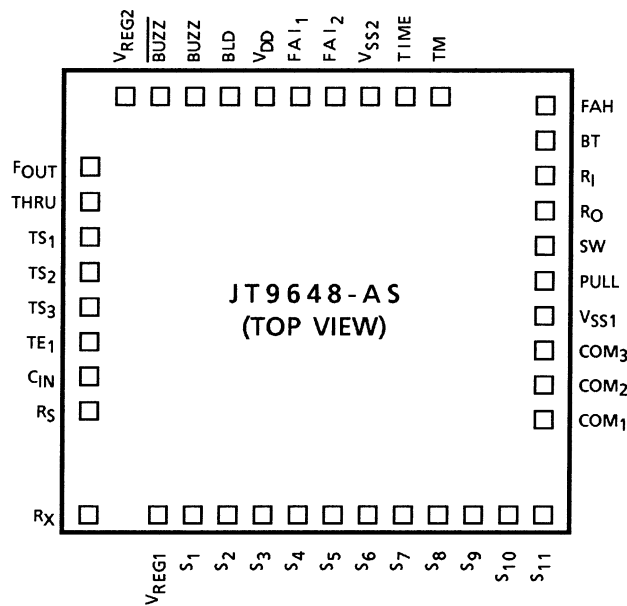
### Block Diagram



## Pin Description

Pin Name	Symbol	No. of Pins
Power Pin	V <sub>DD</sub> , V <sub>SS1</sub> , V <sub>SS2</sub> , V <sub>REG1</sub> , V <sub>REG2</sub> , PULL	6
Oscillator Pin	R <sub>S</sub> , R <sub>X</sub> , C <sub>IN</sub> , R <sub>I</sub> , R <sub>O</sub>	5
SW Pin	SW	1
Function selectional Pin	FAH, TM, BT, TIME	4
Test Pin	TE <sub>1</sub> , TS <sub>1</sub> , TS <sub>2</sub> , TS <sub>3</sub> , THRU, F <sub>OUT</sub>	6
Display Pin	COM <sub>1</sub> , COM <sub>2</sub> , COM <sub>3</sub> , SEG <sub>1-11</sub>	14
Buzzer Pin	BUZZ, $\overline{\text{BUZZ}}$	2
Doubler Pin	FAI <sub>1</sub> , FAI <sub>2</sub>	2
BLD Pin	BLD	1

## Pad Layout



Chip size : 2.72 × 2.41 (mm)  
 Chip thickness : 440 ± 30 (μm)

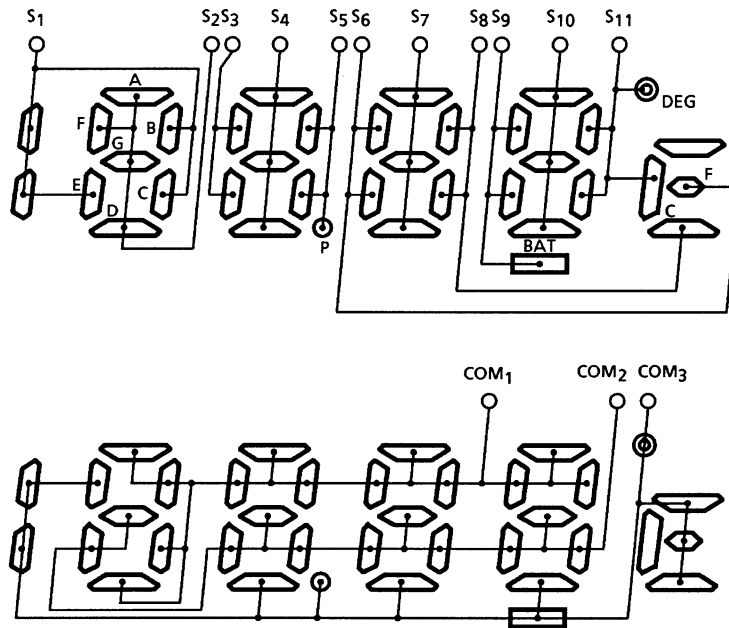
## Pad Location Table

( $\mu\text{m}$ )

Pin Name	X Point	Y Point	Pin Name	X Point	Y Point
R <sub>X</sub>	-1222	-1069	R <sub>1</sub>	1222	636
R <sub>S</sub>	-1222	-578	R <sub>0</sub>	1222	456
C <sub>IN</sub>	-1222	-398	SW	1222	276
TE <sub>1</sub>	-1222	-218	PULL	1222	96
TS <sub>3</sub>	-1222	-38	V <sub>SS1</sub>	1222	-84
TS <sub>2</sub>	-1222	142	COM <sub>3</sub>	1222	-264
TS <sub>1</sub>	-1222	322	COM <sub>2</sub>	1222	-444
THRU	-1222	502	COM <sub>1</sub>	1222	-624
F <sub>OUT</sub>	-1222	682	S <sub>11</sub>	1150	-1069
V <sub>REG2</sub>	-819	1069	S <sub>10</sub>	970	-1069
$\overline{\text{BUZZ}}$	-639	1069	S <sub>9</sub>	790	-1069
BUZZ	-459	1069	S <sub>8</sub>	610	-1069
BLD	-279	1069	S <sub>7</sub>	430	-1069
V <sub>DD</sub>	-99	1069	S <sub>6</sub>	250	-1069
FAI <sub>1</sub>	81	1069	S <sub>5</sub>	70	-1069
FAI <sub>2</sub>	261	1069	S <sub>4</sub>	-110	-1069
V <sub>SS2</sub>	441	1069	S <sub>3</sub>	-290	-1069
TIME	621	1069	S <sub>2</sub>	-470	-1069
TM	801	1069	S <sub>1</sub>	-650	-1069
FAH	1222	996	V <sub>REG1</sub>	-830	-1069
BT	1222	816			

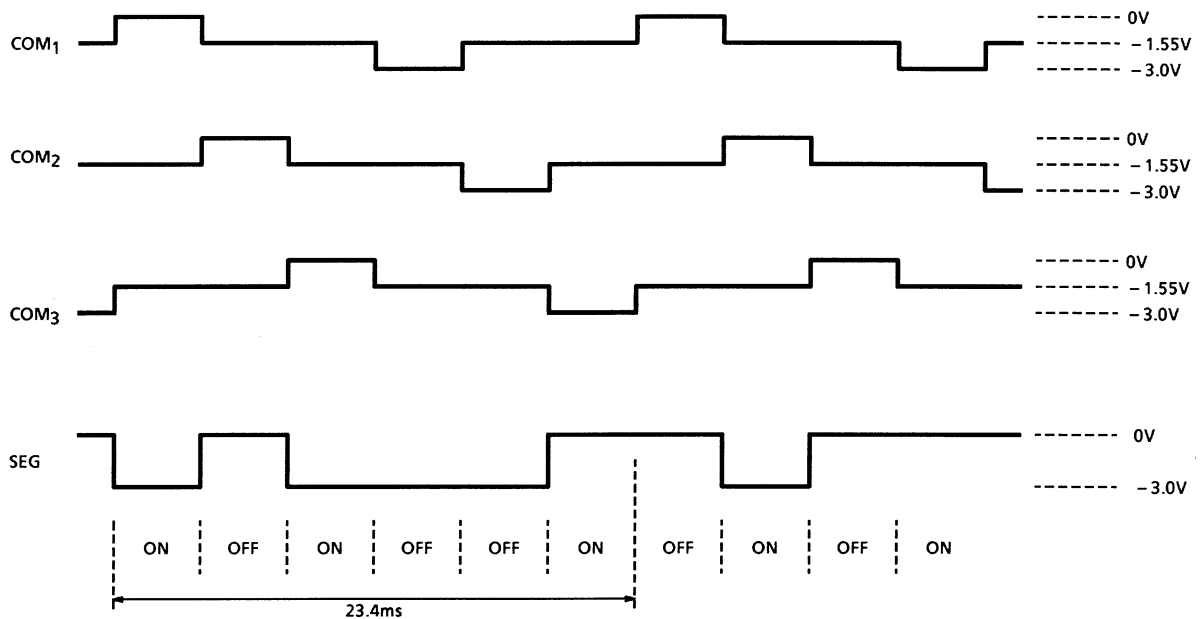
## Functional Specifications

### 1. Display Configuration



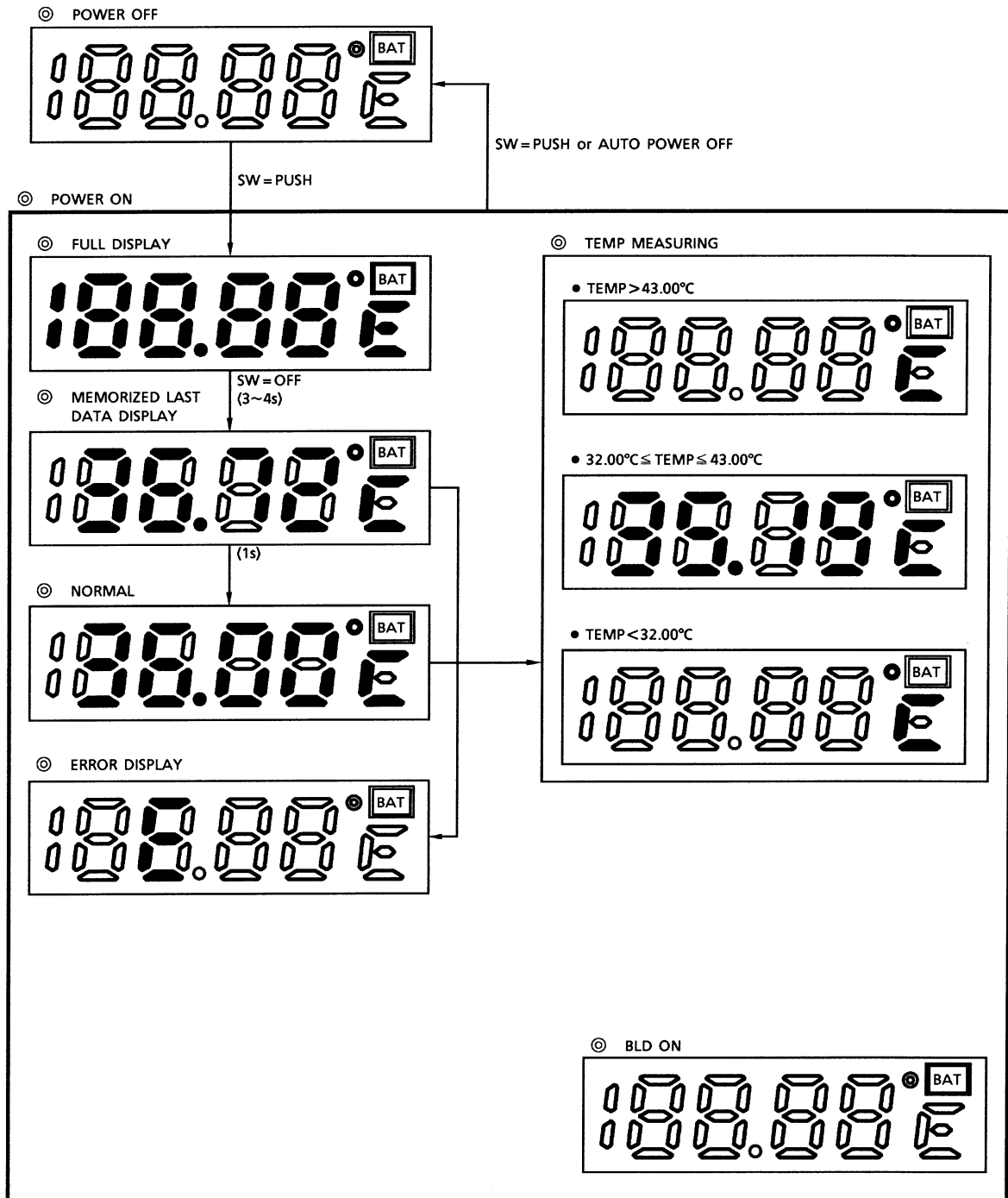
Pad Name	COM <sub>1</sub>	COM <sub>2</sub>	COM <sub>3</sub>	Pad Name	COM <sub>1</sub>	COM <sub>2</sub>	COM <sub>3</sub>
S <sub>1</sub>	2B 2C	2E	1B 1C	S <sub>7</sub>	4A	4G	4D
S <sub>2</sub>	2A 2D	2G	2F	S <sub>8</sub>	4B	4C	C
S <sub>3</sub>	3F	3E		S <sub>9</sub>	5F	5E	BAT
S <sub>4</sub>	3A	3G	3D	S <sub>10</sub>	5A	5G	5D
S <sub>5</sub>	3B	3C	P	S <sub>11</sub>	5B	5C	DEG
S <sub>6</sub>	4F	4E	F				

### 2. Liquid Crystal Drive Waveform

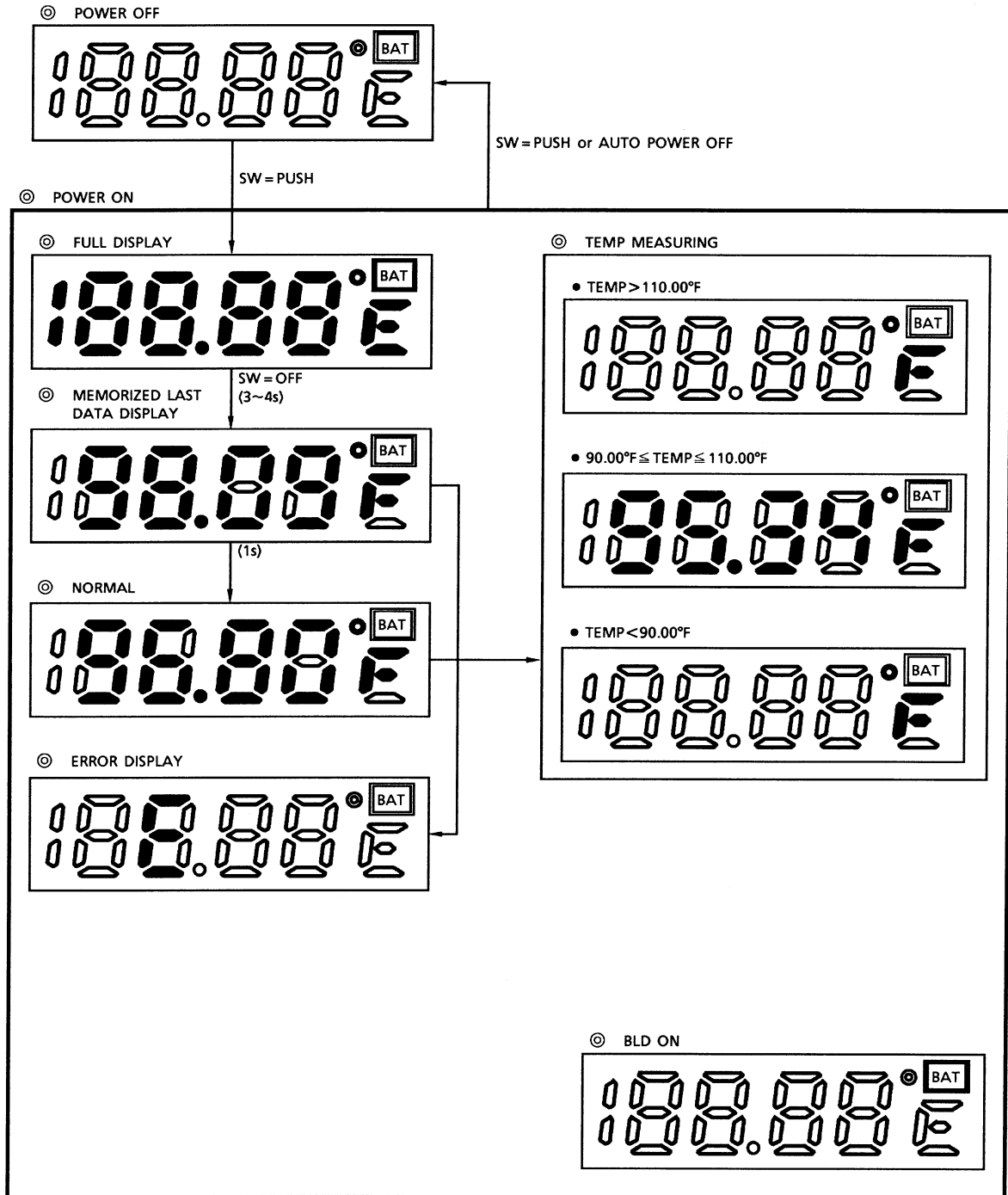


## Display

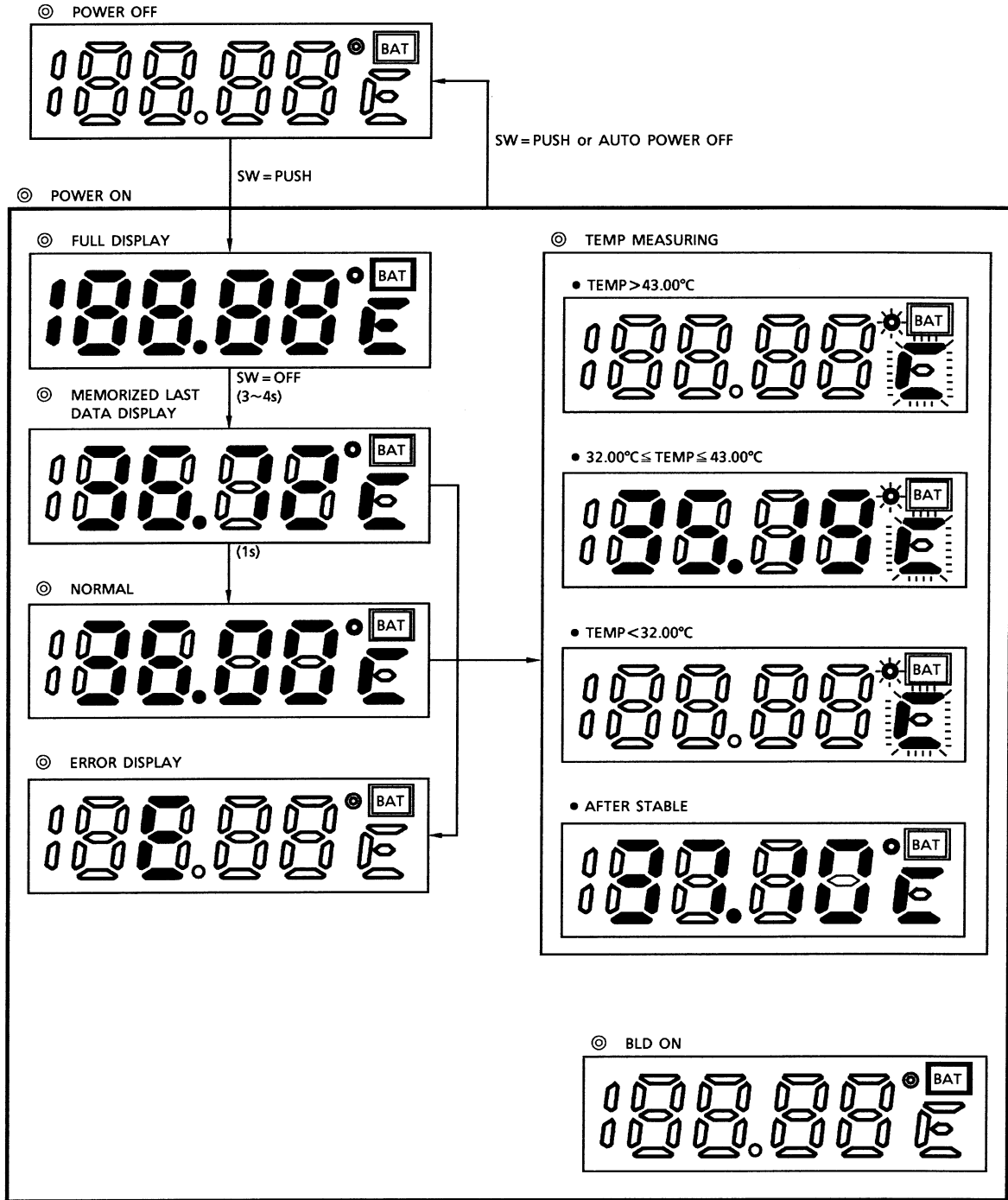
### 1. Switch Operation (in case of the elapsed time informing function, unit: °C)



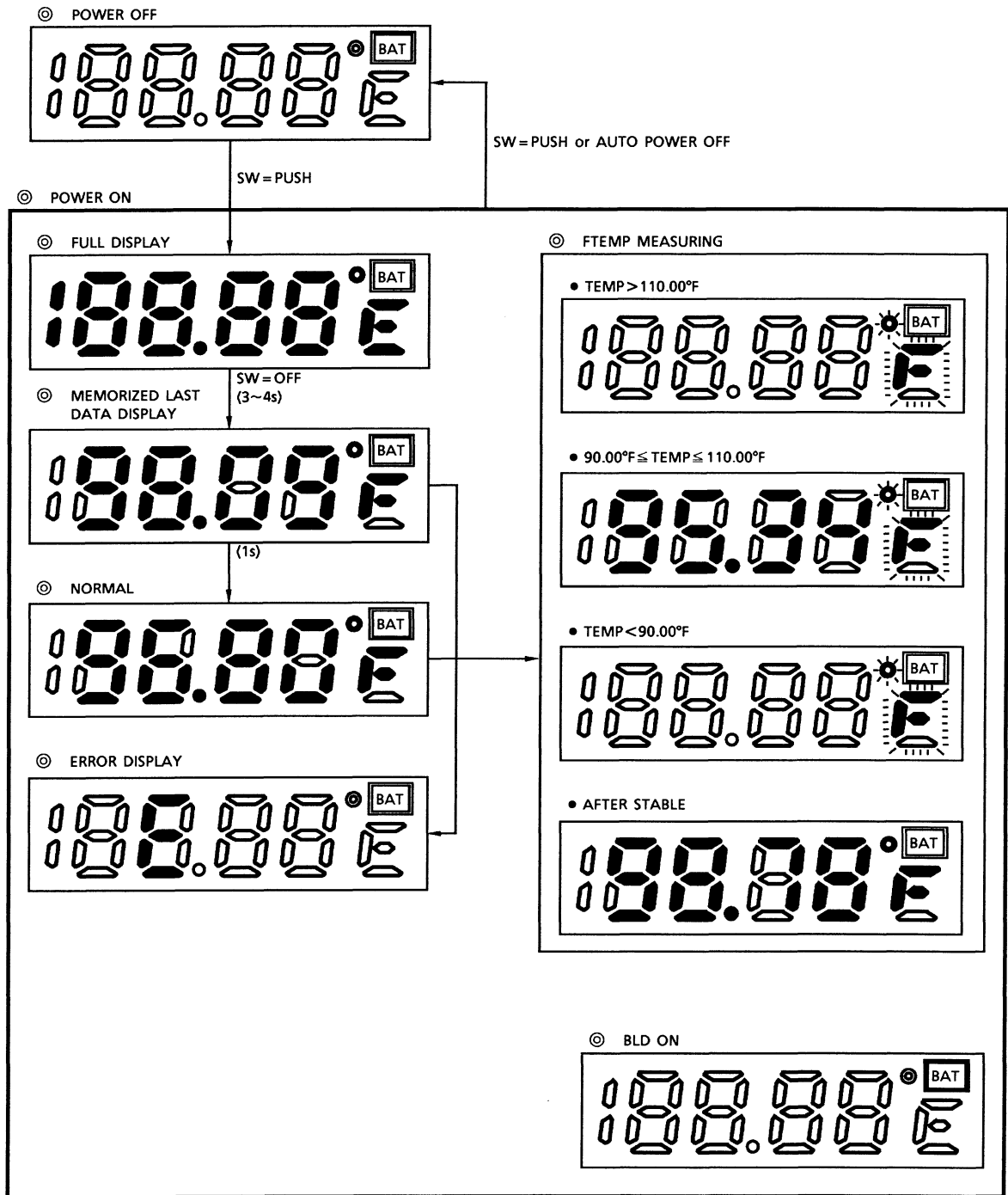
2. Switch Operation (in case of the elapsed time informing function, unit: °F)



3. Switch Operation (stable detection informing function, unit: °C)



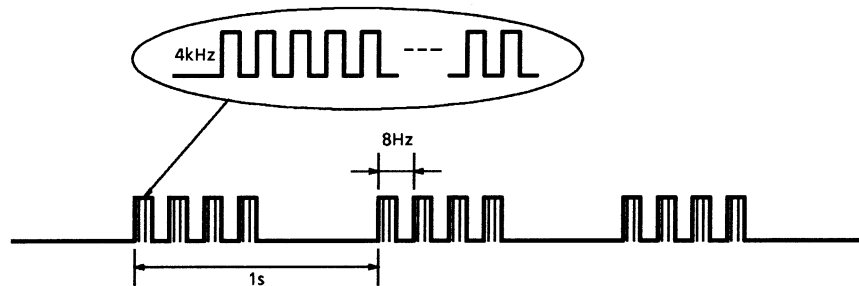
4. Switch Operation (stable detection informing function, unit: °F)





**5. Full Display and BZ Demonstration**

When power SW is on the display keep the FULL DISPLAY and after 1~2 s BZ is out during SW holding, as follows.



**6. Memorized Last Data Display**

After full display (releasing SW), the display show the memorized last data and at the same time TEMP oscillation circuit is tested. If the measuring is error, the display show ERROR DISPLAY.

**7. Low Temp Display**

When measured TEMP is below 32°C (90°F), all digits are off and only UNIT (°C/°F) is on.

**8. High Temp Display**

When measured TEMP is over 43°C (110°F), all digits are off and only UNIT (°C/°F) is on.

**9. Stable Detection Display**

In case of the stable detection informing system, the °C or °F mark is flashing during the temperature measurement. The stable detection is performed by checking the 0.1°~10° digits for °C (0.1°~100° digits for °F). If the 0.1°~10° digits (0.1°~100° digits for °F) do not change for 16 s (8 s for °F), it is judged that temperature is stable and the flashing is stopped, and the °C or °F mark is kept on to inform the stable.

The stable display is held even when a value of measured temperature dropped. If, however, temperature rises again and the 0.1°~10° digits (0.1°~100° digits for °F) changed after stable detection, the °C or °F mark starts to flash again.

**10. Voltage Drop Detection Display**

If battery voltage drops, a display is made to inform the time to exchange the battery. When voltage drop is detected, "BAT" mark is only displayed, but the others are disappeared.

The indicating is kept till power off.

Voltage is detected when SW is pushed and every one minute after power on.

**Selection of Function**

**1. Selection of Elapsed Time Informing/Stable Detection Informing Functions**

The elapsed time informing/stable detection informing function is selectable by the BT pin.

BT { OPEN or VSS1: Time informing function (1 min, 2 min, 3 min, 5 min)  
 { VDD: Stable detection informing function

**2. Selection of °C/°F Measurement**

°C/°F measurement is selectable by the FAH pin.

FAH { VSS1 or OPEN: °C  
 { VDD: °F

### 3. Selection of Auto Power Off Function

Auto power off function can be set by TM and TIME pin.

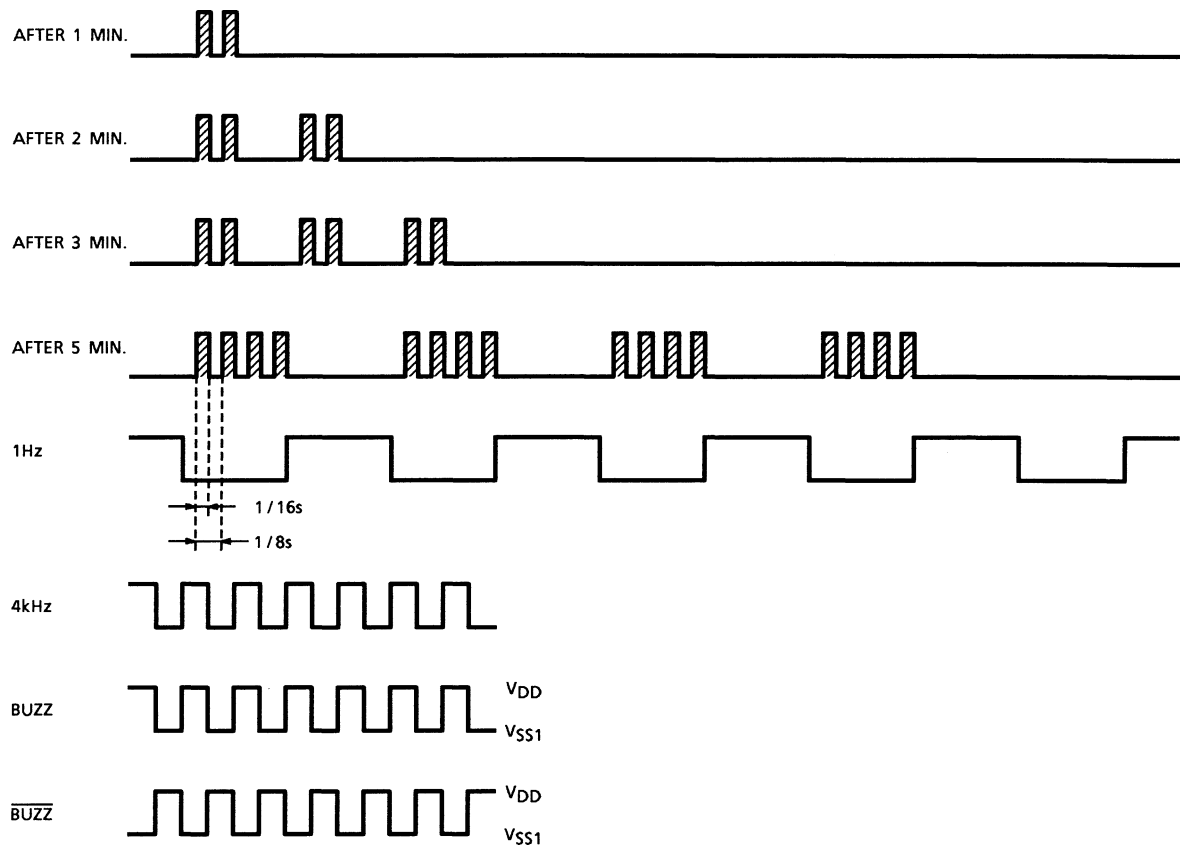
TM	TIME	
V <sub>SS1</sub> or OPEN	V <sub>SS1</sub> or OPEN	AUTO POWER OFF AFTER 16 min (from power ON)
	V <sub>DD</sub>	AUTO POWER OFF AFTER 64 min (from power ON)
V <sub>DD</sub>	—	NO AUTO POWER OFF FUNCTION

“—” Don't care

## Time Informing Function

### 1. In Case of The Elapsed Time Informing System

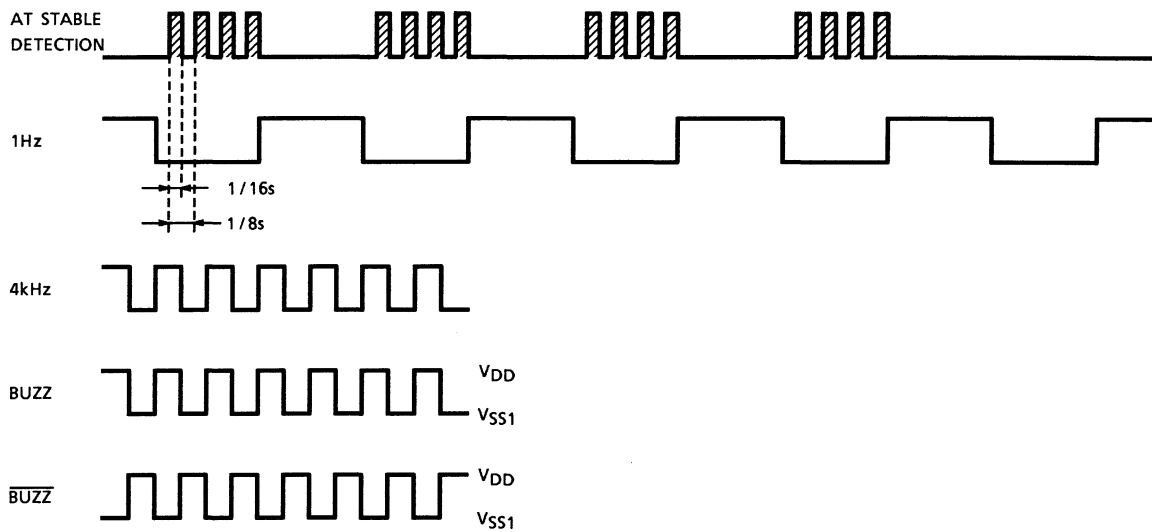
In order to inform time elapsed after power ON, when 1, 2, 3, and 5 minutes have elapsed after times are informed by the buzzer sounding. The buzzer waveforms are shown below.



**2. In Case of The Stable Detection Informing System**

When temperature is judged as the stable, the buzzer sounds for 4 s. to inform.

Even when temperature rises thereafter and stable temperature is detected again, the buzzer does not sound. The buzzer waveforms in this system are shown below.

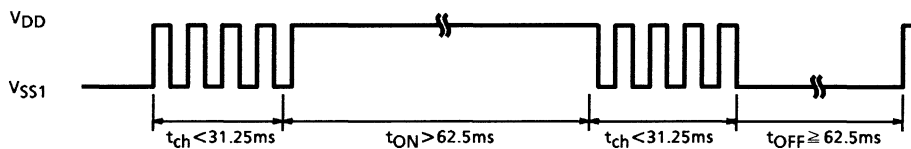


**Input Pins**

**1. SW Pin**

The SW pin has a built-in pull-down resistor and the power supply is repeatedly turned ON/OFF whenever the SW pin is connected to VDD.

In this case, in order to prevent malfunction, the chattering of this switch must satisfy the following requirements:



**2. Function Selecting Pins**

There are 3 function selecting pins; FAH, TM, TIME and BT pins, which select respective functions. (refer to SELECTION OF FUNCTION)

Each of these 3 pins has a built-in pull-down resistor.

**3. Test Pins**

There are 5 test pins; TE1, TS1, TS2, TS3 and THRU pins.

Each of these 5 pins has a built-in pull-down resistor.

For the functions of these test pins, refer to TEST FUNCTIONS, CALIBRATION METHODS.

## Test Functions

### 1. All Clear Function

Connection of the TE1 pin to VDD clears all system. After cleared, the mode is POWER OFF.

### 2. Max Value Holding Stop Function

When connect THRU to VDD, the display show the measured temperature every time. Max doesnot be held.

THRU  $\left\{ \begin{array}{l} = \text{VSS1 or OPEN: Max value holding function Available} \\ = \text{VDD: Not available} \end{array} \right.$

### 3. Output Pin Selecting Function

TE1	SW	TS2	TS3	FOUT
1	1	0	0	BLD CONDITION
1	1	0	1	R <sub>S</sub> OSCILLATION CLOCK
1	1	1	0	R <sub>X</sub> OSCILLATION CLOCK
1	1	1	1	SYSTEM OSCILLATION CLOCK

## Calibration Methods

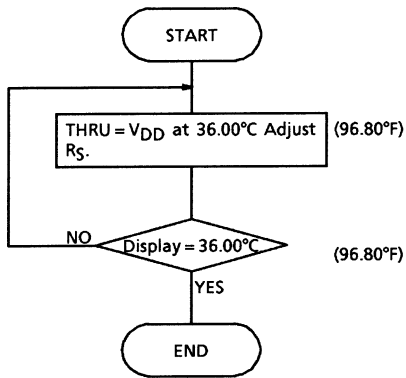
### Method 1

- (1) With a Body Thermometer Immersed in a Water Tank (always maintain water temperature at 36.00°C), Connect the THRU Pin to VDD.  
Adjust R<sub>S</sub> Until the Display on the Body Thermometer Becomes 36.00°C.

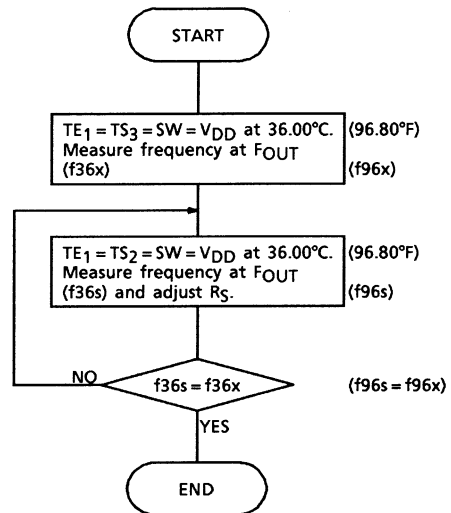
### Method 2

- (1) With a Body Thermometer Immersed in a Water Tank (always maintain water temperature at 36.00°C), Connect the TE<sub>1</sub>, TS<sub>3</sub> and SW Pin to VDD, Set a Frequency Counter and Then, Measure Frequency at the F<sub>OUT</sub> Pin. (f<sub>36x</sub>)
- (2) Connect the TE<sub>1</sub>, TS<sub>2</sub> and SW Pins to VDD. Measure Frequency at the F<sub>OUT</sub> Pin. (f<sub>36s</sub>)  
Adjust R<sub>S</sub> Until Frequency f<sub>36s</sub> and Frequency f<sub>36x</sub> Become Same with Each Other.

**Method 1**



**Method 2**



**Example of Using Thermister**

Thermistor	833ET	503ET
VENDOR	ISHIZUKA ELECTRONICS	ISHIZUKA ELECTRONICS
RESISTANCE (32°C)	61.56 kΩ	36.94 kΩ
RESISTANCE (36°C)	52.10 kΩ	31.26 kΩ
RESISTANCE (43°C)	39.24 kΩ	23.55 kΩ
B-VALUE	3953 ± 1% (B30/45)	3953 ± 1% (B30/45)
Rs	22.12 kΩ~23.23 kΩ	11.83 kΩ~12.42 kΩ
R1	40.15 kΩ	19.81 kΩ
R2	10 kΩ	3.5 kΩ
C	700 pF	1100 pF
RO	150 kΩ ± 2%	150 kΩ ± 2%

## Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage (1)	V <sub>SS1</sub> -V <sub>DD</sub>	-3.0~0.2	V
Supply voltage (2)	V <sub>SS2</sub> -V <sub>DD</sub>	-6.0~0.2	V
Operating temperature	T <sub>opr</sub>	0~50	°C
Storage temperature	T <sub>stg</sub>	-40~125	°C

## Electrical Characteristicse

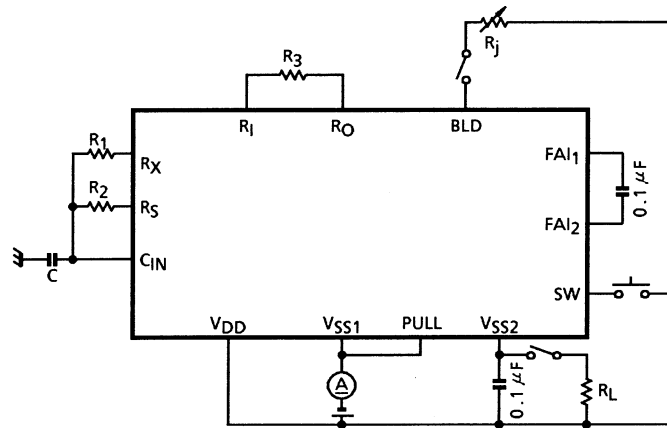
(unless otherwise specified, V<sub>DD</sub> = 0 V, V<sub>SS1</sub> = -1.55 V, V<sub>SS2</sub> = -3.00 V, f<sub>o</sub> = 64.0 kHz, Ta = 25°C)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Typ.	Max	Unit
V <sub>SS1</sub> operating voltage	V <sub>SS1</sub>	2	—	1.25	1.55	2.00	V
V <sub>SS2</sub> operating voltage	V <sub>SS2</sub>	2	—	2.00	3.00	4.00	V
Full consumption current (1)	I <sub>SUP1</sub>	1	FAH, TM = OPEN Power ON, no load	—	—	50	μA
Full consumption current (2)	I <sub>SUP2</sub>	1	FAH, TM = OPEN Power OFF, no load	—	—	0.20	μA
Doubler output voltage	V <sub>UCO</sub>	1	R <sub>L</sub> = 2 MΩ	2.90	—	—	V
Built-in capacitance	C <sub>o</sub>	1	—	—	30	—	pF
Oscillation frequency (1)	f <sub>o</sub>	1	R <sub>O</sub> = 140 kΩ Co = built-in	—	64	—	kHz
Oscillation frequency (2)	f <sub>xs</sub>	1	R <sub>X</sub> = R <sub>S</sub> = 53.9 kΩ C = 300 pF	—	25	—	kHz
Detecting resistor range	R <sub>j</sub>	—	1 detected voltage 1.30 V	200	—	1700	kΩ
Input current (1) (SW, TE <sub>1</sub> , TS <sub>1</sub> , 2, 3)	I <sub>IH1</sub> I <sub>IL1</sub>	3	V <sub>IH1</sub> = 0.00 V V <sub>IL1</sub> = -1.55 V	1.0 -0.1	—	10.0 —	μA
Input current (2) (SW, TE <sub>1</sub> , TS <sub>1</sub> , 2, 3)	— I <sub>IL2</sub>	3	— V <sub>IL2</sub> = -1.25 V	— 50	—	— 250	μA
Input current (3) (FAH, THRU, TM, BT, TIME)	I <sub>IH3</sub> I <sub>IL3</sub>	3	POWER ON V <sub>IH</sub> = 0 V V <sub>IL</sub> = -1.55 V	0.04 -0.1	—	0.28 —	μA
Input current (4) (FAH, THRU, TM, BT, TIME)	— I <sub>IL4</sub>	3	— Power ON state V <sub>IL4</sub> = -1.25 V	— 0.4	—	— 8.0	μA
Input current (5) (FAH, THRU, TM, BT, TIME)	I <sub>IH5</sub> I <sub>IL5</sub>	3	Power OFF state V <sub>IH5</sub> = -0.00 V Power OFF state V <sub>IL5</sub> = -1.55 V	— -0.1	—	0.1 —	μA
Output current (1) (segment)	I <sub>OH1</sub> I <sub>OL1</sub>	3	V <sub>OH1</sub> = -0.3 V V <sub>OL1</sub> = -2.7 V	— 4.00	—	-4.00 —	μA
Output current (2) (COM V <sub>SS2</sub> )	I <sub>OH2</sub> I <sub>OL2</sub>	3	V <sub>OH2</sub> = -0.3 V V <sub>OL2</sub> = -2.7 V	— 60.0	—	-60.0 —	μA
Output current (3) (COM V <sub>SS1</sub> )	I <sub>OH3</sub> I <sub>OL3</sub>	3	V <sub>OH3</sub> = -1.75 V V <sub>OL3</sub> = -1.35 V	— 60.0	—	-60.0 —	μA

Characteristics	Symbol	Test Circuit	Test Condition	Min	Typ.	Max	Unit
Output current (4) (BZ, $\overline{\text{BZ}}$ )	$I_{OH4}$	3	$V_{OH4} = -0.5 \text{ V}$ $V_{SS2} = -2.5 \text{ V}$ $V_{SS1} = -1.25 \text{ V}$	—	—	-200	$\mu\text{A}$
	$I_{OL4}$		$V_{OL4} = -0.75 \text{ V}$ $V_{SS2} = -2.5 \text{ V}$ $V_{SS1} = -1.25 \text{ V}$	200	—	—	
Output current (5) ( $F_{OUT}$ )	$I_{OH5}$	3	$V_{OH5} = -0.2 \text{ V}$	—	—	-30.0	$\mu\text{A}$
	$I_{OL5}$		$V_{OL5} = -1.35 \text{ V}$	30.0	—	—	

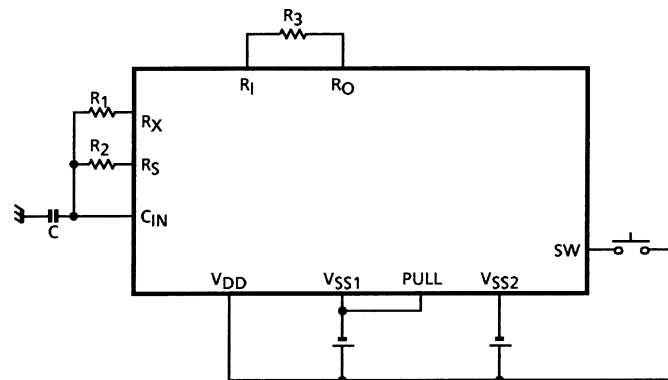
**Test Circuit**

1.



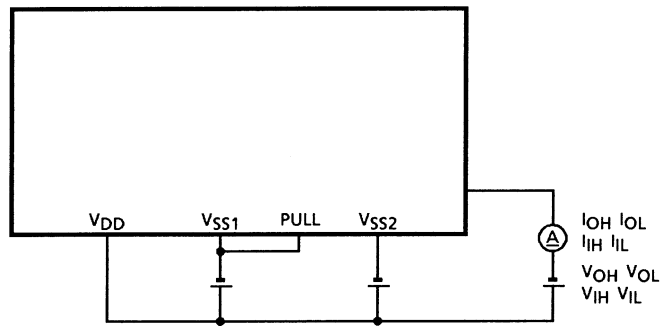
$C = 300 \text{ pF}$   
 $R_1 = R_2 = 56 \text{ k}\Omega$   
 $R_3 = 150 \text{ k}\Omega$   
 $R_L = 2 \text{ M}\Omega$

2.

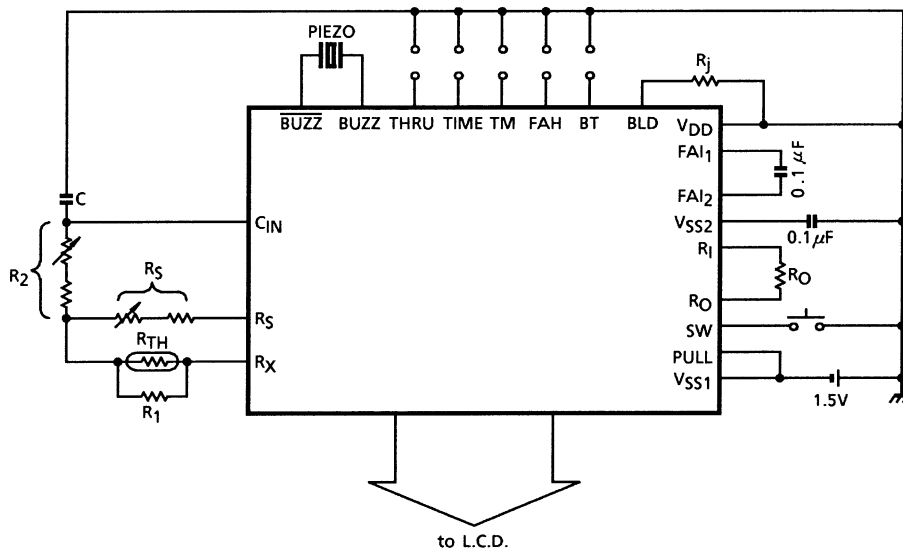


$C = 300 \text{ pF}$   
 $R_1 = R_2 = 56 \text{ k}\Omega$   
 $R_3 = 150 \text{ k}\Omega$

3.



Application Circuit



It is desirable to use C, R1, R2 and RS having the satisfactory temperature characteristic. It is desirable to minimize the range of variable resistance of R2 and RS could as possible.



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