

Amplifier Transistors PNP Silicon

MAXIMUM RATINGS

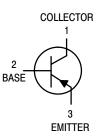
Rating	Symbol	BC327	Unit
Collector–Emitter Voltage	VCEO	-45	Vdc
Collector-Base Voltage	V _{CBO}	-50	Vdc
Emitter-Base Voltage	V _{EBO}	-5.0	Vdc
Collector Current – Continuous	IC	-800	mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	PD	625 5.0	mW mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	PD	1.5 12	Watt mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

BC327, BC327-16, BC327-25, BC327-40



THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	200	°C/W
Thermal Resistance, Junction to Case	$R_{ heta JC}$	83.3	°C/W



ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector–Emitter Breakdown Voltage (I _C = -10 mA, I _B = 0)	V(BR)CEO	-45	_	_	Vdc
Collector–Emitter Breakdown Voltage (I _C = –100 μA, I _E = 0)	V(BR)CES	-50	_	_	Vdc
Emitter–Base Breakdown Voltage ($I_E = -10 \mu A, I_C = 0$)	V(BR)EBO	-5.0	_	_	Vdc
Collector Cutoff Current (V _{CB} = -30 V, I _E = 0)	ICBO	_	_	-100	nAdc
Collector Cutoff Current (V _{CE} = -45 V, V _{BE} = 0)	ICES	_	_	-100	nAdc
Emitter Cutoff Current $(V_{EB} = -4.0 \text{ V}, I_{C} = 0)$	IEBO	_	_	-100	nAdc

BC327, BC327-16, BC327-25, BC327-40

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted) (Continued)

Characteristic		Symbol	Min	Тур	Max	Unit
ON CHARACTERISTICS			•	•		
DC Current Gain $(I_C = -100 \text{ mA}, V_{CE} = -1.0 \text{ V})$ $(I_C = -300 \text{ mA}, V_{CE} = -1.0 \text{ V})$	BC327 BC327–16 BC327–25 BC327–40	h _{FE}	100 100 160 250 40	- - - -	630 250 400 630	-
Base–Emitter On Voltage (I _C = –300 mA, V _{CE} = –1.0 V)		V _{BE(on)}	_	_	-1.2	Vdc
Collector–Emitter Saturation Voltage (I _C = -500 mA, I _B = -50 mA)		VCE(sat)	-	-	-0.7	Vdc
SMALL-SIGNAL CHARACTERISTICS	3	•				
Output Capacitance (V _{CB} = -10 V, I _E = 0, f = 1.0 MHz)		C _{ob}	-	11	-	pF
Current–Gain – Bandwidth Product (I _C = -10 mA, V _{CE} = -5.0 V, f = 100 MH:	<u>z</u>)	fΤ	-	260	_	MHz

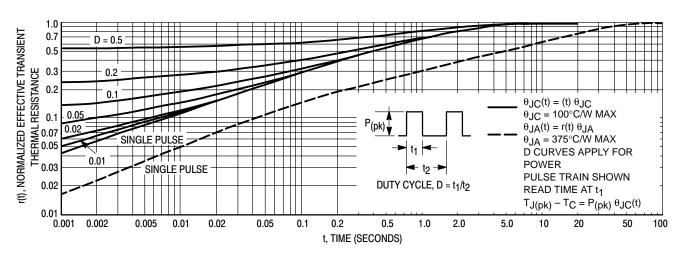
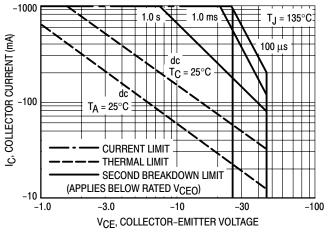


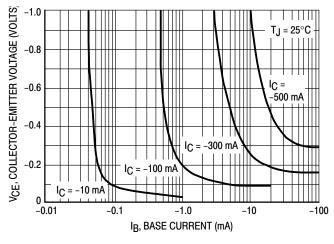
Figure 1. Thermal Response



100 T_A = 25°C T

Figure 2. Active Region - Safe Operating Area

Figure 3. DC Current Gain



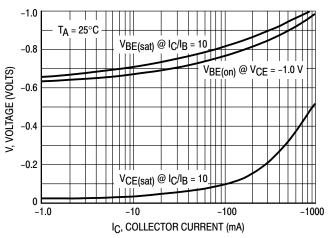
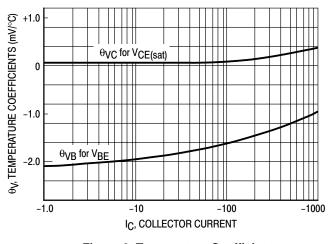


Figure 4. Saturation Region

Figure 5. "On" Voltages



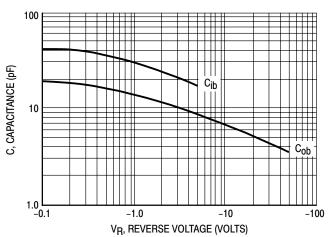


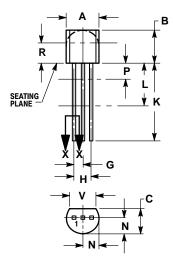
Figure 6. Temperature Coefficients

Figure 7. Capacitances

BC327, BC327-16, BC327-25, BC327-40

PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 **ISSUE AL**





STYLE 17: PIN 1. COLLECTOR

BASE **EMITTER**

NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH.
 CONTOUR OF PACKAGE BEYOND DIMENSION R
- IS UNCONTROLLED.
 LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
P		0.100		2.54
R	0.115		2.93	
٧	0.135		3.43	

ON Semiconductor and War are trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer.

PUBLICATION ORDERING INFORMATION

Literature Fulfillment:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA

Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada

Email: ONlit@hibbertco.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

JAPAN: ON Semiconductor, Japan Customer Focus Center 4-32-1 Nishi-Gotanda, Shinagawa-ku, Tokyo, Japan 141-0031

Phone: 81-3-5740-2700 Email: r14525@onsemi.com

ON Semiconductor Website: http://onsemi.com

For additional information, please contact your local Sales Representative.