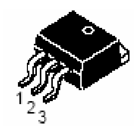
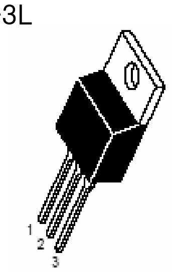
3A Low-Voltage Low-Dropout Regulator LM39300/39301/39302

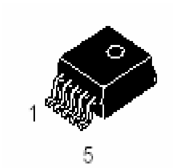
TO263-3L / TO220-3L
(39300-x.x Fixed only)



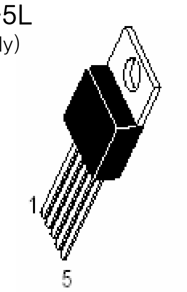
1. In
2. Gnd
3. Out



TO263-5L / TL220-5L
(39301-x.x & 39302 only)



1. En
2. In
3. Gnd
4. Out
5. Flg or Adj



FEATURES

- •3A minimum guaranteed output current
- •500mV typical dropout at 3A
  Ideal for 3.0V to 2.5V conversion
  Ideal for 2.5V to 1.8V or 1.5V conversion
- •1% initial accuracy
- •Low ground current
- •Current limiting and thermal shutdown
- •Reversed-battery protection
- •Reversed-leakage protection
- •Fast transient response
- •TTL/CMOS compatible enable pin-LM39301 only
- •Error flag output - LM39301 only
- •Adjustable version - LM39302 only
- •TO-263 and TO-220 packaging

APPLICATIONS

- •LDO linear regulator for PC add-in cards
- •High-efficiency linear power supplies
- •Multimedia and PC processor supplies
- •SMPS post regulator
- •Low-voltage microcontrollers
- •StrongARM™ processor supply

PIN DESCRIPTION

Enable (Input)	CMOS-compatible control input.
	Logic high = enable, logic
	Logic low or open = Shutdown
IN	Supply (Input): +16V maximum supply
GND	Ground pin and TAB are internally connected.
OUT	Regulator Output
FLG	Flag (Output): Open-collector error flag output.
ADJ	Adjustment Input: Feedback input.

ORDERING INFORMATION

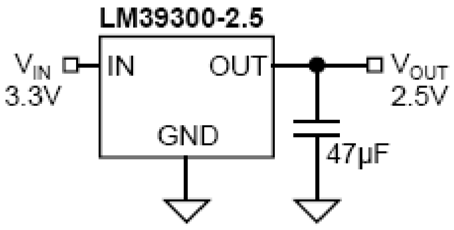
Device	Marking	Package
LM39300R- X.X	LM39300-X.X	TO-263
LM39300T-X.X	LM39300-X.X	TO-220
LM39301R-X.X	LM39301-X.X	TO-263
LM39301T-X.X	LM39301-X.X	TO-220
LM39302R-Adj	LM39302-Adj	TO-263
LM39302T-Adj	LM39302-Adj	TO-220

* X.X = Fixed Vout = 1.5V, 1.8V, 2.5V, 3.3V, 5.0V

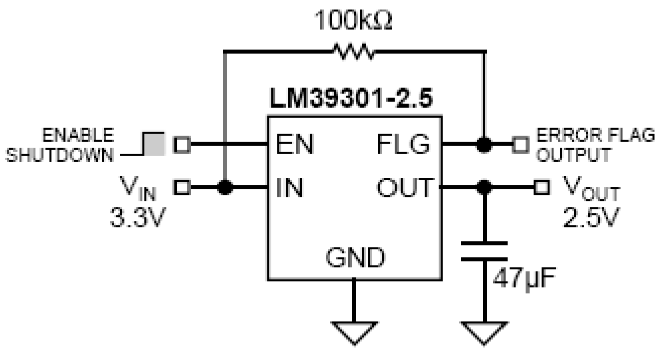
DESCRIPTION

The LM39300, LM39301 and LM39302 are 3.0A low-dropout linear voltage regulators that provide a low voltage, high-current output with a minimum of external components.
The LM39300/1 offers extremely low dropout (typically 400mV at 3.0A) and low ground current (typically 36mA at 3.0A).
The LM39300/1/2 is ideal for PC add-in cards that need to convert from standard 5V or 3.3V down to new, lower core voltages. A guaranteed maximum dropout voltage of 500mV over all operating conditions allows the LM39300/1/2 to pro-vide 2.5V from a supply as low as 3V.
The LM39300/1/2 also has fast transient response for heavy switching applications.The device requires only 47F of output capacitance to maintain stability and achieve fast transient responseThe LM39300/1 is fully protected with over current limiting,thermal shutdown, reversed-battery protection, reversed-leakage protection, and reversed-lead insertion. The LM39301 offers a TTL-logic compatible enable pin and an error flag that indicates under voltage and over current conditions. Offered in fixed voltages, the LM39300/1/2 comesin the TO-220 and TO-263 packages and is an ideal upgrade to older, NPN-based linear voltage regulators.

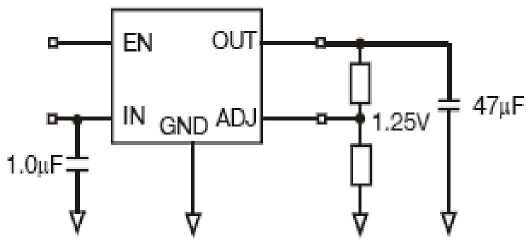
Typical Application Circuit



LM39300



LM39301



LM39302

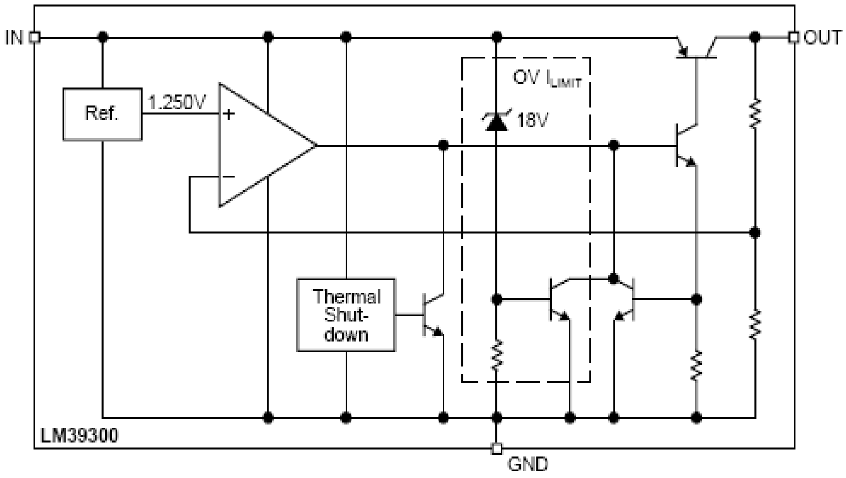
Absolute Maximum Ratings (Note 1)

Supply Voltage (VIN) : −20V to +20V
Enable Voltage (VEN) : +20V
Storage Temperature (TS) : −65°C to +150°C
Lead Temperature (soldering, 5 sec) : 260°C
ESD, Note 3

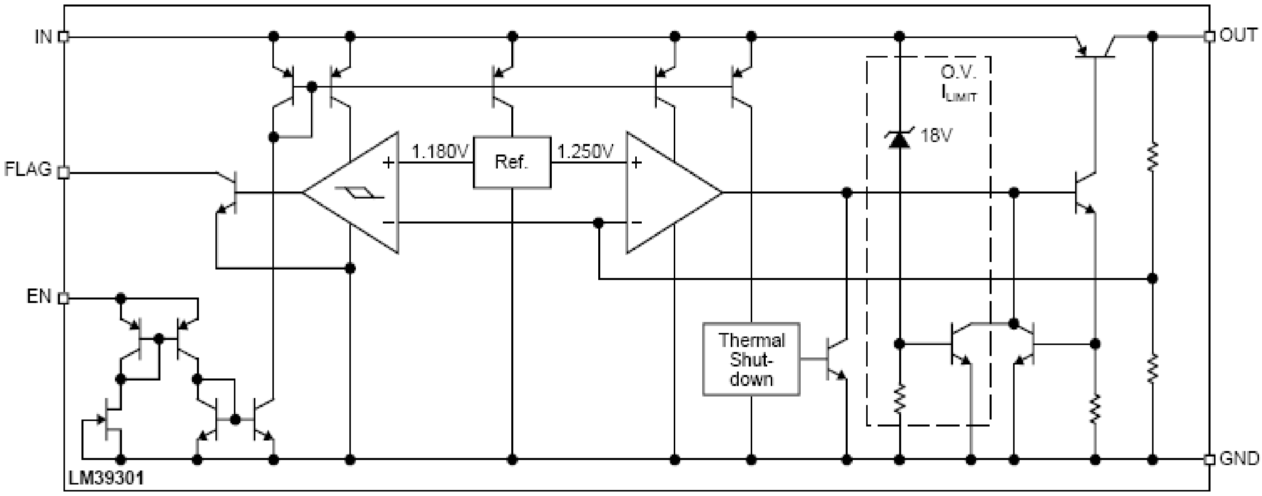
Operating Ratings (Note 2)

Supply Voltage (VIN) : +2.25V to +16V
Enable Voltage (VEN) : +16V
Maximum Power Dissipation (PD(max)) Note 4
Junction Temperature (TJ) : −40°C to +125°C
Package Thermal Resistance
TO−263(θ_{JC}) : 2°C/W
TO−220(θ_{JC}) : 2°C/W

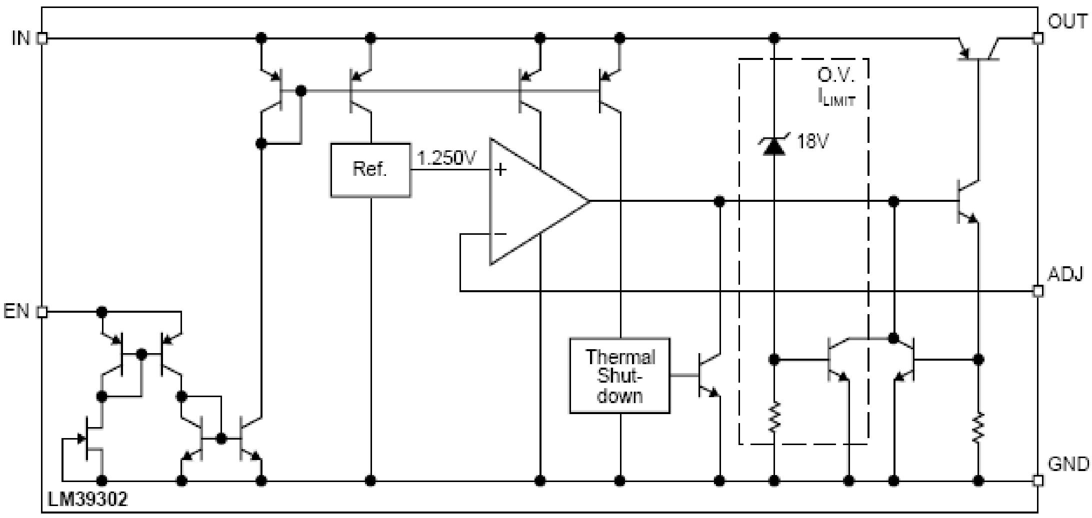
Block Diagram



LM39300 Fixed (1.5V,1.8V,2.5V,3.3V,5.0V)



LM39301 Fixed with Flag and Enable



LM39302 Adjustable

Electrical Characteristics

T_J = 25°C, **bold** values indicate –40°C ≤ T_J ≤ +125°C; unless noted

Symbol	Parameter	Condition	Min	Typ	Max	Units
V_{OUT}	Output Voltage	10mA 10mA ≤ I_{OUT} ≤ 3A, V_{OUT} + 1V ≤ V_{IN} ≤ 8V	–1 **–2**		1 **2**	% %
	Line Regulation	I_{OUT} = 10mA, V_{OUT} + 1V ≤ V_{IN} ≤ 8V		0.06	0.5	%
	Load Regulation	V_{IN} = V_{OUT} + 1V, 10mA ≤ I_{OUT} ≤ 3A		0.2	1	%
$\Delta V_{OUT}/\Delta T$	Output Voltage Temp. Coefficient, **Note 5**			**20**	**100**	ppm/°C
V_{DO}	Dropout Voltage, **Note 6**	I_{OUT} = 100mA, ΔV_{OUT} = –1%		80	**200**	mV
		I_{OUT} = 750mA, ΔV_{OUT} = –1%		200		mV
		I_{OUT} = 1.5A, ΔV_{OUT} = –1%		320		mV
		I_{OUT} = 3A, ΔV_{OUT} = –1%		400	**550**	mV
I_{GND}	Ground Current, **Note 7**	I_{OUT} = 750mA, V_{IN} = V_{OUT} + 1V		10	**20**	mA
		I_{OUT} = 1.5A, V_{IN} = V_{OUT} + 1V		17		mA
		I_{OUT} = 3A, V_{IN} = V_{OUT} + 1V		45		mA
$I_{GND(do)}$	Dropout Ground Pin Current	V_{IN} ≤ $V_{OUT(nominal)}$ – 0.5V, I_{OUT} = 10mA		6		mA
$I_{OUT(lim)}$	Current Limit	V_{OUT} = 0V, V_{IN} = V_{OUT} + 1V		4.5		A
Enable Input (LM39301)						
V_{EN}	Enable Input Voltage	logic low (off)			**0.8**	V
		logic high (on)	**2.25**			V
I_{IN}	Enable Input Current	V_{EN} = V_{IN}		15	30 **75**	µA µA
		V_{EN} = 0.8V			2 **4**	µA µA
$I_{OUT(shdn)}$	Shutdown Output Current	**Note 8**		10	**20**	µA
Flag Output (LM39301)						
$I_{FLG(leak)}$	Output Leakage Current	V_{OH} = 16V		0.01	1 **2**	µA µA
$V_{FLG(do)}$	Output Low Voltage	V_{IN} = 2.250V, I_{OL}, = 250µA, **Note 9**		220	300 **400**	mV mV
V_{FLG}	Low Threshold	% of V_{OUT}	93			%
	High Threshold				99.2	%
	Hysteresis			1		%
LM39102 Only						
	Reference Voltage	**Note 10**	1.238 1.225 **1.213**	1.250	1.262 **1.275** **1.277**	V **V** **V**
	Adjust Pin Bias Current			40	80 **120**	nA nA
	Reference Voltage Temp. Coefficient	**Note 7**		20		ppm/°C
	Adjust Pin Bias Current Temp. Coefficient			0.1		nA/°C

Note 1. Exceeding the absolute maximum ratings may damage the device.

Note 2. The device is not guaranteed to function outside its operating rating.

Note 3. Devices are ESD sensitive. Handling precautions recommended.

Note 4. $P_{D(max)} = (T_{J(max)} - T_A)$ θ_{JA}, where θ_{JA} depends upon the printed circuit layout. See "Applications Information."

Note 5.Vout temperature coefficient is $\Delta V_{OUT(worst\ case)}$ $(T_{J(max)} - T_{J(min)})$ where $T_{J(max)}$ is +125℃ and $T_{J(min)}$ is 0℃

Note 6. $V_{DO} = V_{IN} - V_{OUT}$ when V_{OUT} decreases to 99% of its nominal output voltage with $V_{IN} = V_{OUT} + 1V$.

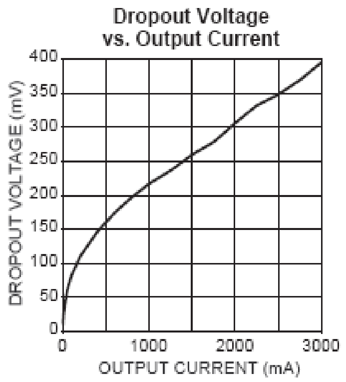
Note 7. I_{GND} is the quiescent current. $I_{IN} = I_{GND} + I_{OUT}$.

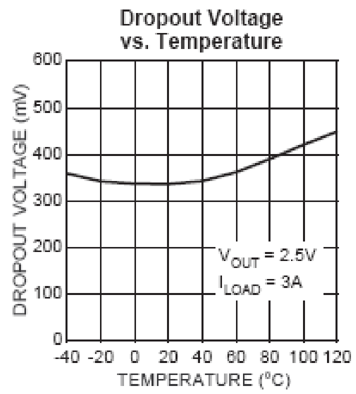
Note 8. V_{EN} 0.8V, V_{IN} 8V, and V_{OUT} = 0V

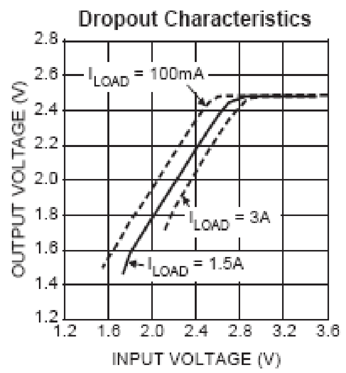
Note 9. For a 2.5V device, V_{IN} = 2.250V (device is in dropout).

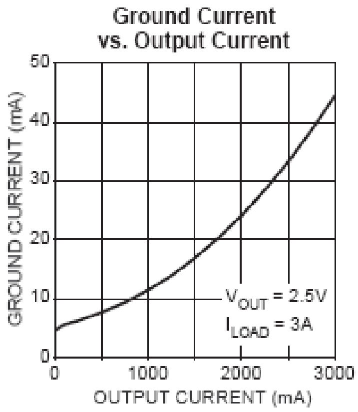
Note 10.VREF ≤ VOUT ≤ (VIN – 1V), 2.25V ≤ VIN ≤ 16V, 10mA ≤ IL ≤ 1A, TJ = TMAX.

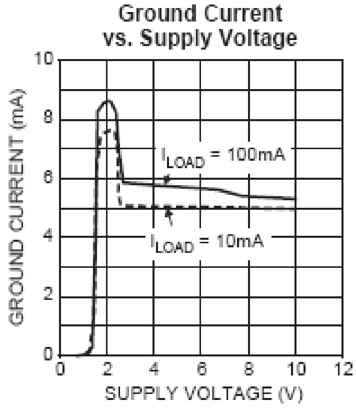
TYPICAL PERFORMANCE CHARACTERISTICS

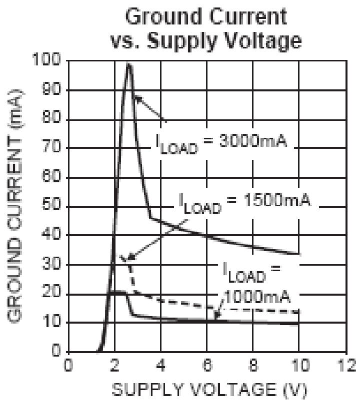


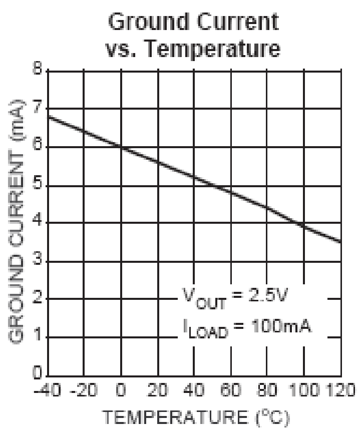


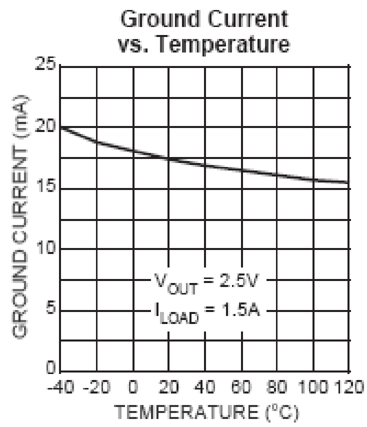


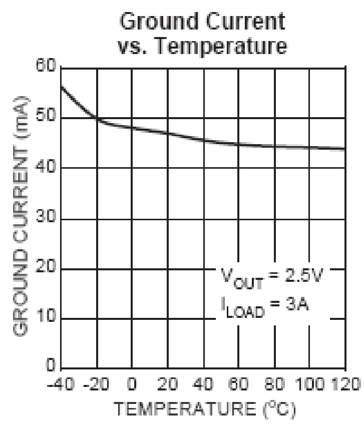




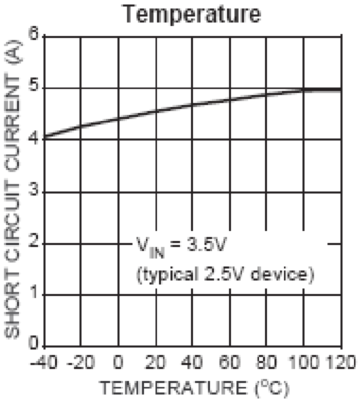




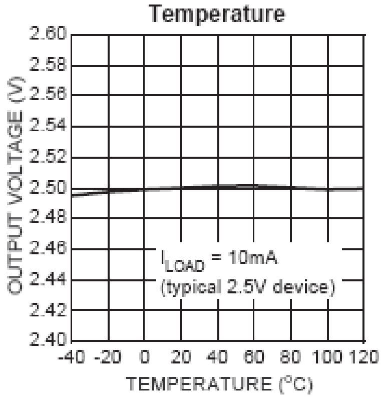




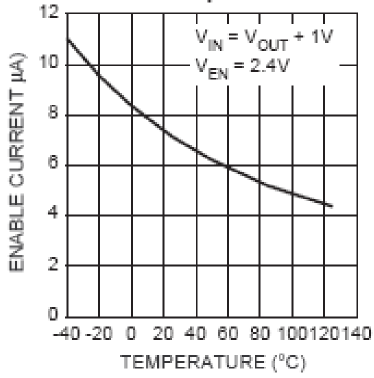
Short Circuit vs. Temperature



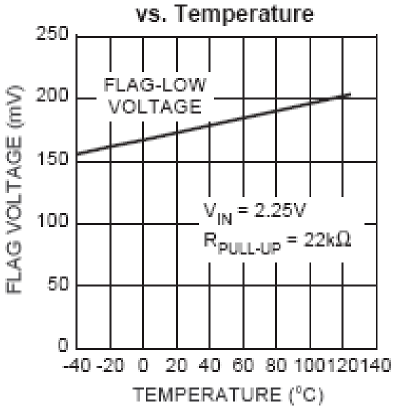
Output Voltage vs. Temperature



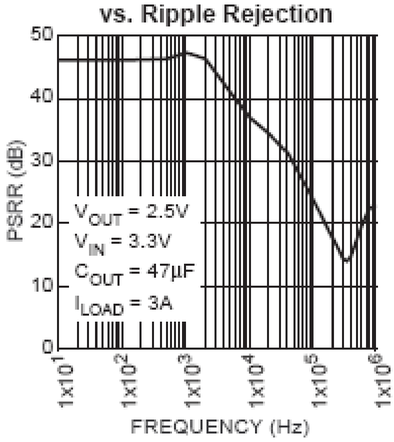
Enable Current vs. Temperature



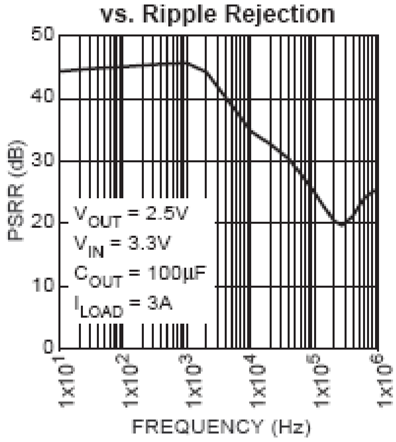
Flag-Low Voltage vs. Temperature



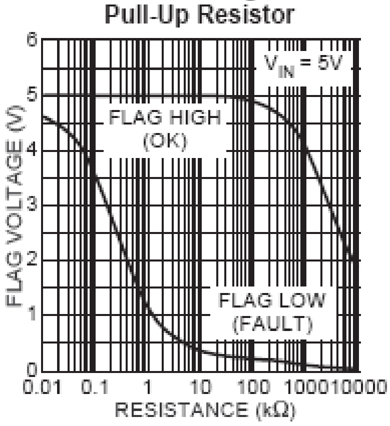
Power Supply vs. Ripple Rejection



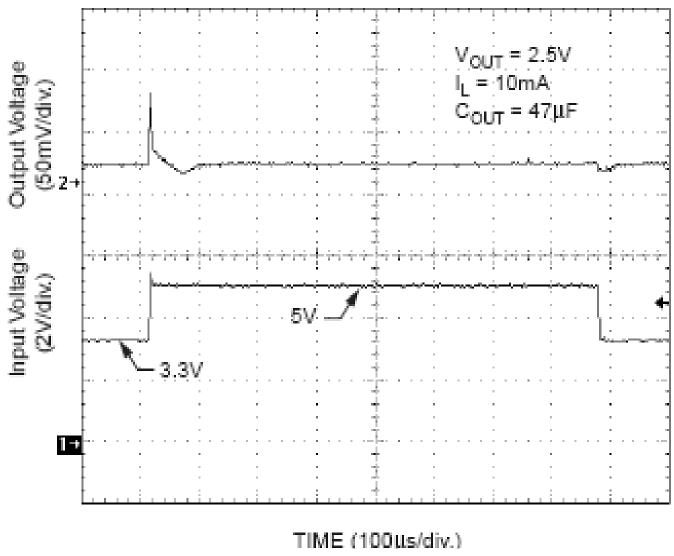
Power Supply vs. Ripple Rejection



Error Flag Pull-Up Resistor



Line Transient Response



APPLICATION INFORMATION

The LM39300/1 is a high-performance low-dropout voltage regulator suitable for moderate to high-current voltage regu-lator applications. Its 500mV dropout voltage at full load makes it especially valuable in battery-powered systems and as a high-efficiency noise filter in post-regulator applications. Unlike older NPN-pass transistor designs, where the mini-mum dropout voltage is limited by the base-to-emitter voltage drop and collector-to-emitter saturation voltage, dropout per-formance of the PNP output of these devices is limited only by the low V_{CE} saturation voltage.A trade-off for the low dropout voltage is a varying base drive requirement. The LM39300/1/2 regulator is fully protected from damage due to fault conditions. Current limiting is provided. This limiting is linear output current during overload conditions is constant. Thermal shutdown disables the device when the die temperature exceeds the maximum safe operating tem-perature. Transient protection allows device (and load) sur-vival even when the input voltage spikes above and below nominal. The output structure of these regulators allows voltages in excess of the desired output voltage to be applied without reverse current flow.

Thermal Design

Linear regulators are simple to use. The most complicated design parameters to consider are thermal characteristics.Thermal design requires four application-specific param-eters:

- Maximum ambient temperature (T_A)
- Output Current (I_{OUT})
- Output Voltage (V_{OUT})
- Input Voltage (V_{IN})
- Ground Current (I_{GND})

Calculate the power dissipation of the regulator from these numbers and the device parameters from this datasheet,where the ground current is taken from the data sheet.

$$PD = (V_{IN} - V_{OUT})\ I_{OUT} + V_{IN} \cdot I_{GND}$$

The heat sink thermal resistance is determined by:

$$\theta_{SA} = (T_{JMAX} - T_A)/P_D - (\theta_{JC} + \theta_{CS})$$

where TJ (max) 125 ℃ and θ_{CS} is between 0℃ and 2℃/W.

The heat sink may be significantly reduced in applications where the minimum input voltage is known and is large compared with the dropout voltage. Use a series input resistor to drop excessive voltage and distribute the heat between this resistor and the regulator. The low dropout properties of Taejin regulators allow signifi-cant reductions in regulator power dissipation and the asso-ciated heat sink without compromising performance. When this technique is employed, a capacitor of at least 1.0F is needed directly between the input and regulator ground.Refer to Application Note 9 for further details and examples on thermal design and heat sink specification.

Output Capacitor

The LM39300/1/2 requires an output capacitor to maintain stability and improve transient response. Proper capacitor selection is important to ensure proper operation. The LM39300/1/2 output capacitor selection is dependent upon the ESR (equivalent series resistance) of the output capacitor to maintain stability. When the output capacitor is 47F or greater, the output capacitor should have less than 1 of ESR. This will improve transient response as well as promote stability. Ultralow ESR capacitors, such as ceramic chip capacitors may promote instability. These very low ESR levels may cause an oscillation and/or underdamped tran-sient response. A low-ESR solid tantalum capacitor works extremely well and provides good transient response and stability over temperature. Aluminum electrolytics can also be used, as long as the ESR of the capacitor is < 1.The value of the output capacitor can be increased without limit. Higher capacitance values help to improve transient response and ripple rejection and reduce output noise.

Input Capacitor

An input capacitor of 1F or greater is recommended when the device is more than 4 inches away from the bulk ac supply capacitance, or when the supply is a battery. Small, surface-mount, ceramic chip capacitors can be used for the bypass-ing. Larger values will help to improve ripple rejection by bypassing the input to the regulator, further improving the integrity of the output voltage.Transient Response and 3.3V.

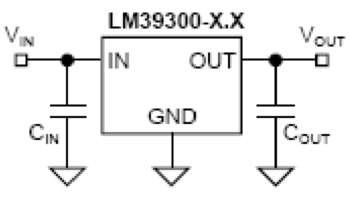


Fig 1. Capacitor Requirements

Minimum Load Current

The LM39300/1/2 regulator is specified between finite loads.
If the output current is too small, leakage currents dominate and the output voltage rises.
A 10mA minimum load current is necessary for proper regulation.

Transient Response and 3.3V to 2.5V Conversion

The LM39300/1/2 has excellent transient response to varia-tions in input voltage and load current. The device has been designed to respond quickly to load current variations and input voltage variations. Large output capacitors are not required to obtain this performance. A standard 47F output capacitor, preferably tantalum, is all that is required. Larger values help to improve performance even further.By virtue of its low-dropout voltage, this device does not saturate into dropout as readily as similar NPN-based de-signs. When converting from 3.3V to 2.5V, the NPN-based regulators are already operating in dropout, with typical dropout requirements of 1.2V or greater. To convert down to 2.5V without operating in dropout, NPN-based regulators require an input voltage of 3.7V at the very least. The LM39300/1/2 regulator will provide excellent performance with an input as low as 3.0V. This gives the PNP-based regulators a distinct advantage over older, NPN-based linear regulators.

Error Flag

The LM39301 version features an error flag circuit which monitors the output voltage and signals an error condition when the voltage drops 5% below the nominal output voltage. The error flag is an open-collector output that can sink 10mA during a fault condition.Low output voltage can be caused by a number of problems, including an overcurrent fault (device in current limit) or low input voltage. The flag is inoperative during overtemperature shutdown.

Enable Input

The LM39301 version features an enable input for on/off control of the device. Its shutdown state draws “zero” current (only microamperes of leakage). The enable input is TTL/ CMOS compatible for simple logic interface, but can be connected to up to 20V. When enabled, it draws approxi-mately 15A.

Adjustable Regulator Design

The LM39302 allows programming the output voltage any-where between 1.25V and the 16V maximum operating rating of the family. Two resistors are used. Resistors can be quite large, up to 1MΩ, because of the very high input impedance and low bias current of the sense comparator: The resistor values are calculated by： *R1=R2(Vout/1.250-1)*
Where VO is the desired output voltage. Figure 1 shows component definition. Applications with widely varying load currents may scale the resistors to draw the minimum load current required for proper operation (see below).