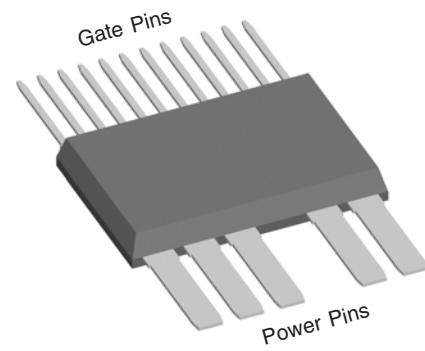
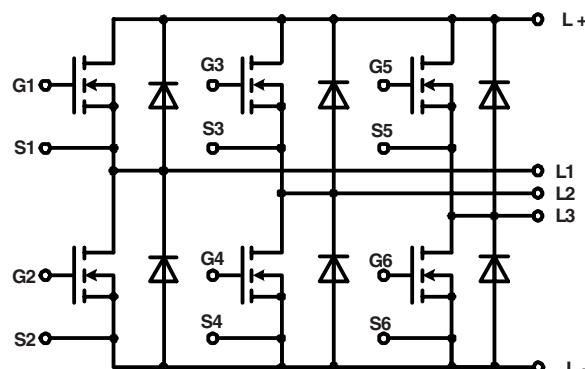


# Three phase full bridge

with Trench MOSFETs  
in DCB isolated high current package

**V<sub>DSS</sub>** = 100 V  
**I<sub>D25</sub>** = 70 A  
**R<sub>DSon typ.</sub>** = 11 mΩ



## MOSFETs

| Symbol           | Conditions                      | Maximum Ratings |   |  |
|------------------|---------------------------------|-----------------|---|--|
| V <sub>DSS</sub> | T <sub>VJ</sub> = 25°C to 150°C | 100             | V |  |
| V <sub>GS</sub>  |                                 | ±20             | V |  |
| I <sub>D25</sub> | T <sub>C</sub> = 25°C           | 70              | A |  |
| I <sub>D90</sub> | T <sub>C</sub> = 90°C           | 50              | A |  |
| I <sub>F25</sub> | T <sub>C</sub> = 25°C (diode)   | 130             | A |  |
| I <sub>F90</sub> | T <sub>C</sub> = 90°C (diode)   | 85              | A |  |

| Symbol  | Conditions  | Characteristic Values                                |                       |                      |
|---|---|--|-----------------------|----------------------|
|   |   | (T <sub>VJ</sub> = 25°C, unless otherwise specified) | min.                  | typ.                 |
| R <sub>DSon</sub>   | on chip level at<br>V <sub>GS</sub> = 10 V; I <sub>D</sub> = 35 A } T <sub>VJ</sub> = 25°C<br>V <sub>GS</sub> = 10 V; I <sub>D</sub> = 35 A } T <sub>VJ</sub> = 125°C |  | 11<br>24              | 14 mΩ<br>mΩ          |
| V <sub>Gsth</sub>   | V <sub>DS</sub> = 20 V; I <sub>D</sub> = 1 mA   | 2  |                       | 4 V                  |
| I <sub>DSS</sub>  | V <sub>DS</sub> = V <sub>DSS</sub> ; V <sub>GS</sub> = 0 V; T <sub>VJ</sub> = 25°C<br>T <sub>VJ</sub> = 125°C   |  | 0.1                   | 1 μA<br>mA           |
| I <sub>GSS</sub>  | V <sub>GS</sub> = ±20 V; V <sub>DS</sub> = 0 V  |  |                       | 0.2 μA               |
| Q <sub>g</sub><br>Q <sub>gs</sub><br>Q <sub>gd</sub>                          | V <sub>GS</sub> = 10 V; V <sub>DS</sub> = 80 V; I <sub>D</sub> = 25 A }<br>V <sub>GS</sub> = 10 V; V <sub>DS</sub> = 80 V; I <sub>D</sub> = 25 A }                    |  | 110<br>18<br>44       | nC<br>nC<br>nC       |
| t <sub>d(on)</sub><br>t <sub>r</sub><br>t <sub>d(off)</sub><br>t <sub>f</sub> | V <sub>GS</sub> = 10 V; V <sub>DS</sub> = 30 V;<br>I <sub>D</sub> = 25 A; R <sub>G</sub> = 10 Ω   |  | 35<br>85<br>150<br>70 | ns<br>ns<br>ns<br>ns |
| V <sub>F</sub>  | (diode) I <sub>F</sub> = 35 A; V <sub>GS</sub> = 0 V  | 0.8  | 1.25                  | V                    |
| t <sub>rr</sub>   | (diode) I <sub>F</sub> = 75 A; -di/dt = 100 A/μs; V <sub>DS</sub> = 30 V  | 80   |                       | ns                   |
| R <sub>thJC</sub><br>R <sub>thJH</sub>  | with heat transfer paste  | 1.7  | 0.85                  | K/W<br>K/W           |

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## Component

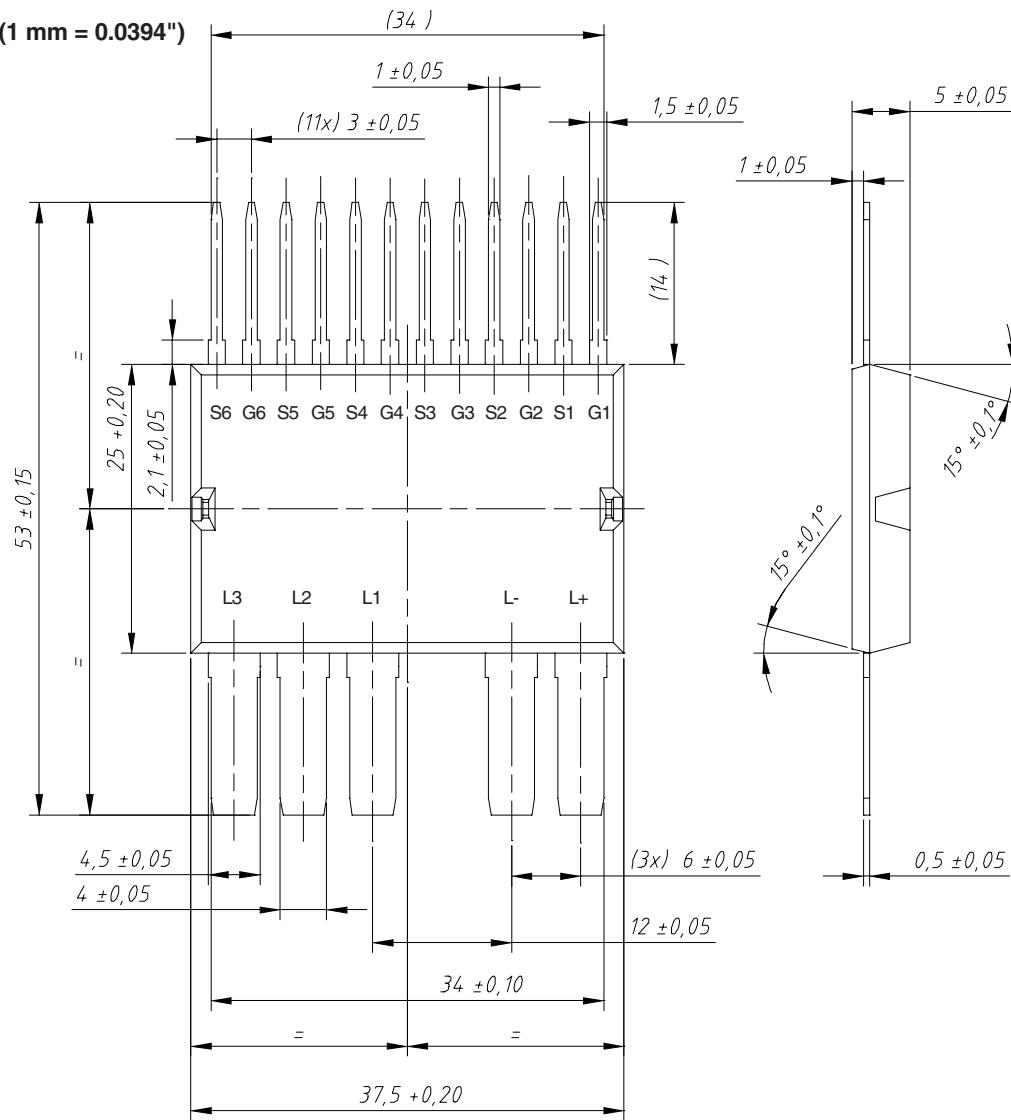
| Symbol     | Conditions   | Maximum Ratings |    |
|------------|--|-----------------|----|
| $I_{RMS}$  | per pin in main current paths ( $L_+, L_-, L_1, L_2, L_3$ )<br>may be additionally limited by external connections | 300             | A  |
| $T_{VJ}$   |  | -40...+175      | °C |
| $T_{stg}$  |  | -55...+125      | °C |
| $V_{ISOL}$ | $I_{ISOL} \leq 1 \text{ mA}; 50/60 \text{ Hz}; t = 1 \text{ min}$  | 1000            | V~ |
| $F_c$      | Mounting force with clip   | 50 - 250        | N  |

## Symbol Conditions

**Characteristic Values**  
 $(T_{V,I} = 25^\circ\text{C}$ , unless otherwise specified)

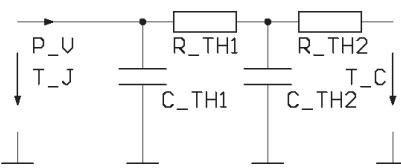
|                          |   | min. | typ. | max.             |
|--------------------------|---|------|------|------------------|
| $R_{\text{pin to chip}}$ |   | 0.6  |      | $\text{m}\Omega$ |
| $C_p$                    | coupling capacity between shorted pins and mounting tab in the case | 160  |      | $\text{pF}$      |
| <b>Weight</b>            | typ.  | 25   |      | g                |

Dimensions in mm (1 mm = 0.0394")



## Equivalent Circuits for Simulation

## Thermal Response



| junction - case (typ.)

$$C_{th1} = 0.039 \text{ J/K}; R_{th1} = 0.28 \text{ K/W}$$