



# SPP1023

## Dual P-Channel Enhancement Mode MOSFET

### DESCRIPTION

The SPP1023 is the Dual P-Channel enhancement mode power field effect transistors are produced using high cell density , DMOS trench technology. This high density process is especially tailored to minimize on-state resistance and provide superior switching performance. These devices are particularly suited for low voltage applications such as notebook computer power management and other battery powered circuits where high-side switching , low in-line power loss, and resistance to transients are needed.

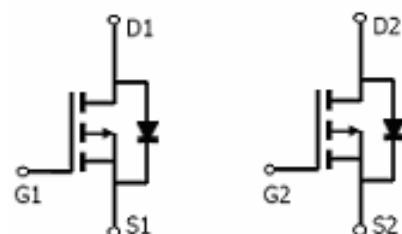
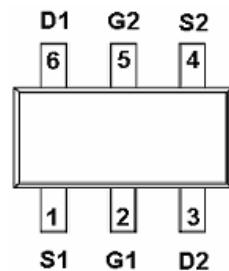
### APPLICATIONS

- Power Management in Note book
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- DSC
- LCD Display inverter

### FEATURES

- ◆ P-Channel
  - 20V/0.45A,R<sub>DS(ON)</sub>= 0.52Ω@V<sub>GS</sub>=-4.5V
  - 20V/0.35A,R<sub>DS(ON)</sub>= 0.70Ω@V<sub>GS</sub>=-2.5V
  - 20V/0.25A,R<sub>DS(ON)</sub>= 0.95Ω@V<sub>GS</sub>=-1.8V
- ◆ Super high density cell design for extremely low RDS (ON)
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ SOT-563 (SC-89-6L) package design

### PIN CONFIGURATION( SOT-563 / SC-89-6L)



p-channel  
PART MARKING

p-channel





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### PIN DESCRIPTION

Pin	Symbol	Description
1	S1	Source 1
2	G1	Gate 1
3	D2	Drain 2
4	S2	Source 2
5	G2	Gate 2
6	D1	Drain1

### ORDERING INFORMATION

Part Number	Package	Part Marking
SPP1023S56RG	SOT-563	A

※ Week Code : A ~ Z( 1 ~ 26 ) ; a ~ z( 27 ~ 52 )

※ SPP1023S56RG : Tape Reel ; Pb – Free

### ABSOULTE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Typical	Unit
Drain-Source Voltage	V <sub>DSS</sub>	-20	V
Gate –Source Voltage	V <sub>GSS</sub>	±12	V
Continuous Drain Current(T <sub>J</sub> =150°C)	T <sub>A</sub> =25°C	ID	A
	T <sub>A</sub> =80°C		
Pulsed Drain Current	I <sub>DM</sub>	-1.0	A
Continuous Source Current(Diode Conduction)	I <sub>S</sub>	-0.3	A
Power Dissipation	T <sub>A</sub> =25°C	P <sub>D</sub>	W
	T <sub>A</sub> =70°C		
Operating Junction Temperature	T <sub>J</sub>	-55/150	°C
Storage Temperature Range	T <sub>STG</sub>	-55/150	°C



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### ELECTRICAL CHARACTERISTICS

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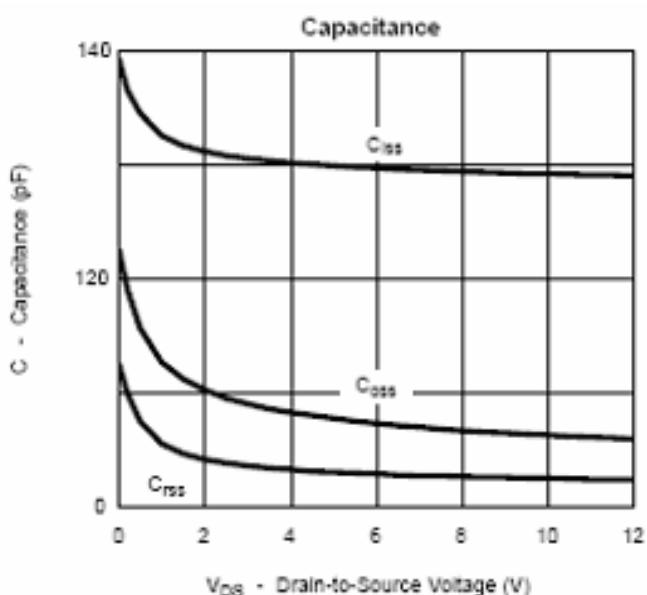
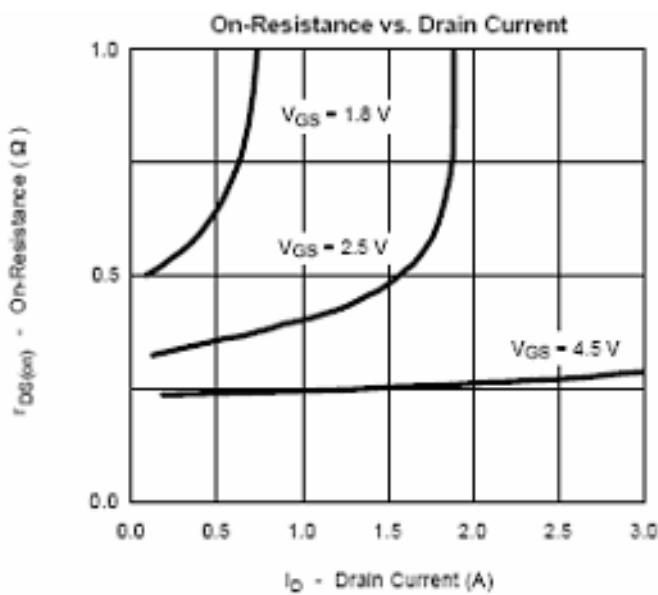
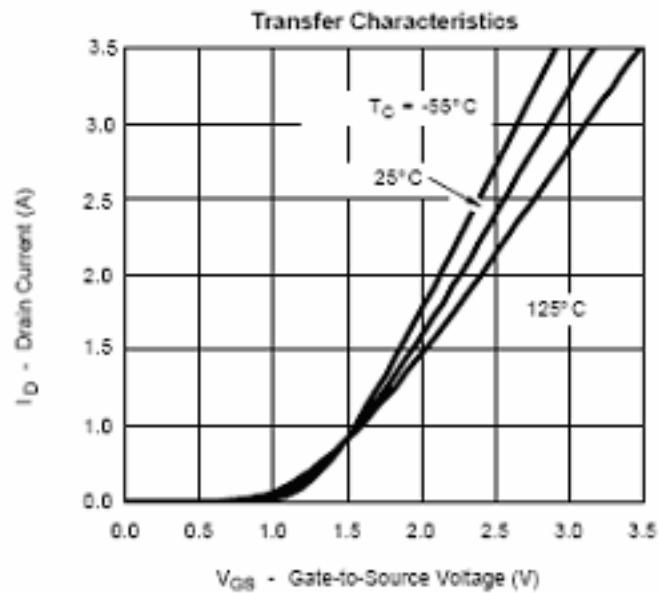
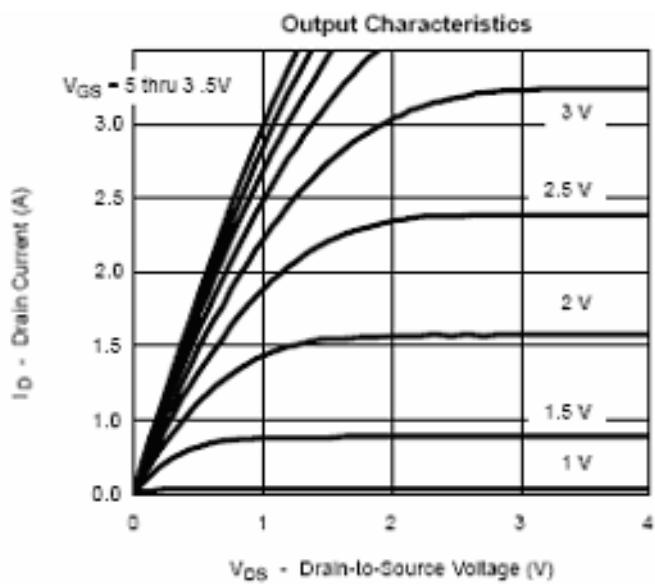
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	V(BR)DSS	VGS=0V, ID=-250uA	-20			V
Gate Threshold Voltage	VGS(th)	VDS=VGS, ID=-250uA	-0.35		-0.8	
Gate Leakage Current	IGSS	VDS=0V, VGS=±12V			±100	nA
Zero Gate Voltage Drain Current	IDSS	VDS=-20V, VGS=0V			-1	uA
		VDS=-20V, VGS=0V TJ=55°C			-5	
On-State Drain Current	ID(on)	VDS≤ -4.5V, VGS =-5V	-0.7			A
Drain-Source On-Resistance	RDS(on)	VGS=-4.5V, ID=-0.45A		0.42	0.52	Ω
		VGS=-2.5V, ID=-0.35A		0.58	0.70	
		VGS=-1.8V, ID=-0.25A		0.75	0.95	
Forward Transconductance	gfs	VDS=-10V, ID=-0.25A		0.4		S
Diode Forward Voltage	VSD	Is=-0.15A, VGS=0V		-0.8	-1.2	V
<b>Dynamic</b>						
Total Gate Charge	Qg	VDS=-10V, VGS=-4.5V , ID ≡-0.6A		1.5	2.0	nC
Gate-Source Charge	Qgs			0.3		
Gate-Drain Charge	Qgd			0.35		
Turn-On Time	td(on)	VDD=-10V, RL=10Ω , ID≡-0.4A VGEN=-4.5V , RG=6Ω		5	10	ns
	tr			15	25	
Turn-Off Time	td(off)			8	15	
	tf			1.4	1.8	



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### TYPICAL CHARACTERISTICS

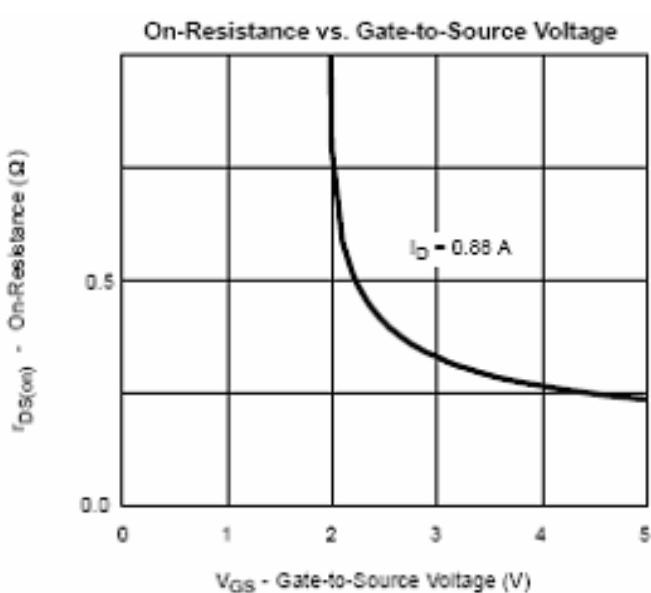
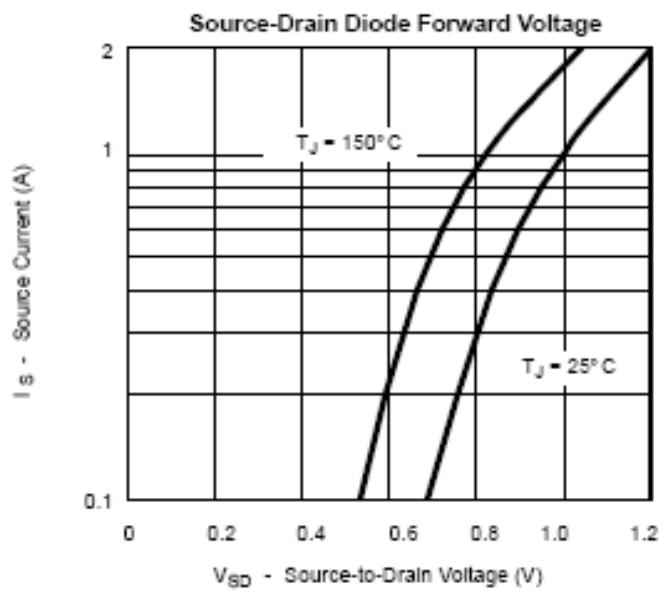
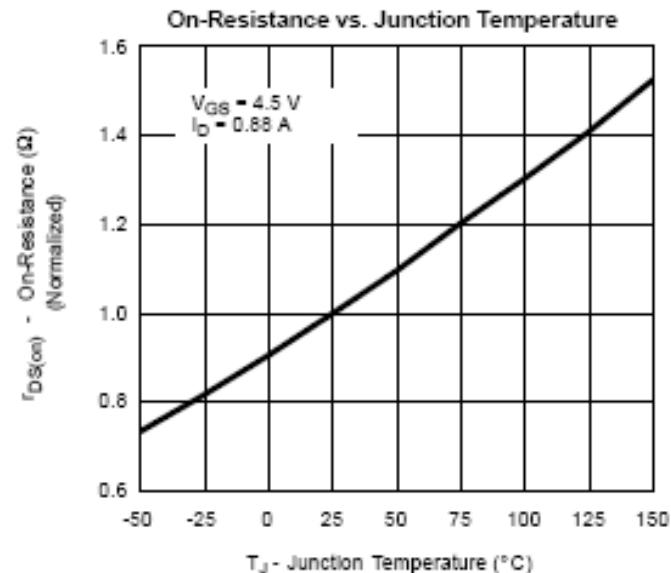
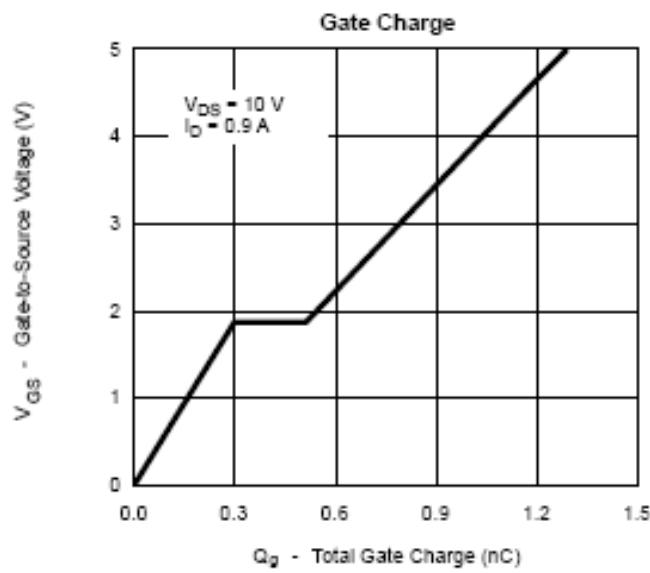




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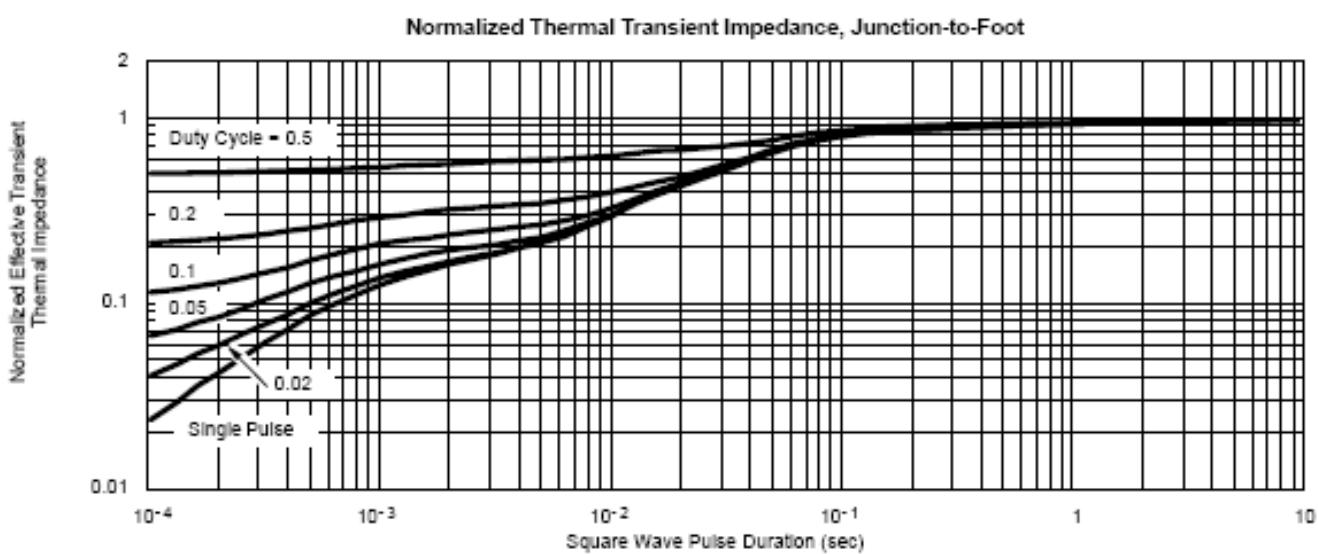
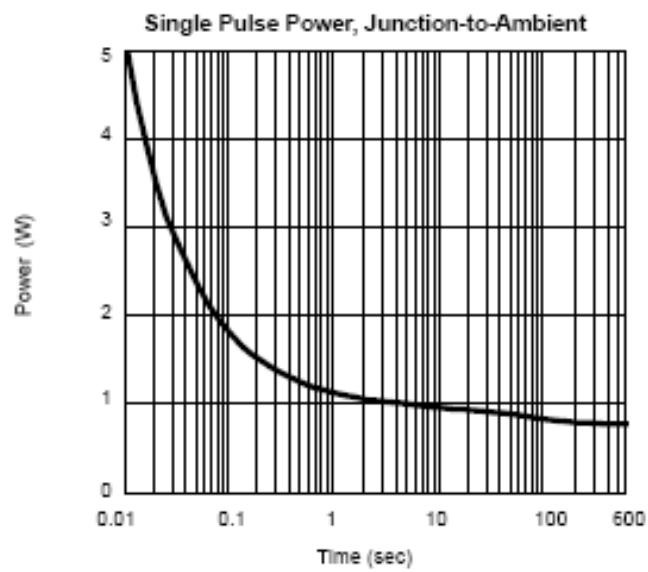
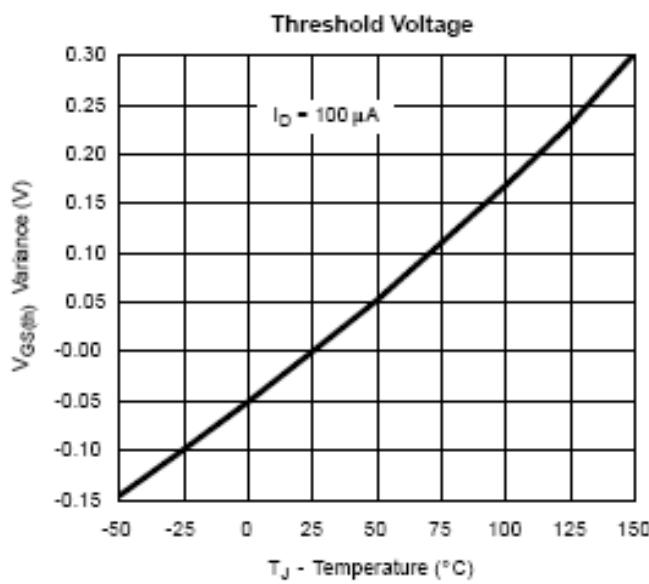




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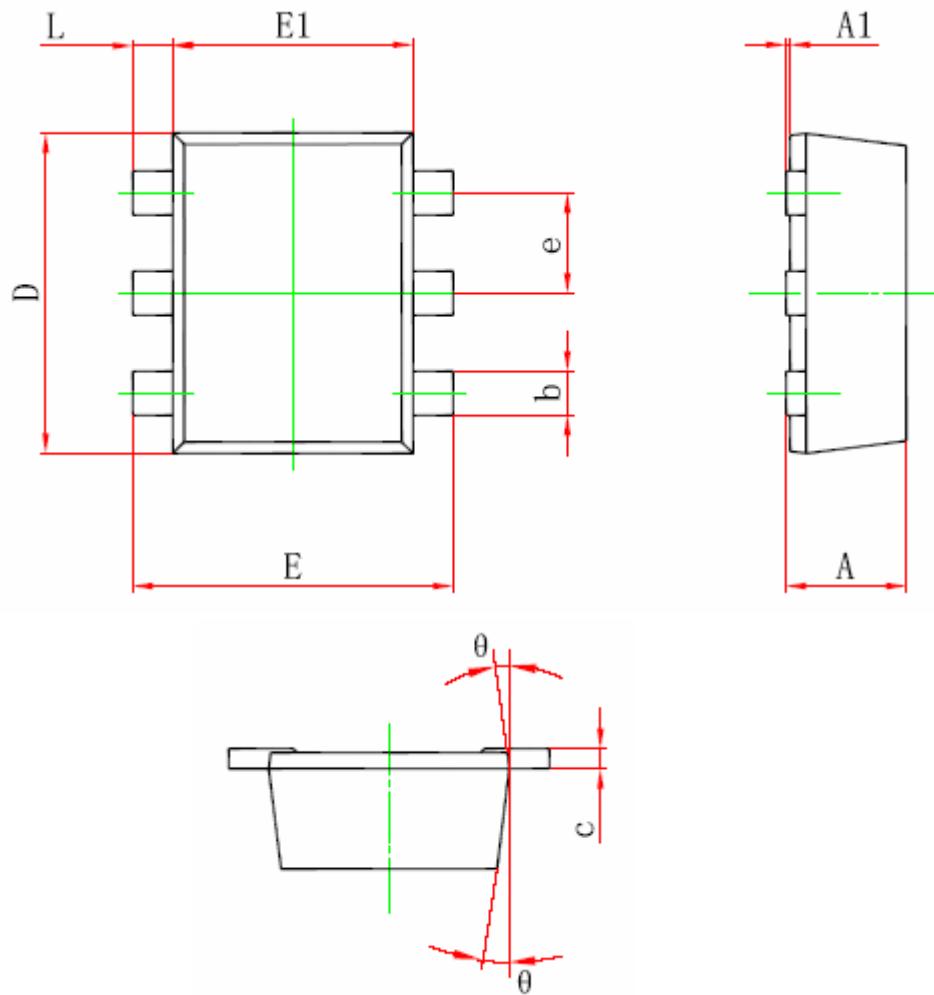




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### SOT-563 PACKAGE OUTLINE



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.525	0.600	0.021	0.024
A1	0.000	0.050	0.000	0.002
e	0.450	0.550	0.018	0.022
c	0.090	0.160	0.004	0.006
D	1.500	1.700	0.059	0.067
b	0.170	0.270	0.007	0.011
E1	1.100	1.300	0.043	0.051
E	1.500	1.700	0.059	0.067
L	0.100	0.300	0.004	0.012
$\theta$	$7^{\circ}$ REF.		$7^{\circ}$ REF.	



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