

4V Drive Pch MOSFET

RW1E015RP

Structure

Silicon P-channel MOSFET

Features

- 1) Low on-resistance.
- 2) Space saving, high power package.
- 3) Low voltage drive. (4V)

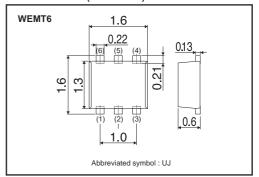
Applications

Switching

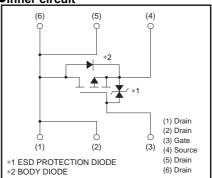
Packaging specifications

	Package	Taping		
Type	Code	T2R		
	Basic ordering unit (pieces)	8000		
RW1E015R	0			

●Dimensions (Unit : mm)







●Absolute maximum ratings (Ta=25°C)

Parameter		Symbol		Limits	Unit
Drain-source voltage		V _{DSS}		-30	V
Gate-source voltage		Vgss		±20	V
Drain current	Continuous	ΙD		±1.5	Α
	Pulsed	I_{DP}	*1	±6	Α
Source current	Continuous	Is		-0.5	Α
(Body diode)	Pulsed	Isp	*1	-6	Α
Total power dissipation		PD	*2	0.7	W
Channel temperature		Tch		150	°C
Range of Storage temperature		Tstg		-55 to +150	°C

^{*1} Pw≤10μs, Duty cycle≤1%

●Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	Rth(ch-a) *	179	°C/W

^{*} When mounted on a ceramic board.

^{*2} When mounted on a ceramic board

RW1E015RP Data Sheet

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Gate-source leakage	Igss	_	_	±10	μΑ	V _{GS} =±20V, V _{DS} =0V	
Drain-source breakdown voltage	V _{(BR) DSS}	-30	-	_	V	I _D = -1mA, V _G S=0V	
Zero gate voltage drain current	I _{DSS}	_	-	-1	μΑ	V _{DS} = -30V, V _{GS} =0V	
Gate threshold voltage	V _{GS (th)}	-1.0	-	-2.5	V	V _{DS} = -10V, I _D = -1mA	
Static drain-source on-state resistance		-	115	160	mΩ	I _D = -1.5A, V _G S= -10V	
	RDS (on)*	-	170	240	mΩ	ID= -0.7A, VGS= -4.5V	
		-	190	270	mΩ	I _D = -0.7A, V _G S= -4V	
Forward transfer admittance	Y _{fs} *	1.2	-	_	S	V _{DS} = -10V, I _D = -1.5A	
Input capacitance	Ciss	_	230	_	pF	V _{DS} = -10V	
Output capacitance	Coss	_	40	_	pF	Vgs=0V	
Reverse transfer capacitance	Crss	_	33	_	pF	f=1MHz	
Turn-on delay time	t _{d (on)} *	_	12	_	ns	V _{DD} ≒ −15V	
Rise time	tr *	_	8	_	ns	ID= -0.7A	
Turn-off delay time	td (off) *	_	40	_	ns	Vgs= −10V Ri≒ 21.4Ω	
Fall time	t _f *	_	13	_	ns	R _G =10Ω	
Total gate charge	Qg *	_	3.2	-	nC	V _{DD} ≒−15V R _L ≒10Ω	
Gate-source charge	Q _{gs} *	_	1.2	_	nC	I _D = -1.5A R _G =10Ω	
Gate-drain charge	Qgd *	_	0.7	_	nC	V _{GS} = -5V	

^{*}Pulsed

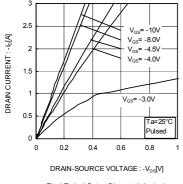
●Body diode characteristics (Source-drain) (Ta=25°C)

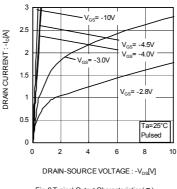
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsp *	_	_	-1.2	V	I _S = -1.5A, V _{GS} =0V

^{*}Pulsed

RW1E015RP **Data Sheet**

•Electrical characteristics curves





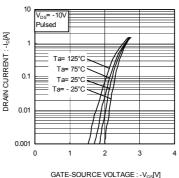
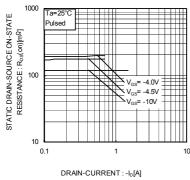
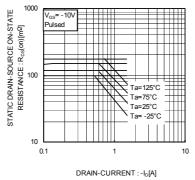


Fig.1 Typical Output Characteristics(I)

Fig.2 Typical Output Characteristics(II)

Fig.3 Typical Transfer Characteristics





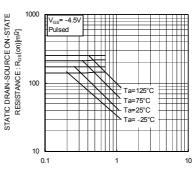
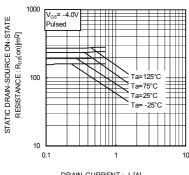


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current(I)

Fig.5 Static Drain-Source On-State Resistance vs. Drain Current(II)

DRAIN-CURRENT : -I_D[A] Fig.6 Static Drain-Source On-State Resistance vs. Drain Current(III)



DRAIN-CURRENT : -I_D[A] Fig.7 Static Drain-Source On-State Resistance vs. Drain Current(TV)

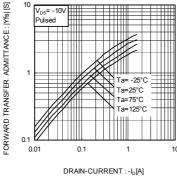


Fig.8 Forward Transfer Admittance vs. Drain Current

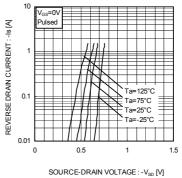
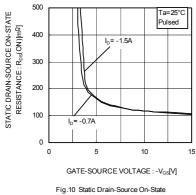
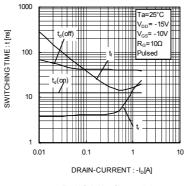
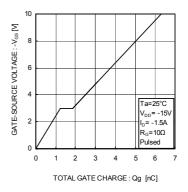


Fig.9 Reverse Drain Current vs. Sourse-Drain Voltage

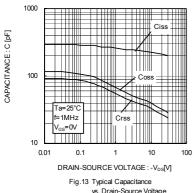






Static Drain-Source On-State Fig.11 Switching Characteristics Resistance vs. Gate Source Voltage

Fig.12 Dynamic Input Characteristics



vs. Drain-Source Voltage

●Measurement circuit

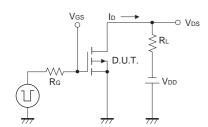


Fig.1-1 Switching Time Measurement Circuit

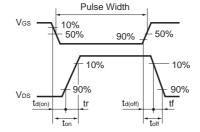


Fig.1-2 Switching Waveforms

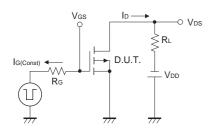


Fig.2-1 Gate Charge Measurement Circuit

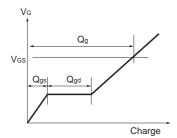


Fig.2-2 Gate Charge Waveform

Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

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