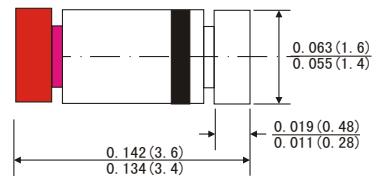


## FEATURES

- For general purpose applications
- Metal-on-silicon junction Schottky barrier device which is protected by a PN junction guard ring. The low forward voltage drop and fast switching make it ideal for protection of MOS devices, steering, biasing and coupling diodes for fast switching and low logic level applications
- These diodes are also available in the DO-35 case with type designation 1N5711 and 1N6263.
- High temperature soldering guaranteed: 260°C/10 seconds at terminals
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

### MiniMELF



## MECHANICAL DATA

Dimensions in inches and (millimeters)

- Case: MiniMELF glass case(SOD-80 )
- Weight: Approx. 0.05 gram

## ABSOLUTE RATINGS(LIMITING VALUES)

		Symbols	Value	Units
Peak Reverse Voltage	LL5711 LL6263	V <sub>RRM</sub> V <sub>RRM</sub>	70 60	V V
Power Dissipation (infinite Heat Sink)		P <sub>tot</sub>	400 <sup>1)</sup>	mW
Maximum Single cycle surge 10μs square wave		I <sub>FSM</sub>	2.0	A
Junction temperature		T <sub>J</sub>	125	°C
Storage Temperature Range		T <sub>STG</sub>	-55 to+150	°C

1) Valid provided that electrodes are kept at ambient temperature

## ELECTRICAL CHARACTERISTICS

(Ratings at 25°C ambient temperature unless otherwise specified)

		Symbols	Min.	Typ.	Max.	Units
Reverse breakdown voltage	LL5711 LL6263	V <sub>R</sub> V <sub>R</sub>	70 60			V V
Leakage current at V <sub>R</sub> =50V		I <sub>R</sub>			200	nA
Forward voltage drop at I <sub>F</sub> =1mA I <sub>F</sub> =15mA		V <sub>F</sub> V <sub>F</sub>			0.41 1.0	V V
Junction Capacitance at V <sub>R</sub> =0V ,f=1MHz		C <sub>J</sub>			2.0	pF
Reverse Recovery time at I <sub>F</sub> =I <sub>R</sub> =5mA,recover to 0.1 I <sub>R</sub>		t <sub>rr</sub>			1	ns
Thermal resistance		R <sub>θJA</sub>			300	K/W

# RATINGS AND CHARACTERISTICS CURVES LL5711 & LL6263

Fig.1 Typical variation of fwd. current vs forward. voltage for primary conduction through the Schottky barrier

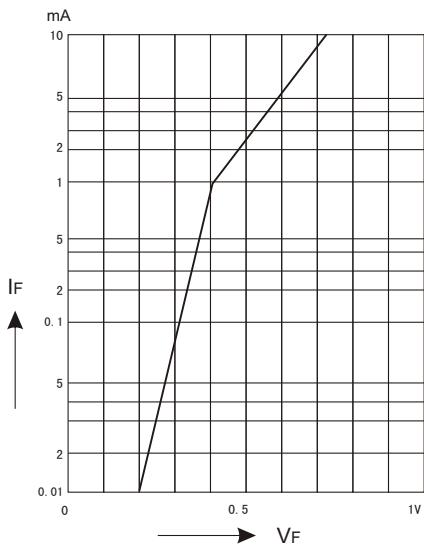


Fig.2 Typical forward conduction curve of combination Schottky barrier and PN junction guard ring

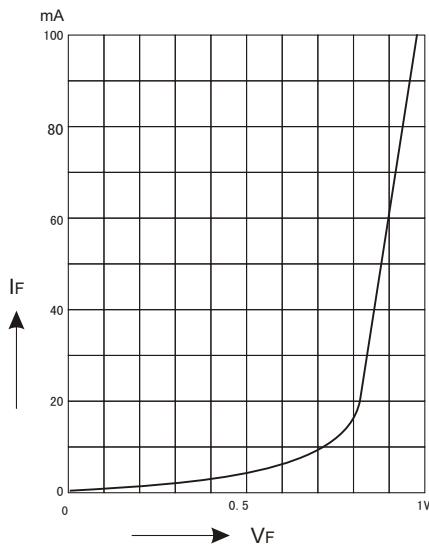


Fig.3 Typical variation of reverse current at various temperatures

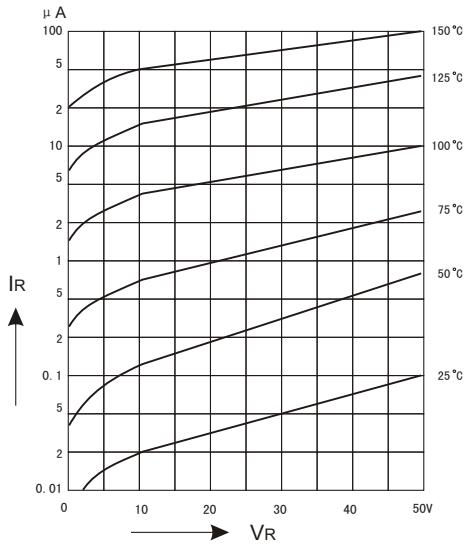


Fig.4 Typical capacitance curve as a function of reverse voltage

