

SANYO Semiconductors DATA SHEET

P-Channel Silicon MOSFET

3LP03SS — General-Purpose Switching Device **Applications**

Features

- · Low ON-resistance.
- · High-speed switching.
- · 2.5V drive.
- High resistance to damage from ESD (TYP 300V) [with a protection diode connected between the gate and source].

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		-30	V
Gate-to-Source Voltage (*1)	VGSS		-10	V
Drain Current (DC)	ID		-0.25	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	-1	Α
Allowable Power Dissipation	PD		0.15	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

^{(*1):} Note, when designing a circuit using this product, that it has a gate (oxide film) protection diode connected only between its gate and source.

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=-1mA, VGS=0V	-30			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =-30V, V _{GS} =0V			-1	μΑ
Gate-to-Source Leakage Current	IGSS	VGS=-8V, VDS=0V			-1	μΑ
Cutoff Voltage	VGS(off)	V _{DS} =-10V, I _D =-100μA	-0.4		-1.4	V
Forward Transfer Admittance	yfs	V _{DS} =-10V, I _D =-120mA	0.24	0.4		S
Static Drain-to-Source On-State Resistance	RDS(on)1	ID=-120mA, VGS=-4V		1.5	1.9	Ω
	R _{DS} (on)2	ID=-60mA, VGS=-2.5V		2.0	2.8	Ω
	R _{DS} (on)3	ID=-10mA, VGS=-1.5V		4.0	8.0	Ω
Input Capacitance	Ciss	VDS=-10V, f=1MHz		40		pF
Output Capacitance	Coss	V _{DS} =-10V, f=1MHz		8		pF
Reverse Transfer Capacitance	Crss	V _{DS} =-10V, f=1MHz		4.5		pF

Marking: XG Continued on next page.

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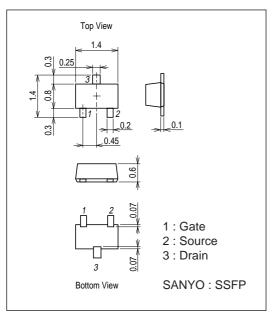
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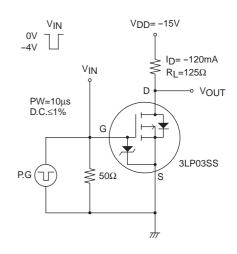
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Uill
Turn-ON Delay Time	td(on)	See specified Test Circuit.		9.5		ns
Rise Time	t _r	See specified Test Circuit.		5		ns
Turn-OFF Delay Time	td(off)	See specified Test Circuit.		15		ns
Fall Time	tf	See specified Test Circuit.		13		ns
Total Gate Charge	Qg	V _{DS} =-10V, V _{GS} =-4V, I _D =-250mA		0.8		nC
Gate-to-Source Charge	Qgs	V _{DS} =-10V, V _{GS} =-4V, I _D =-250mA		0.3		nC
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =-10V, V _{GS} =-4V, I _D =-250mA		0.2		nC
Diode Forward Voltage	V _{SD}	IS=-250mA, VGS=0V	·	-0.9	-1.2	V

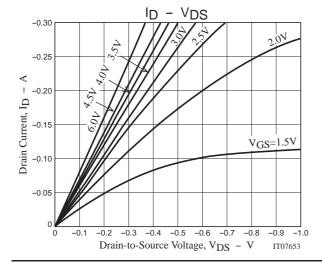
Package Dimensions

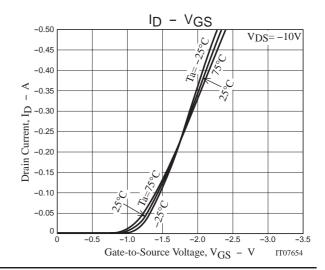
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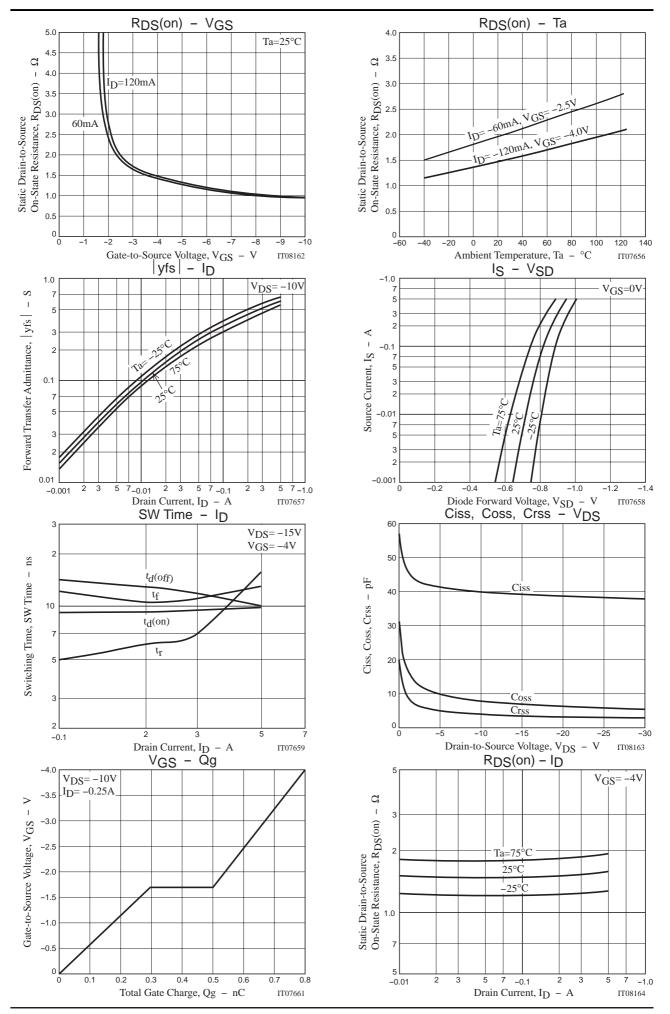


Switching Time Test Circuit

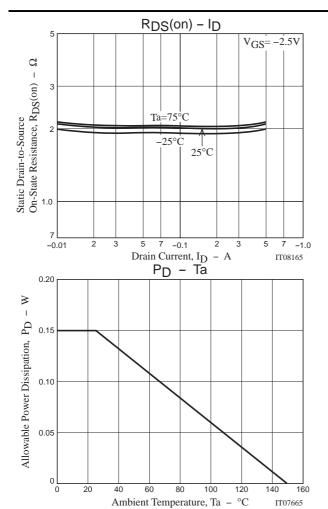


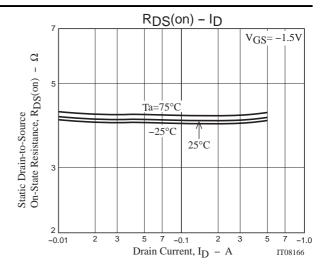






3LP03SS





Note on usage: Since the 3LP03SS is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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