

Single N-channel MOSFET

ELM33402CA-S

■ General description

ELM33402CA-S uses advanced trench technology to provide excellent $R_{ds(on)}$, low gate charge and low gate resistance.

■ Features

- $V_{ds}=20V$
- $I_d=3A$
- $R_{ds(on)} < 75m\Omega$ ($V_{gs}=4.5V$)
- $R_{ds(on)} < 105m\Omega$ ($V_{gs}=2.5V$)

■ Maximum absolute ratings

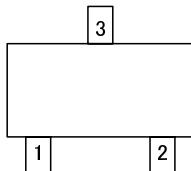
Parameter	Symbol	Limit	Unit	Note
Gate-source voltage	V_{gs}	± 12	V	
Continuous drain current	I_d	3	A	
		2		
Pulsed drain current	I_{dm}	20	A	3
Power dissipation	P_d	0.6	W	
		0.5		
Junction and storage temperature range	T_j, T_{stg}	-55 to 150	°C	

■ Thermal characteristics

Parameter		Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-case	Steady-state	$R\theta_{jc}$		65	°C/W	
Maximum junction-to-ambient	Steady-state	$R\theta_{ja}$		230	°C/W	

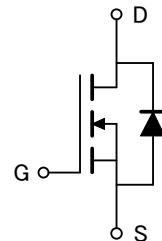
■ Pin configuration

SOT-23 (TOP VIEW)



Pin No.	Pin name
1	GATE
2	SOURCE
3	DRAIN

■ Circuit



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■ Electrical characteristics

$T_a=25^\circ\text{C}$

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
STATIC PARAMETERS							
Drain-source breakdown voltage	BVdss	$\text{Id}=250\ \mu\text{A}, \text{Vgs}=0\text{V}$	20			V	
Zero gate voltage drain current	Idss	$\text{Vds}=16\text{V}, \text{Vgs}=0\text{V}$ $\text{Vds}=16\text{V}, \text{Vgs}=0\text{V}, \text{T}_j=125^\circ\text{C}$		1	10	μA	
Gate-body leakage current	Igss	$\text{Vds}=0\text{V}, \text{Vgs}=\pm 12\text{V}$			± 100	nA	
Gate threshold voltage	Vgs(th)	$\text{Vds}=\text{Vgs}, \text{Id}=250\ \mu\text{A}$	0.45	0.75	1.20	V	
On state drain current	Id(on)	$\text{Vgs}=10\text{V}, \text{Vds}=10\text{V}$	3			A	1
Static drain-source on-resistance	Rds(on)	$\text{Vgs}=4.5\text{V}, \text{Id}=3\text{A}$ $\text{Vgs}=2.5\text{V}, \text{Id}=1.5\text{A}$		50	75	$\text{m}\Omega$	1
Forward transconductance	Gfs	$\text{Vds}=15\text{V}, \text{Id}=3\text{A}$		16		S	
Diode forward voltage	Vsd	$\text{If}=\text{Is}, \text{Vgs}=0\text{V}$			1.5	V	1
Max. body-diode continuous current	Is				2.3	A	
Pulsed body-diode current	Ism				4.6	A	3
DYNAMIC PARAMETERS							
Input capacitance	Ciss	$\text{Vgs}=0\text{V}, \text{Vds}=15\text{V}, f=1\text{MHz}$		450		pF	
Output capacitance	Coss			200		pF	
Reverse transfer capacitance	Crss			60		pF	
SWITCHING PARAMETERS							
Total gate charge	Qg	$\text{Vgs}=10\text{V}, \text{Vds}=10\text{V}, \text{Id}=3\text{A}$		15.0		nC	2
Gate-source charge	Qgs			2.0		nC	2
Gate-drain charge	Qgd			7.0		nC	2
Turn-on delay time	td(on)	$\text{Vgs}=10\text{V}, \text{Vds}=15\text{V}, \text{Id} \approx 1\text{A}$ $\text{R}_{\text{gen}}=2.5\ \Omega$		6.0		ns	2
Turn-on rise time	tr			6.0		ns	2
Turn-off delay time	td(off)			20.0		ns	2
Turn-off fall time	tf			5.0		ns	2

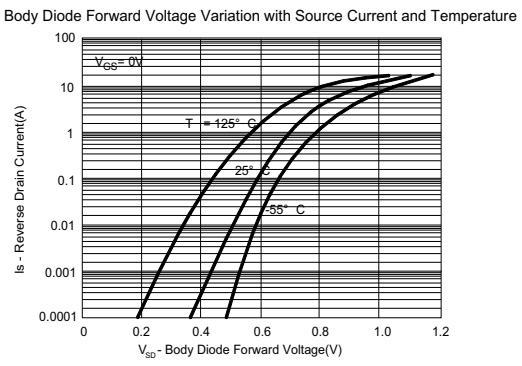
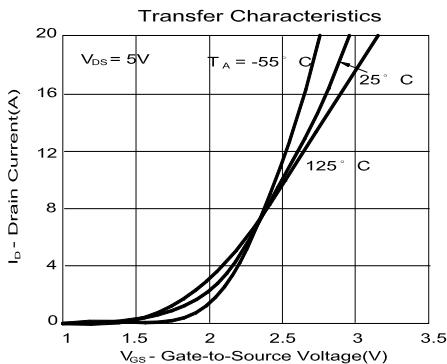
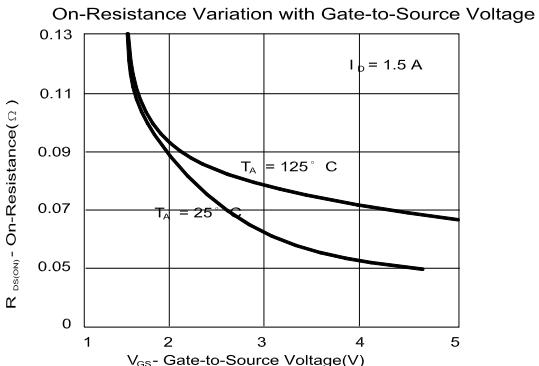
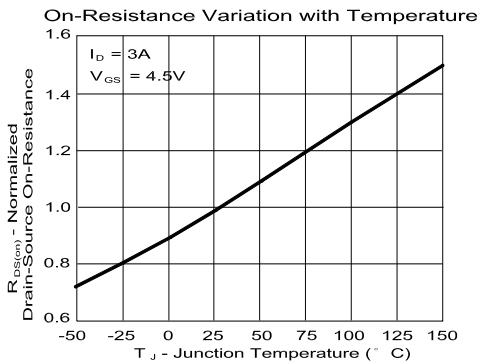
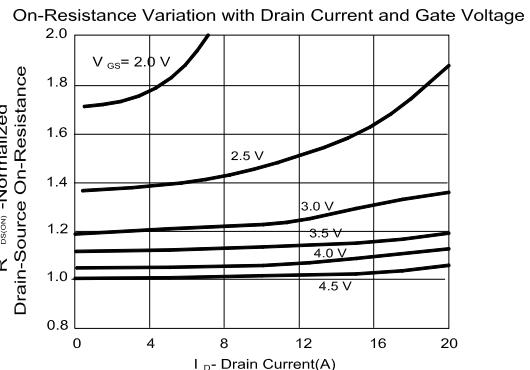
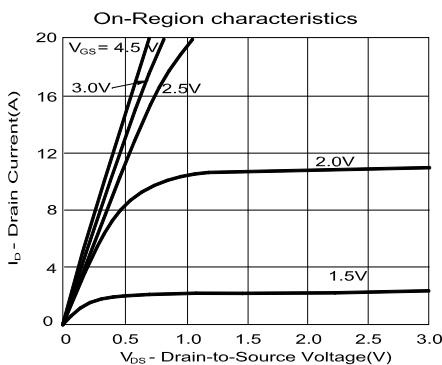
NOTE :

1. Pulse test : Pulsed width $\leq 300\ \mu\text{sec}$ and Duty cycle $\leq 2\%$.
2. Independent of operating temperature.
3. Pulsed width limited by maximum junction temperature.
4. Duty cycle $\leq 1\%$.

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■ Typical electrical and thermal characteristics



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