

RKZ6.8Z4MFAKT

Silicon Planar Zener Diode for Surge Absorption

REJ03G1351-0100

Rev.1.00

Feb 22, 2006

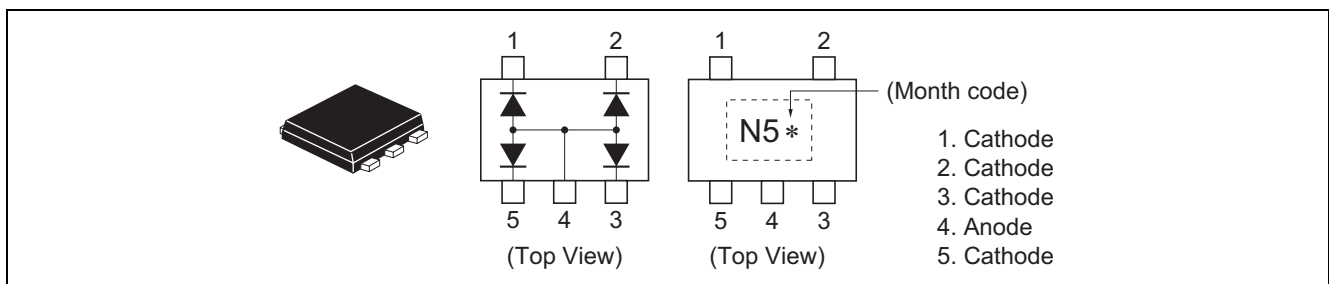
Features

- RKZ6.8Z4MFAKT has four devices in a monolithic, and can absorb surge.
- Low capacitance ($C = 4.0 \text{ pF Typ}$) and can protect ESD of signal line.
- VSON-5 Package is suitable for high density surface mounting.

Ordering Information

Type No.	Laser Mark	Package Name	Package Code
RKZ6.8Z4MFAKT	N5	VSON-5	PUSN0005ZB-A

Pin Arrangement



Month Code

Month of Manufacture	Assemble	
	JAPAN	MALAYSIA
January	A	1
February	B	2
March	C	3
April	D	4
May	E	5
June	F	6

Month of Manufacture	Assemble	
	JAPAN	MALAYSIA
July	G	7
August	H	8
September	J	9
October	K	W
November	L	X
December	M	Y

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Power dissipation	Pd *	150	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

Note: Four device total, See Fig.2.

Electrical Characteristics *¹

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Zener voltage	V _Z	6.47	—	7.00	V	I _Z = 5 mA, 40 ms pulse
Reverse current	I _R	—	—	2	μA	V _R = 3.5 V
Capacitance	C	—	4.0	4.5	pF	V _R = 0 V, f = 1 MHz
Dynamic resistance	r _d	—	—	30	Ω	I _Z = 5 mA
ESD-Capability * ² , * ³	—	8	—	—	kV	C = 150 pF, R = 330 Ω, Both forward and reverse direction 10 pulse

Notes: 1. Per one device.

2. Failure criterion ; I_R > 2 μA at V_R = 3.5 V.

3. Between cathode and anode.

Main Characteristic

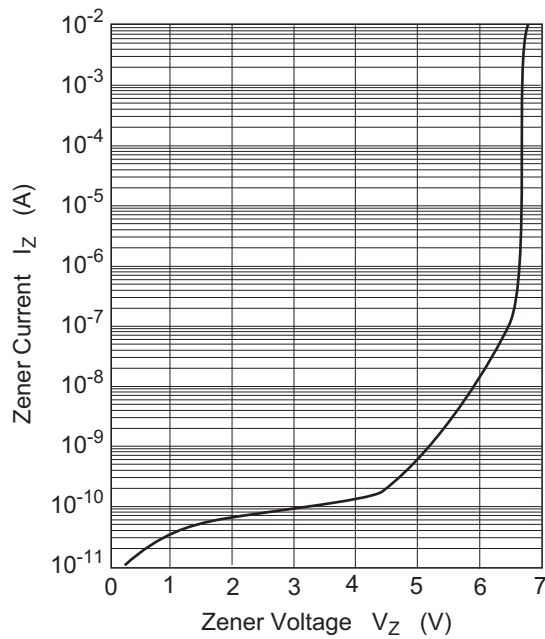


Fig.1 Zener current vs. Zener voltage

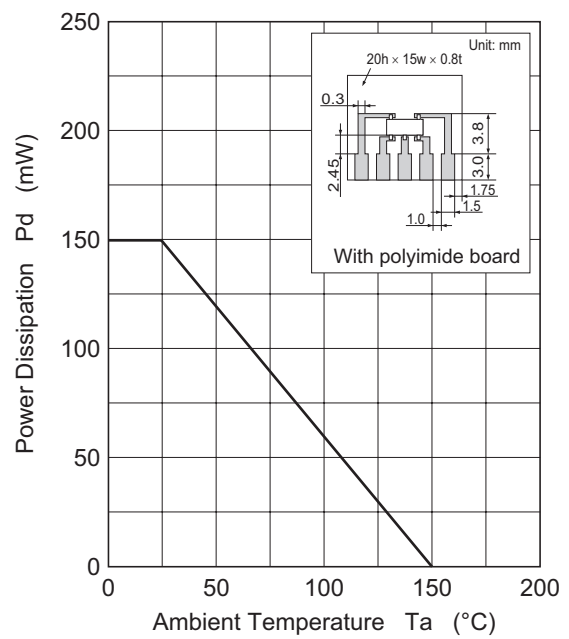


Fig.2 Power Dissipation vs. Ambient Temperature

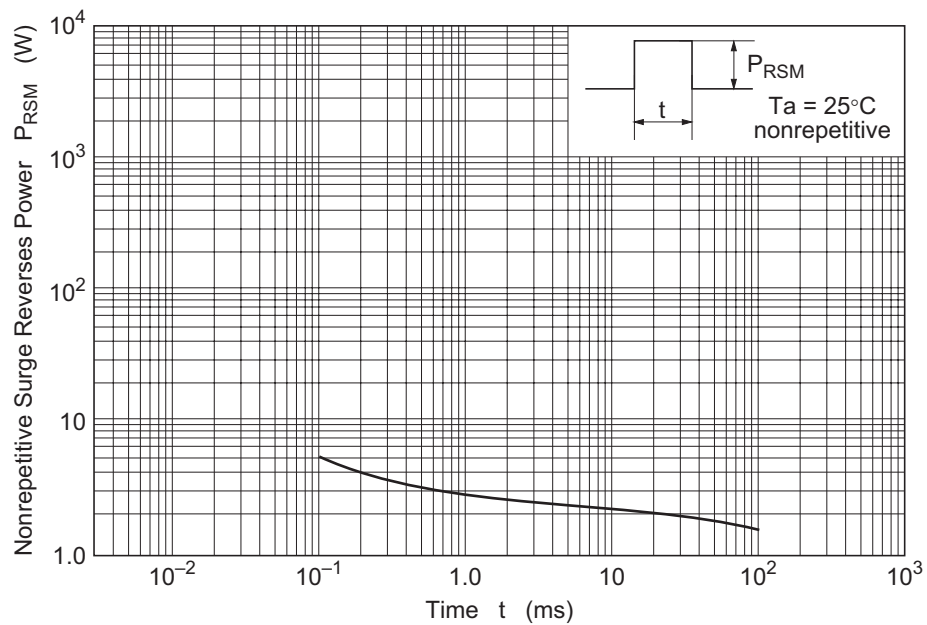
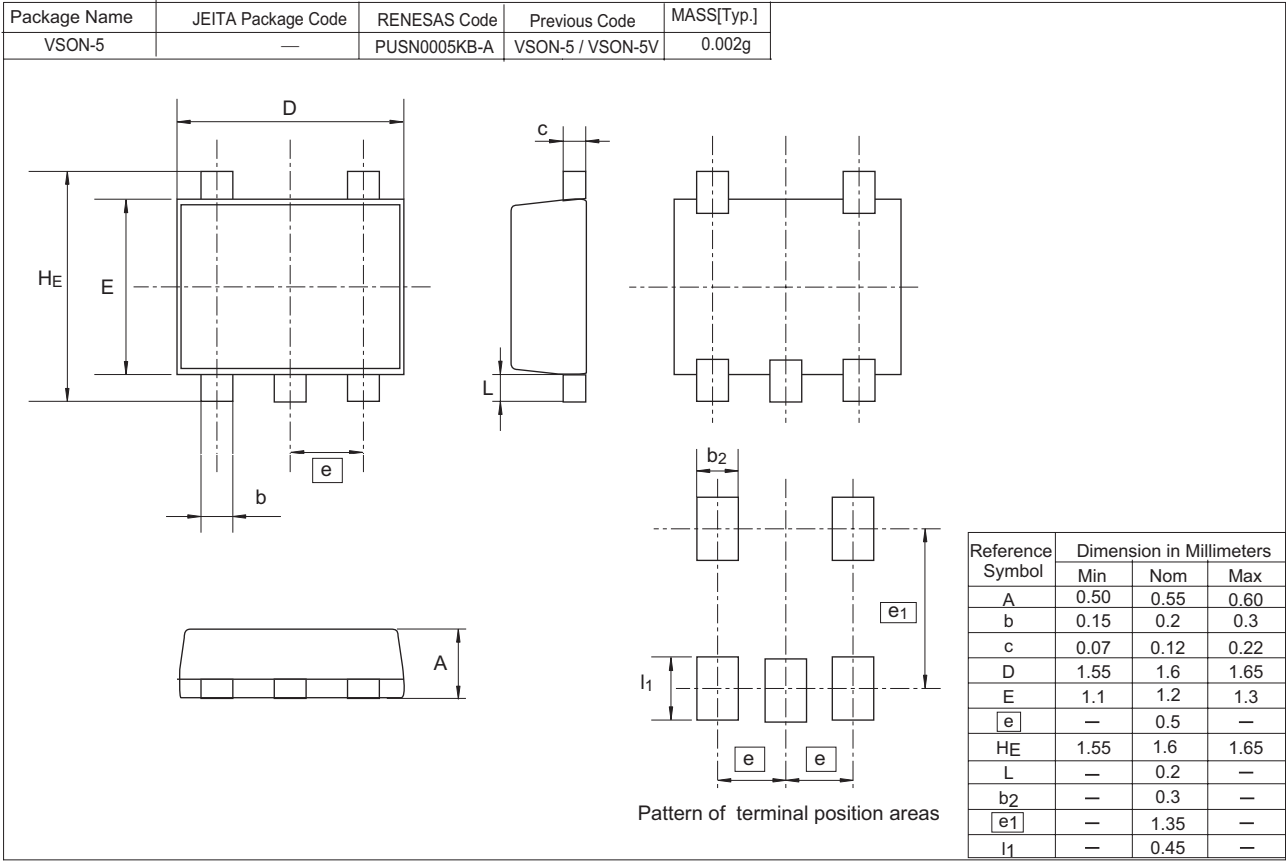


Fig.3 Surge Reverse Power Ratings

Package Dimensions



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Renesas Technology America, Inc.

450 Holger Way, San Jose, CA 95134-1368, U.S.A
Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited

Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology (Shanghai) Co., Ltd.

Unit 204, 205, AZIACenter, No.1233 Lujiazui Ring Rd, Pudong District, Shanghai, China 200120
Tel: <86> (21) 5877-1818, Fax: <86> (21) 6887-7898

Renesas Technology Hong Kong Ltd.

7th Floor, North Tower, World Finance Centre, Harbour City, 1 Canton Road, Tsimshatsui, Kowloon, Hong Kong
Tel: <852> 2265-6688, Fax: <852> 2730-6071

Renesas Technology Taiwan Co., Ltd.

10th Floor, No.99, Fushing North Road, Taipei, Taiwan
Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology Singapore Pte. Ltd.

1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: <65> 6213-0200, Fax: <65> 6278-8001

Renesas Technology Korea Co., Ltd.

Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea
Tel: <82> (2) 796-3115, Fax: <82> (2) 796-2145

Renesas Technology Malaysia Sdn. Bhd

Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jalan Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: <603> 7955-9390, Fax: <603> 7955-9510