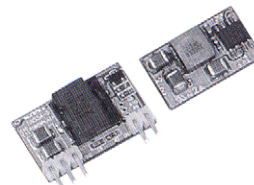


Features

- Delivers 5A of output current
- High Efficiency
- Low output ripple and noise
- Small size and low profile
- Cost-efficient open frame design
- Single-in-line (SIP) & SMT package
- Remote ON/OFF (active low)
- Trimmable output voltage via external resistor
- Output overcurrent protection
- Over temperature protection
- Wide operating temperature -40°C~85°C
- Non-isolated



Model Number	Input Voltage	Output Voltage	Output Current	Max. Power	Ripple & Noise max	MTBF	Efficiency typ.
VSNS5-5-1R0	3.0-5.5V	1.0V	5A	5.0W	30mVp-p	1.5x10 ⁶ hrs	82%
VSNS5-5-1R5	3.0-5.5V	1.5V	5A	7.5W	30mVp-p	1.5x10 ⁶ hrs	87%
VSNS5-5-1R8	3.0-5.5V	1.8V	5A	9.0W	30mVp-p	1.5x10 ⁶ hrs	88%
VSNS5-5-2R0	3.0-5.5V	2.0V	5A	10.0W	30mVp-p	1.5x10 ⁶ hrs	90%
VSNS5-5-2R5	3.0-5.5V	2.5V	5A	12.0W	30mVp-p	1.5x10 ⁶ hrs	92%
VSNS5-5-3R3	4.5-5.5V	3.3V	5A	16.5W	30mVp-p	1.5x10 ⁶ hrs	94%
VSNS5-12-1R0	10-14V	1.0V	5A	5.0W	50mVp-p	1.5x10 ⁶ hrs	81%
VSNS5-12-1R5	10-14V	1.5V	5A	7.5W	50mVp-p	1.5x10 ⁶ hrs	84%
VSNS5-12-1R8	10-14V	1.8V	5A	9.0W	50mVp-p	1.5x10 ⁶ hrs	85%
VSNS5-12-2R5	10-14V	2.5V	5A	12.0W	50mVp-p	1.5x10 ⁶ hrs	87%
VSNS5-12-3R3	10-14V	3.3V	5A	16.5W	50mVp-p	1.5x10 ⁶ hrs	89%
VSNS5-12-5R0	10-14V	5.0V	5A	25.0W	50mVp-p	1.5x10 ⁶ hrs	92%

Notes:

1. Typical at Ta+ +25°C under nominal line voltage and full load conditions, unless other wise noted. All models are tested and specified with external input and output capacitors. (Cin=100uFx2 Cout=10uF), These capacitors are necessary to accommodate out test equipment.
2. Ripple and Noise are tested/specified over a 20MHz bandwidth and may be reduced with external filtering.
3. This device has no minimum load requirement and will regulate under no-load conditions. The regulation part of the specification describes the output voltage deviation as the line voltage or load is varied from its minimum value to either extreme .

Electrical Specifications

Voltage tolerance	±2.0%
Line regulation	±0.3%
Load regulation	±0.4%
Switching frequency	300KHz
Dimensions	22.9x13.9x8.6mm (SIP package)

Note: Suffix V is for SIP packaging
 Suffix H is for SMT packaging

Drawings

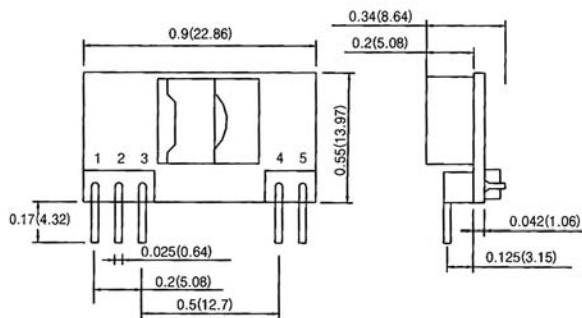
Pin assignment

Single Output (SIP)

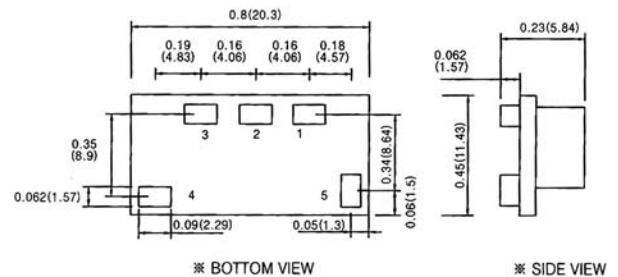
1. +Vout
2. Trim
3. COM
4. +Vin
5. ON/OFF

Single Output (SMT)

1. +Vout
2. Trim
3. COM
4. +Vin
5. ON/OFF



<SIP TYPE>



※ BOTTOM VIEW

※ SIDE VIEW

<SMT TYPE>

NOTES

1. All dimensions are in inches (mm)
2. Weight : 3.5g or less (SIP)
2.4g or less (SMT)

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