

Composite Transistor For Muting Application Silicon NPN Epitaxial Type

DESCRIPTION

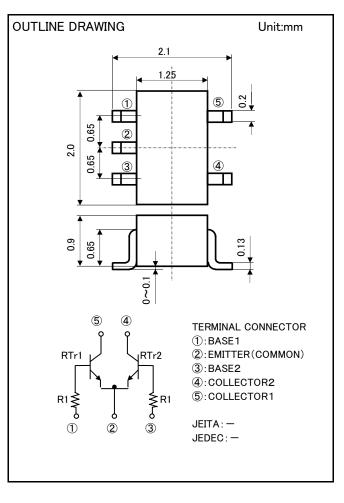
RT2N65M is a composite transistor with built-in bias resistor

FEATURE

- •Built-in bias resistor (R1=10 K Ω)
- Mini package for easy mounting

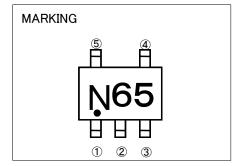
APPLICATION

muting circuit, switching circuit



MAXIMUM RATINGS (Ta=25°C) (RTr1, RTr2)

Symbol	Parameter	Ratings	Unit
V _{CBO}	Collector to Base voltage	40	V
V_{EBO}	Emitter to Base voltage	40	V
V _{CEO}	Collector to Emitter voltage	20	V
Ι _c	Collector current	400	mA
Pc	Collector dissipation(Total Ta=25°C)	150	mW
T _j	Junction temperature	+150	°C
T _{stg}	Storage temperature	-55~+150	°C

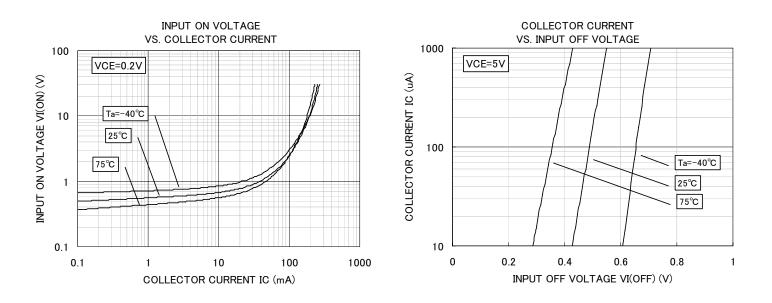


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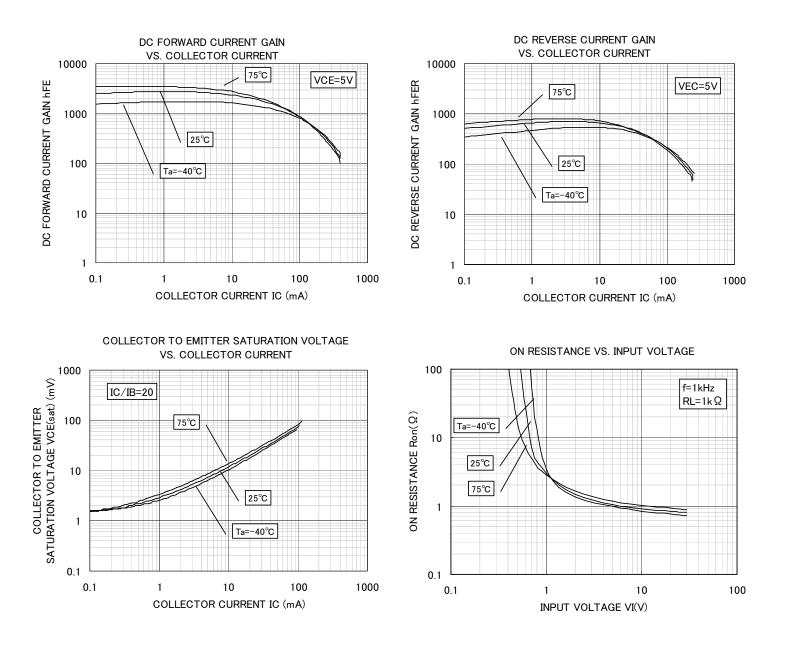
Electrical characteristics (Ta=25°C)

Symbol	Parameter	Test conditions		Limits		
			Min	Тур	Max	Unit
V _{CBO}	Collector-base breakdown voltage	Iο=50 μ A , Iε=0mA	40			V
Vebo	Emitter-base breakdown voltage	Iε=50 μ A , c=0mA	40			V
Vceo	Collector-emitter breakdown voltage	Ic=1mA , R _{BE} =∞	20			V
Ісво	Collector cutoff current	V _{CB} =40V , I _E =0mA			0.5	μA
Iево	Emitter cutoff current	V _{EB} =40V , Ic=0mA			0.5	μA
hfe	DC current transfer ratio	V _{CE} =5V , Ic=-10mA	820		2500	-
VCE(sat)	Collector-emitter saturation voltage	Ic=10mA , I _B =0.5mA		10		mV
R1	Input resistance	-	7	10	13	КΩ
fT	Transition frequency	V _{CE} =10V, I _E =-10mA, f=100MHz		35		MHz
Ron	Output On-resistance	V _i =7V, f=1MHz		0.94		Ω

TYPICAL CHARACTERISTICS (Tr1, Tr2)







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