

H7N0401LD, H7N0401LS, H7N0401LM

Silicon N Channel MOS FET High Speed Power Switching

REJ03G1129-0500

(Previous: ADE-208-1527C)

Gate
 Drain
 Source
 Drain

Rev.5.00

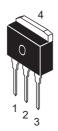
Apr 07, 2006

Features

- Low on-resistance $R_{DS (on)} = 3.1 \text{ m}\Omega \text{ typ.}$
- 4.5 V gate drive devices
- High Speed Switching

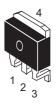
Outline

RENESAS Package code: PRSS0004AE-A (Package name: LDPAK (L))



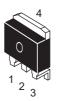
H7N0401LD

RENESAS Package code: PRSS0004AE-C (Package name: LDPAK (S)-(2))

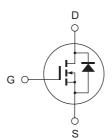


H7N0401LM

RENESAS Package code: PRSS0004AE-B (Package name: LDPAK (S)-(1))



H7N0401LS



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Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit	
Drain to source voltage	V _{DSS}	40	V	
Gate to source voltage	V _{GSS}	±20	V	
Drain current	I _D	95	А	
Drain peak current	I _D (pulse) Note 1	380	A	
Body to drain diode reverse drain current	I _{DR}	95	A	
Avalanche current	I _{AP} Note 3	65	Α	
Avalanche energy	E _{AR} Note 3	560	mJ	
Channel dissipation	Pch Note 2	100	W	
Channel temperature	Tch	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. Value at Tc = 25°C

3. Value at Tch = 25°C, Rg \geq 50 Ω

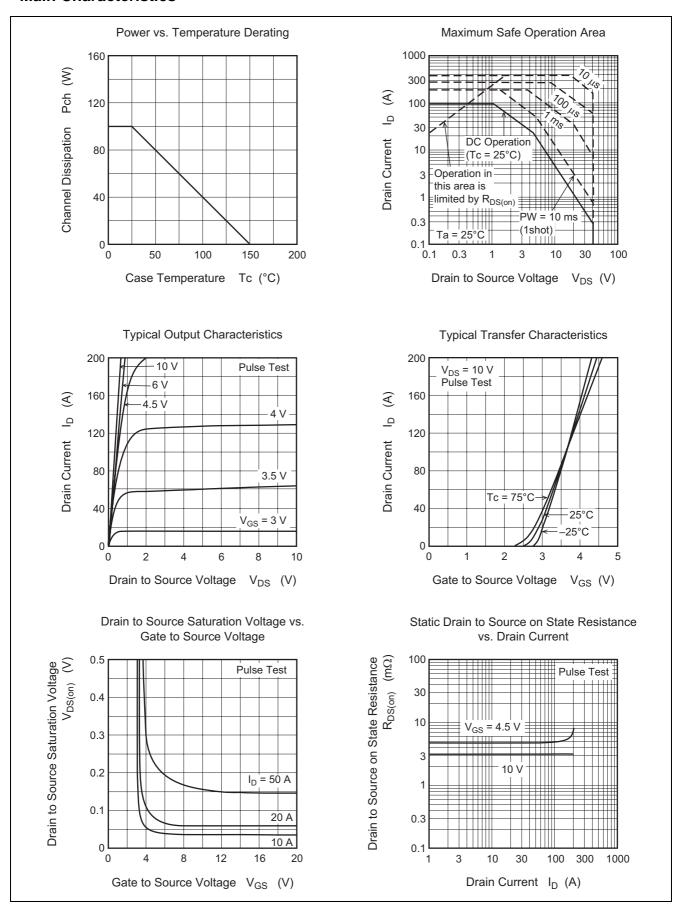
Electrical Characteristics

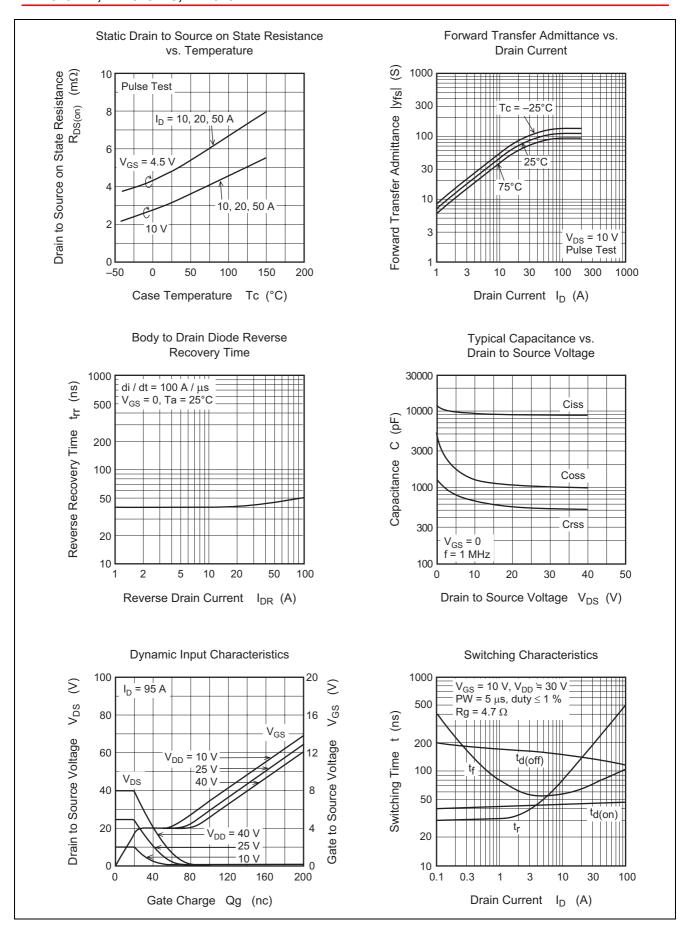
 $(Ta = 25^{\circ}C)$

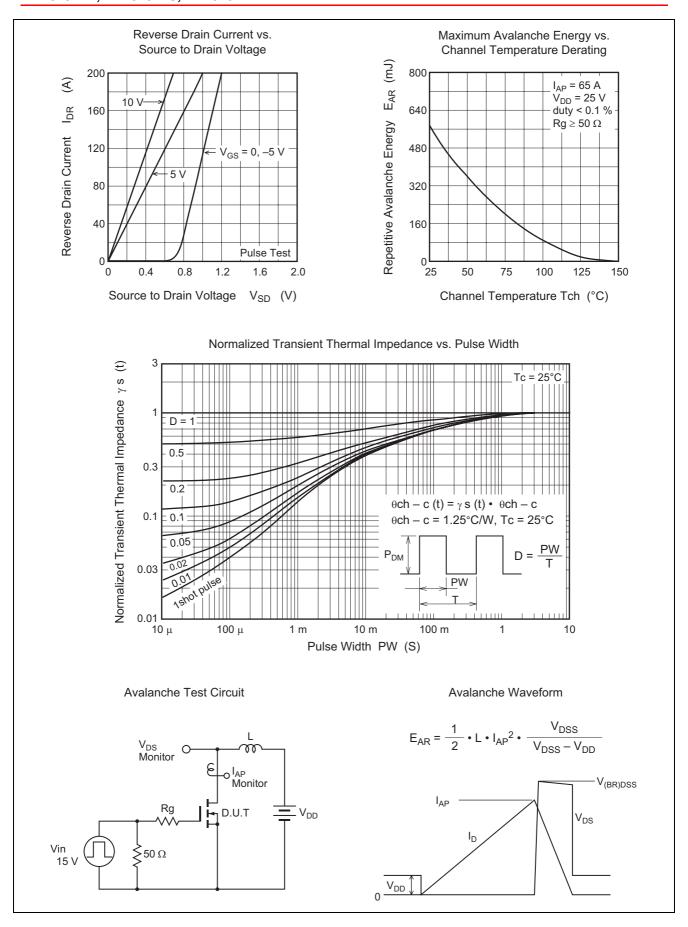
Item	Symbol	Min	Тур	Max	Unit	Test Conditions	
Drain to source breakdown voltage	V (BR) DSS	40	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$	
Zero gate voltage drain current	I _{DSS}	_	_	10	μΑ	$V_{DS} = 40 \text{ V}, V_{GS} = 0$	
Gate to source leak current	I _{GSS}	_	_	±0.1	μΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$	
Gate to source cutoff voltage	V _{GS (off)}	1.5	_	2.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}^{\text{Note 4}}$	
Forward transfer admittance	y _{fs}	60	100	_	S	$I_D = 47.5 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note 4}}$	
Static drain to source on state	R _{DS (on)}	_	3.1	4.2	mΩ	$I_D = 47.5 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note 4}}$	
resistance		_	4.8	7.0	mΩ	$I_D = 47.5 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note 4}}$	
Input capacitance	Ciss	_	9300	_	pF	V _{DS} = 10 V	
Output capacitance	Coss	_	1300	_	pF	$V_{GS} = 0$	
Reverse transfer capacitance	Crss	_	670	_	pF	f = 1 MHz	
Total gate charge	Qg	_	160	_	nC	V _{DD} = 25 V	
Gate to source charge	Qgs	_	36	_	nC	V _{GS} = 10 V	
Gate to drain charge	Qgd	_	40	_	nC	I _D = 95 A	
Turn-on delay time	t _{d (on)}	_	45	_	ns	V _{GS} = 10 V	
Rise time	t _r	_	270	_	ns	I _D = 47.5 A	
Turn-off delay time	t _{d (off)}	_	130	_	ns	$R_L = 0.63 \Omega$	
Fall time	t _f	_	85	_	ns	$Rg = 4.7 \Omega$	
Body to drain diode forward voltage	V_{DF}	_	0.95	_	V	I _F = 95 A, V _{GS} = 0	
Body to drain diode reverse recovery	t _{rr}	_	50	_	ns	I _F = 95 A, V _{GS} = 0	
time						$di_F/dt = 100 A/\mu s$	

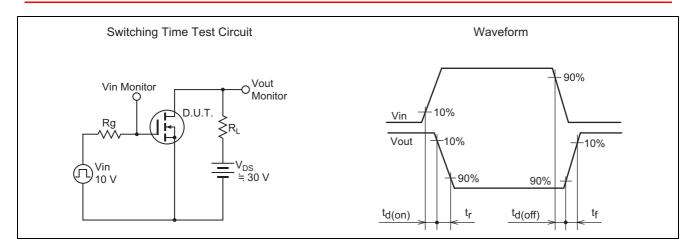
Note: 4. Pulse test

Main Characteristics

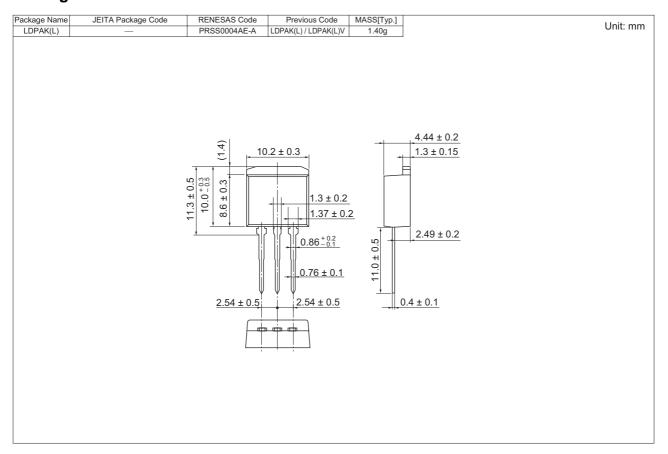


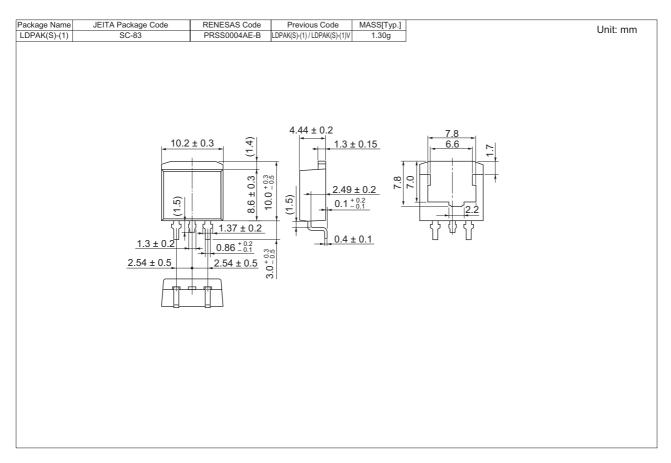


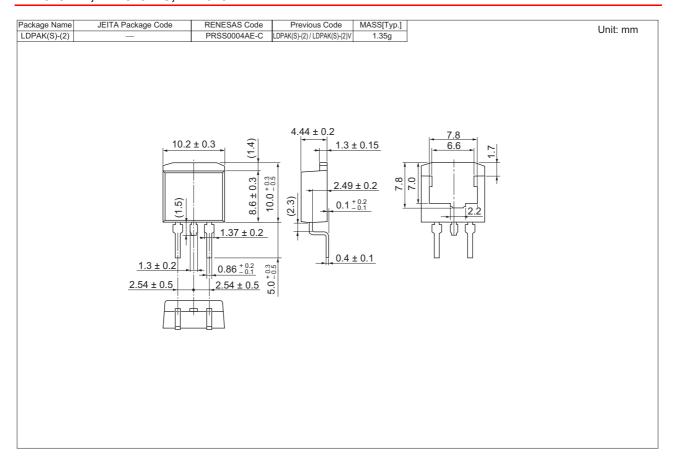




Package Dimensions







Ordering Information

Part Name	Quantity	Shipping Container
H7N0401LD-E	D-E 500 pcs Box (Conductive Sack)	
H7N0401LSTL-E	1000 pcs	Taping
H7N0401LMTL-E	1000 pcs	Taping

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