unit: mm



Low Drop output Voltage Regulator

Descriptions

• Three Terminal Positive Low Dropout Voltage Regulator

Features

- Low Standby Current Consumption (500 / Typ.)
- Maximum Output Current (180 mA Max.)
- Less I/O voltage Difference (250 mV Max.)

Ordering Information

Type NO.	Marking	Package Code		
S78DL33	S78DL33	TO-92		

Outline Dimensions

4.40~4.80 **PIN Connections** 1. Output 2. GND 3. Input 0.50 Max. $13.50 \sim 14.50$ ■Functional Block Diagram VIN VOUT Bias Circuit 1.27 Typ. _1.27 Typ. 1 2 3 Bandgap Reference Current Limit Circuit

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GND

Maximum ratings

Ta=25°C

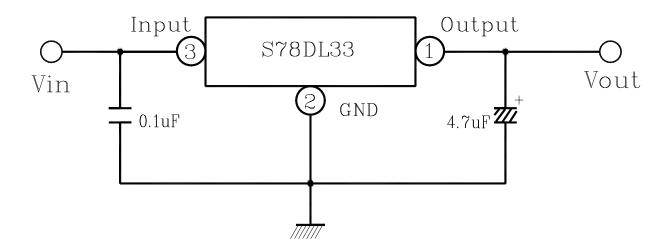
Characteristic	Symbol	Ratings	Unit
Operating Input voltage	V _{IN}	16	V
Power Dissipation	P_{D}	625	mW
Operating Temperature Range	T _{OPR}	-40~+85	°C
Junction Temperature	Tj	150	°C
Storage Temperature Range	T _{stg}	-55~150	°C
Lead Temperature Time	T _{sol}	260 (10 Sec)	°C

Electrical Characteristics

 $(\begin{tabular}{ll} (\begin{tabular}{ll} \begin{tabular}{ll} \b$

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output voltage	V _{OUT}	V _{IN} =4.3V, I _{OUT} =100uA	3.168	3.3	3.432	V
Voltage Regulation	∆ V _{OUT} (1)	$V_{IN} = 4.3V \sim 10V$, $I_{OUT} = 100uA$	-	2	15	mV
Load Regulation	∆ V _{OUT} (2)	V_{IN} =4.3V , I_{OUT} =1~100mA	-	7	28	mV
Dropout Voltage	V_{DROP}	I _{OUT} =50mA	-	110	230	- mV
		I _{OUT} =100mA	-	150	300	
Ripple Rejection Ratio	RR	f=100Hz, I _{OUT} =100uA	-	75	-	dB
Ground pin Current	I _{GND}	V _{IN} =4.3V , I _{OUT} =100uA		200	400	uA
		V_{IN} =4.3V , I_{OUT} =50mA		0.9	1.8	mA
		V _{IN} =4.3V , I _{OUT} =100mA		2.1	4	mA

■Test circuit



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Electrical Characteristic Curves

Fig. 1. Vin - Vout

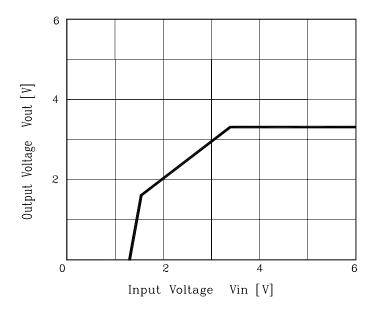


Fig. 3. Pd - Ta

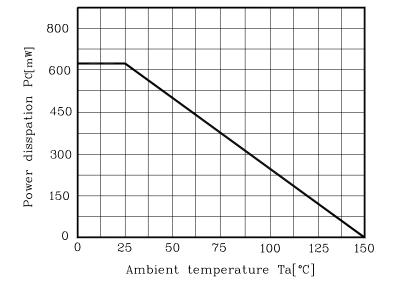


Fig. 2 $|Vout - Vin| - I_{OUT}$

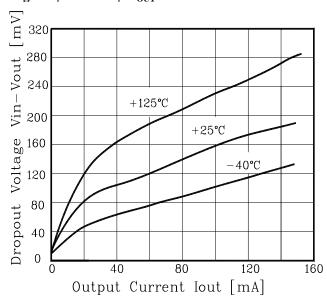
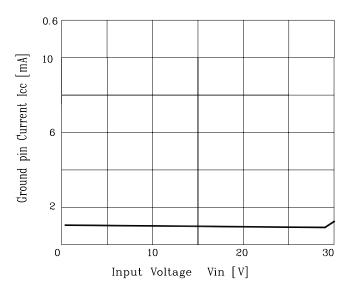


Fig. 4. Input voltage – Ground pin Current



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