## Features

## QFN12L (3 x 3 mm)

- Low Insertion Loss : 0.8 dB @ 2.5 GHz

$$
1.0 \text { dB @ } 4.9 \text { to } 6.0 \mathrm{GHz}
$$

- High Isolation: 43 dB @ 2.5 GHz

$$
36 \mathrm{~dB} @ 4.9 \text { to } 6.0 \mathrm{GHz}
$$

- Low DC Power Consumption
- Miniature QFN12L (3x3 mm) Plastic Package
- PHEMT process


## Description

The HWS411 is a GaAs PHEMT MMIC DPDT switch operating at DC-6 GHz in a low cost miniature QFN12L ( $3 \times 3 \mathrm{~mm}$ ) plastic package. The HWS411 features low insertion loss and high isolation up to 6 GHz with very low DC power consumption. This switch can be used in IEEE $802.11 \mathrm{a} / \mathrm{b} / \mathrm{g}$ WLAN systems for combination of transmit/receive and antenna diversity functions.


## Electrical Specifications at $25^{\circ} \mathrm{C}$ with $0,+3 \mathrm{~V}$ Control Voltages

| Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Insertion Loss | $\begin{aligned} & \text { 0.1-6.0 GHz } \\ & 2.4-2.5 \mathrm{GHz} \\ & 4.9-6.0 \mathrm{GHz} \end{aligned}$ |  | $\begin{aligned} & 1.0 \\ & 0.8 \\ & 1.0 \end{aligned}$ | 1.3 | dB <br> dB <br> dB |
| Isolation (on/off or off/on) | $\begin{aligned} & \text { 0.1-6.0 GHz } \\ & 2.4-2.5 \mathrm{GHz} \\ & 4.9-6.0 \mathrm{GHz} \end{aligned}$ | 26 | $\begin{aligned} & \hline 36 \\ & 43 \\ & 36 \end{aligned}$ |  | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \\ & \mathrm{~dB} \end{aligned}$ |
| Isolation (off/off) | $\begin{aligned} & 2.4-2.5 \mathrm{GHz} \\ & 4.9-6.0 \mathrm{GHz} \end{aligned}$ |  | $\begin{aligned} & 10 \\ & 17 \end{aligned}$ |  | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \end{aligned}$ |
| Return Loss | $\begin{aligned} & \text { 0.1-6.0 GHz } \\ & 2.4-2.5 \mathrm{GHz} \\ & 4.9-6.0 \mathrm{GHz} \end{aligned}$ |  | $\begin{aligned} & 15 \\ & 20 \\ & 18 \end{aligned}$ |  | dB <br> dB <br> dB |
| Input Power for One dB Compression | 2.0-6.0 GHz |  | 30 |  | dBm |
| Second Harmonic | Pin $=20 \mathrm{dBm}$ |  | -75 |  | dBc |
| Third Harmonic | Pin $=20 \mathrm{dBm}$ |  | -75 |  | dBc |
| Input Third Order Intermodulation Intercept Point | 20 dBm Per Tone @ 5.85 GHz |  | 45 |  | dBm |
| Switching Time |  |  | 50 |  | ns |
| Control Current |  |  | 5 | 100 | uA |

Note: All measurements made in a 50 ohm system with $0 /+3.0 \mathrm{~V}$ control voltages, unless otherwise specified.

## Typical Performance Data with 8pF Capacitors @ $+25^{\circ} \mathrm{C}$

Insertion Loss vs Frequency


Isolation vs Frequency


## Return Loss vs Frequency



## Absolute Maximum Ratings

| Parameter | Absolute Maximum |
| :--- | :---: |
| RF Input Power | $+32 \mathrm{dBm} @+3 \mathrm{~V}$ |
| Control Voltage | +6 V |
| Operating Temperature | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |
| Storage Temperature | $-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |

## Pin Out (Top View)



Note:

1. DC blocking capacitors $\mathrm{C}_{\mathrm{B}}=8 \mathrm{pF}$ are required on all RF ports.
2. Exposed pad in the bottom must be connected to ground by via holes.
3. TX and RX ports can be used interchangeably.

Logic Table for Switch On-Path

| VC1 | VC2 | ANT1-RX | ANT1-TX | ANT2-TX | ANT2-RX |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | 0 | On | Off | On | Off |
| 0 | 1 | Off | On | Off | On |
| 1 | 1 | Off | Off | Off | Off |
| 0 | 0 | Off | Off | Off | Off |


| '1' $=+3 V$ to +5 V |
| :--- |
| $0^{\prime}=0 V$ to +0.2 V |

