

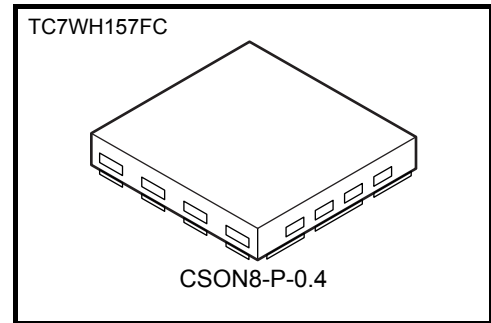
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7WH157FC

2-Channel Multiplexer

Features

- High-speed : $t_{pd} = 4.1 \text{ ns (Typ.) at } V_{CC} = 5 \text{ V}$
- Low power dissipation : $I_{CC} = 2\mu\text{A(Max.) at } T_a = 25^\circ\text{C}$
- High noise immunity : $V_{NIH} = V_{NIL} = 28\%V_{CC} \text{ (Min.)}$
- Operation voltage range : $V_{CC(opr)} = 2\sim 5.5 \text{ V}$
- 5.5-V Tolerant inputs.



Weight: 0.002g (typ.)

Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Characteristics	Symbol	Ratingh	Unit
Power supply voltage	V_{CC}	$-0.5\sim 7.0$	V
DC input voltage	V_{IN}	$-0.5\sim 7.0$	V
DC output voltage	V_{OUT}	$-0.5\sim V_{CC} + 0.5 \text{ (Note1)}$	V
Input diode current	I_{IK}	-20	mA
Output diode current	I_{OK}	$\pm 20 \text{ (Note2)}$	mA
DC output current	I_{OUT}	± 25	mA
DC V_{CC}/GND current	I_{CC}	± 50	mA
Power dissipation	P_D	150 (Note3)	mW
Storage temperature	T_{stg}	$-65\sim 150$	$^\circ\text{C}$

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1 : High or Low State.

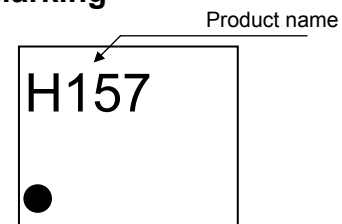
I_{OUT} absolute maximum rating must be observed.

Note2 : $V_{OUT} < \text{GND}$, $V_{OUT} > V_{CC}$

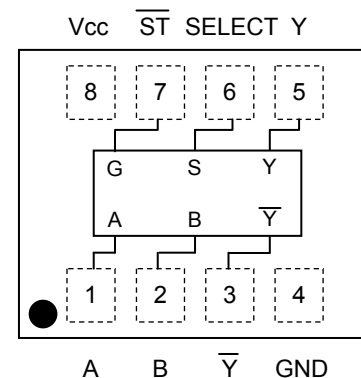
Note3 : Mounted on an FR4 board.

(25.4 mm × 25.4 mm × 1.6 t, Cu Pad: 11.56 mm²)

Marking



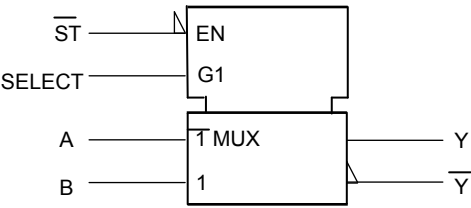
Pin Assignment (top view)



Truth Table

Inputs				Outputs	
$\overline{\text{ST}}$	SELECT	A	B	Y	$\overline{\text{Y}}$
H	X	X	X	L	H
L	L	L	X	L	H
L	L	H	X	H	L
L	H	X	L	L	H
L	H	X	H	H	L

IEC Logic Diagram



Operating Ranges

Characteristics	Symbol	Rating	Unit
Power supply voltage	V_{CC}	2~5.5	V
Input voltage	V_{IN}	0~5.5	V
Output voltage	V_{OUT}	0~ V_{CC}	V
Operating temperature	T_{opr}	-40~85	°C
Input rise and fall time	dt/dv	0~100 ($V_{CC} = 3.3 \text{ V} \pm 0.3 \text{ V}$)	ns/V
		0~20 ($V_{CC} = 5 \text{ V} \pm 0.5 \text{ V}$)	

DC Electrical Characteristics

Characteristic	Symbol	Test condition		Ta = 25°C			Ta = -40~85°C		Unit	
				VCC (V)	Min.	Typ.	Max.	Min.		Max.
High-level input voltage	VIH	—		2.0	1.5	—	—	1.5	—	V
				3.0~5.5	VCC × 0.7	—	—	VCC × 0.7	—	
Low-level input voltage	VIL	—		2.0	—	—	0.5	—	0.5	
				3.0~5.5	—	—	VCC × 0.3	—	VCC × 0.3	
High-level output voltage	VOH	VIN = VIL or VIH	IOH = -50 μA	2.0	1.9	2.0	—	1.9	—	V
				3.0	2.9	3.0	—	2.9	—	
				4.5	4.4	4.5	—	4.4	—	
			IOH = -4 mA	3.0	2.58	—	—	2.48	—	
				IOH = -8 mA	4.5	3.94	—	—	3.80	
Low-level output voltage	VOL	VIN = VIL or VIH	IOL = 50 μA	2.0	—	0.0	0.1	—	0.1	V
				3.0	—	0.0	0.1	—	0.1	
				4.5	—	0.0	0.1	—	0.1	
			IOL = 4 mA	3.0	—	—	0.36	—	0.44	
				IOL = 8 mA	4.5	—	—	0.36	—	
Input leakage current	IIN	VIN = 5.5 V or GND	0~5.5	—	—	±0.1	—	±1.0	μA	
Quiescent supply current	ICC	VIN = VCC or GND	5.5	—	—	2.0	—	20.0	μA	

AC Electrical Characteristics (Input : $t_r = t_f = 3 \text{ ns}$)

Characteristic	Symbol	Test condition		Ta = 25°C			Ta = -40~85°C		Unit
		V _{CC} (V)	C _L (pF)	Min.	Typ.	Max.	Min.	Max.	
Propagation deley time (A,B – Y , \overline{Y})	t_{pLH} t_{pHL}	3.3 ± 0.3	15	—	6.2	9.7	1.0	11.5	ns
			50	—	8.7	13.2	1.0	15.0	
		5.0 ± 0.5	15	—	4.1	6.4	1.0	7.5	
			50	—	5.6	8.4	1.0	9.5	
Propagation deley time (SELECT-Y , \overline{Y})	t_{pLH} t_{pHL}	3.3 ± 0.3	15	—	8.4	13.2	1.0	15.5	ns
			50	—	10.9	16.7	1.0	19.0	
		5.0 ± 0.5	15	—	5.3	8.1	1.0	9.5	
			50	—	6.8	10.1	1.0	11.5	
Propagation deley time (\overline{ST} -Y , \overline{Y})	t_{pLH} t_{pHL}	3.3 ± 0.3	15	—	8.7	13.6	1.0	16.0	ns
			50	—	11.2	17.1	1.0	19.5	
		5.0 ± 0.5	15	—	5.6	8.6	1.0	10.0	
			50	—	7.1	10.6	1.0	12.0	
Input capacitance	C _{IN}	—		—	4	10	—	10	pF
Power dissipation capacitance	C _{PD}	(Note 4)		—	20	—	—	—	pF

(Note 4): C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

$$I_{CC}(\text{opr.}) = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

CSON8-P-0.4

Technical drawing of a mechanical part with dimensions in mm. The drawing includes a top view, a front view, and a side view. The top view shows a rectangular plate with a central rectangular cutout. The front view shows the plate with four small rectangular features. The side view shows the plate with a central rectangular feature. The dimensions are as follows:

- Top view:
 - Overall width: 1.35 ± 0.05
 - Overall height: 1.45 ± 0.05
 - Central cutout width: 0.45 ± 0.03
 - Central cutout height: 1.10 ± 0.03
 - Distance from top edge to cutout top: 0.20 ± 0.03
 - Distance from bottom edge to cutout bottom: 0.40 ± 0.02
 - Distance from left edge to cutout left: 0.525 ± 0.02
 - Distance from right edge to cutout right: 0.05 ± 0.03
- Front view:
 - Overall width: 1.35 ± 0.05
 - Overall height: 1.45 ± 0.05
 - Distance from top edge to first feature: $0.38^{+0.02}_{-0.03}$
- Side view:
 - Overall width: 1.35 ± 0.05
 - Overall height: 1.45 ± 0.05
 - Distance from top edge to first feature: $0.38^{+0.02}_{-0.03}$
 - Distance from top edge to second feature: 0.05 ± 0.03
 - Distance from top edge to third feature: 0.15 ± 0.03

2007-11-01

RESTRICTIONS ON PRODUCT USE

20070701-EN GENERAL

- The information contained herein is subject to change without notice.
- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property.
In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc.
- The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc.. Unintended Usage of TOSHIBA products listed in this document shall be made at the customer's own risk.
- The products described in this document shall not be used or embedded to any downstream products of which manufacture, use and/or sale are prohibited under any applicable laws and regulations.
- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA for any infringements of patents or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any patents or other rights of TOSHIBA or the third parties.
- Please contact your sales representative for product-by-product details in this document regarding RoHS compatibility. Please use these products in this document in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances. Toshiba assumes no liability for damage or losses occurring as a result of noncompliance with applicable laws and regulations.