TOSHIBA Transistor Silicon PNP Epitaxial Type (PCT Process) Silicon NPN Epitaxial Type (PCT Process)

# HN1B04F

Audio Frequency General Purpose Amplifier Applications Driver Stage Amplifier Applications Switching application

Q1:

Excellent h<sub>FE</sub> linearity

:  $h_{FE(2)}$  =25 (Min.) at  $V_{CE}$  = -6V I<sub>C</sub> = -400mA

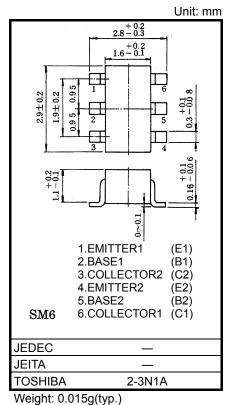
Q2:

• Excellent h<sub>FE</sub> linearity

: h<sub>FE(2)</sub> =25 (Min.) at V<sub>CE</sub> = 6V I<sub>C</sub> = 400mA

#### Q1 Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	V <sub>CBO</sub>	-35	V
Collector-emitter voltage	V <sub>CEO</sub>	-30	V
Emitter-base voltage	V <sub>EBO</sub>	-5	V
Collector current	Ι <sub>C</sub>	-500	mA



#### Q2 Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	V <sub>CBO</sub>	35	V
Collector-emitter voltage	V <sub>CEO</sub>	30	V
Emitter-base voltage	V <sub>EBO</sub>	5	V
Collector current	ΙC	500	mA

### Q1,Q2 Common Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector power dissipation	P <sub>C</sub> *	300	mW
Junction temperature	Tj	150	°C
Storage temperature range	T <sub>stg</sub>	-55~150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

\* Total rating. 200mW per element must be exceeded.

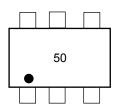
### Q1 Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I <sub>CBO</sub>	-	$V_{CB}$ = -35V, I <sub>E</sub> = 0	_	_	-100	nA
Emitter cut-off current	I <sub>EBO</sub>	-	$V_{EB} = -5V, I_C = 0$	-	-	-100	nA
DC current gain	h <sub>FE(1)</sub>	—	V <sub>CE</sub> = -1V, I <sub>C</sub> = -100mA	70		400	
	h <sub>FE(2)</sub>	_	$V_{CE} = -6V, I_C = -400mA$	25	_	_	
Collector-emitter saturation voltage	V <sub>CE (sat)</sub>	_	I <sub>C</sub> = –100mA, I <sub>B</sub> = –10mA	_	-0.1	-0.25	V
Base-Emitter Voltage	V <sub>BE</sub>	_	V <sub>CE</sub> = -1V, I <sub>C</sub> = -100mA	_	-0.8	-1.0	V
Transition frequency	f <sub>T</sub>	_	V <sub>CE</sub> = -6V, I <sub>C</sub> = -20mA	_	200	_	MHz
Collector output capacitance	C <sub>ob</sub>	_	V <sub>CB</sub> = –6V, I <sub>E</sub> = 0, f = 1MHz		7	_	pF

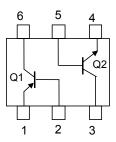
### Q2 Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I <sub>CBO</sub>	_	V <sub>CB</sub> = 35V, I <sub>E</sub> = 0	_	_	100	nA
Emitter cut-off current	I <sub>EBO</sub>	—	V <sub>EB</sub> = 5V, I <sub>C</sub> = 0		—	100	nA
DC current gain	h <sub>FE(1)</sub>	_	V <sub>CE</sub> = 1V, I <sub>C</sub> = 100mA	70	_	400	
	h <sub>FE(2)</sub>	_	V <sub>CE</sub> = 6V, I <sub>C</sub> = 400mA	25	_	_	
Collector-emitter saturation voltage	V <sub>CE (sat)</sub>	—	I <sub>C</sub> = 100mA, I <sub>B</sub> = 10mA	_	0.1	0.25	V
Base-Emitter Voltage	V <sub>BE</sub>	—	V <sub>CE</sub> = 1V, I <sub>C</sub> = 100mA		0.8	1.0	V
Transition frequency	f <sub>T</sub>	_	$V_{CE}$ = 6V, $I_{C}$ = 20mA	_	300	_	MHz
Collector output capacitance	C <sub>ob</sub>	—	V <sub>CB</sub> = 6V, I <sub>E</sub> = 0, f = 1MHz		7		pF

### Marking

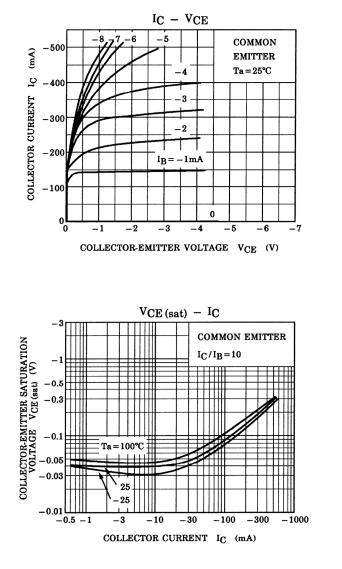


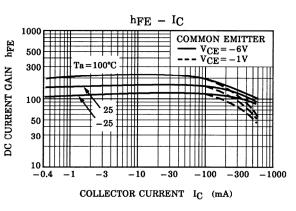
### Equivalent Circuit (Top View)

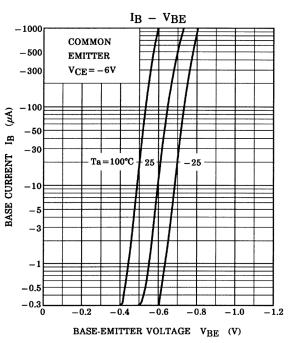


# **TOSHIBA**

## Q1 (PNP transistor)

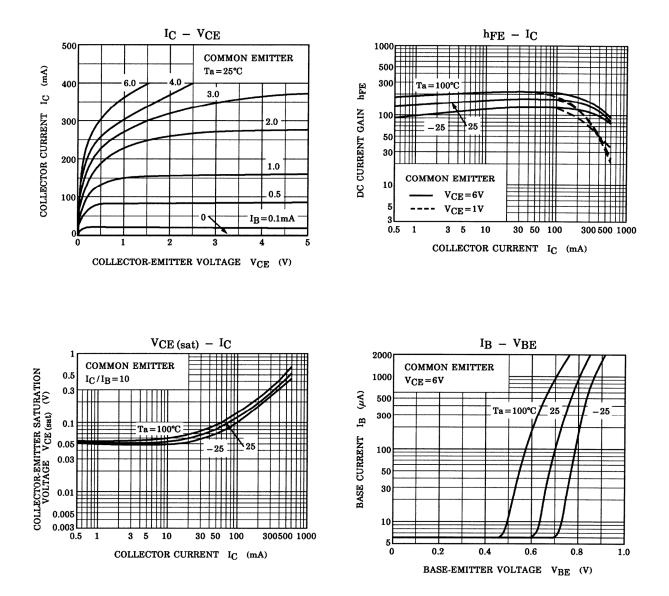






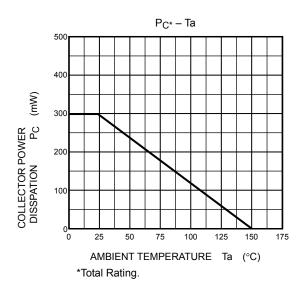
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### Q2 (NPN transistor)



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### (Q1, Q2 Common)



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20070701-EN GENERAL

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