

TOSHIBA Diode Silicon Epitaxial Planar Type

1SV324

TCXO/VCO

Unit: mm

- High capacitance ratio: $C_1 V/C_4 V = 4.3$ (typ.)
- Low series resistance: $r_s = 0.4 \Omega$ (typ.)
- Useful for small size tuner.

Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Characteristics	Symbol	Rating	Unit
Reverse voltage	V_R	10	V
Junction temperature	T_j	125	$^\circ\text{C}$
Storage temperature range	T_{stg}	-55~125	$^\circ\text{C}$

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

		USC
JEDEC	—	
JEITA	—	
TOSHIBA	1-1E1A	

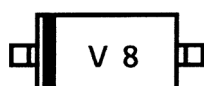
Weight: 0.004 g (typ.)

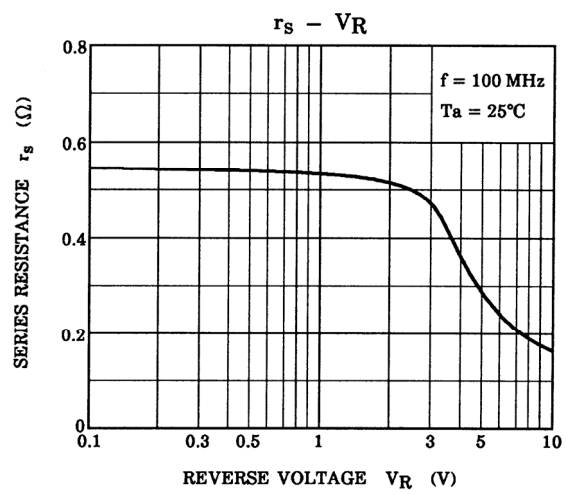
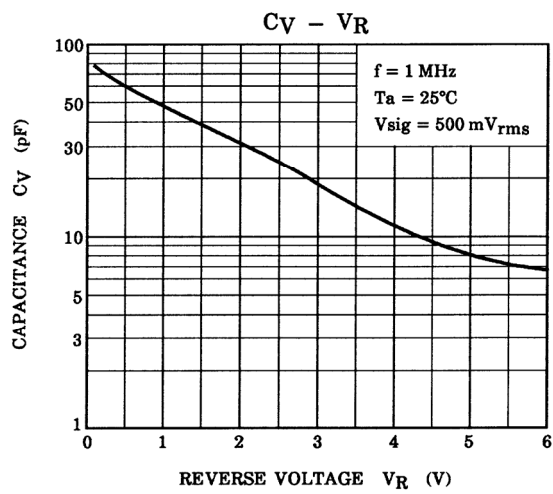
Electrical Characteristics ($T_a = 25^\circ\text{C}$)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Reverse voltage	V_R	$I_R = 1 \mu\text{A}$	10	—	—	V
Reverse current	I_R	$V_R = 10 \text{ V}$	—	—	3	nA
Capacitance	$C_1 V$	$V_R = 1 \text{ V}, f = 1 \text{ MHz}$	44	—	49.5	pF
Capacitance	$C_4 V$	$V_R = 4 \text{ V}, f = 1 \text{ MHz}$	9.2	—	12	pF
Capacitance ratio	$C_1 V/C_4 V$	—	4	4.3	—	—
Series resistance	r_s	$V_R = 4 \text{ V}, f = 100 \text{ MHz}$	—	0.4	0.8	Ω

Note: Signal level when capacitance is measured: $V_{sig} = 500 \text{ mVrms}$

Marking





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20070701-EN GENERAL

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