

RJK1525DPS

Silicon N Channel MOS FET
High Speed Power Switching

REJ03G1314-0200

Rev.2.00

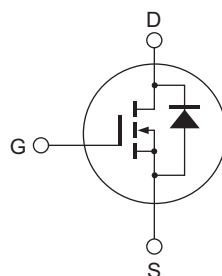
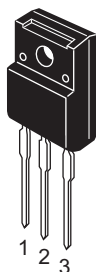
Feb 08, 2007

Features

- Low on-resistance
- Low leakage current
- High speed switching

Outline

RENESAS Package code: PRSS0003AE-A
(Package name: TO-220CFM)



1. Gate
2. Drain
3. Source

Absolute Maximum Ratings

($T_a = 25^\circ\text{C}$)

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	150	V
Gate to source voltage	V_{GSS}	± 30	V
Drain current	I_D	17	A
Drain peak current	$I_{D(pulse)}$ ^{Note1}	50	A
Body-drain diode reverse drain current	I_{DR}	17	A
Body-drain diode reverse drain peak current	$I_{DR(pulse)}$ ^{Note1}	50	A
Avalanche current	I_{AP} ^{Note3}	17	A
Avalanche energy	E_{AR} ^{Note3}	21.6	mJ
Channel dissipation	P_{ch} ^{Note2}	30	W
Channel to case thermal impedance	θ_{ch-c}	4.17	$^\circ\text{C}/\text{W}$
Channel temperature	T_{ch}	150	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 to +150	$^\circ\text{C}$

Notes: 1. $PW \leq 10 \mu\text{s}$, duty cycle $\leq 1\%$

2. Value at $T_c = 25^\circ\text{C}$

3. $ST_{ch} = 25^\circ\text{C}$, $T_{ch} \leq 150^\circ\text{C}$

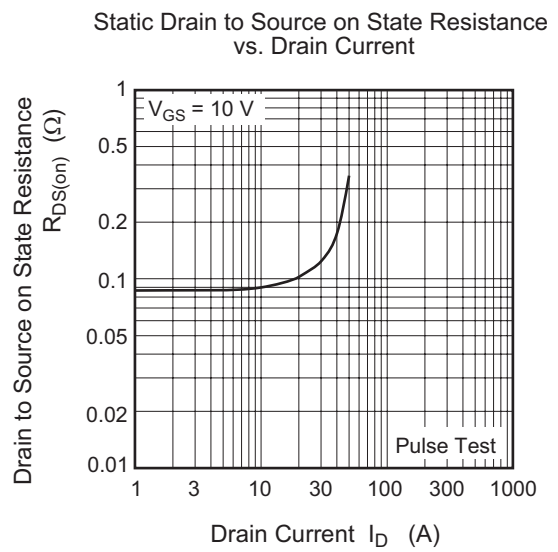
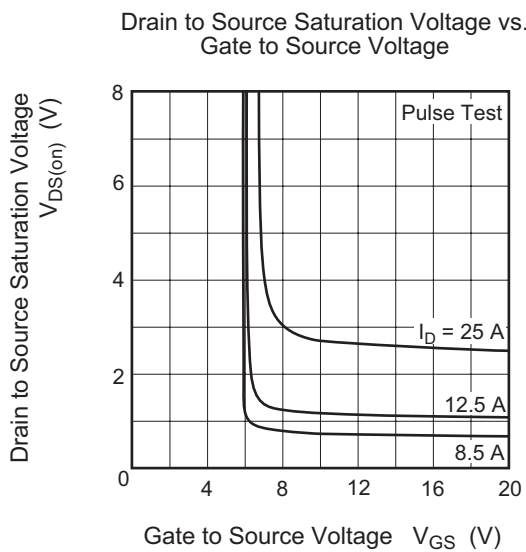
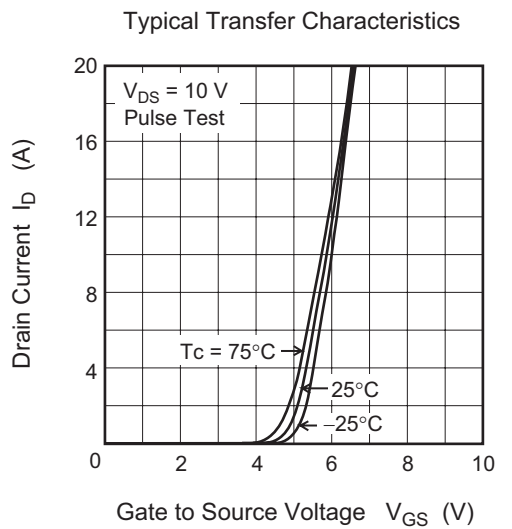
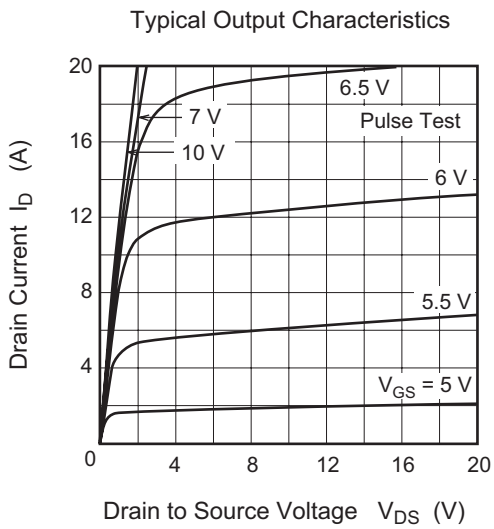
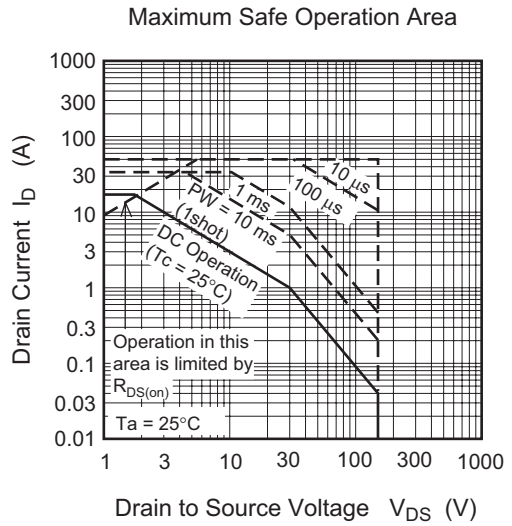
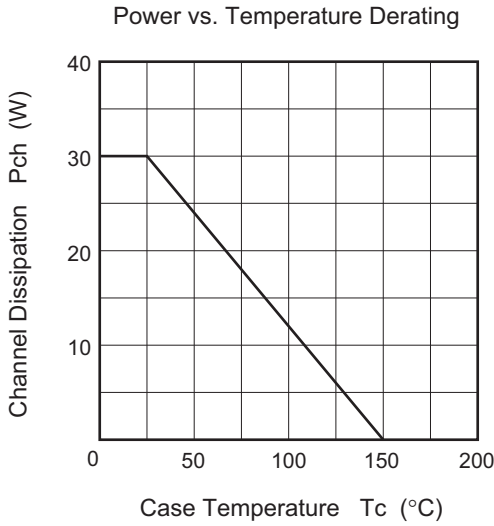
Electrical Characteristics

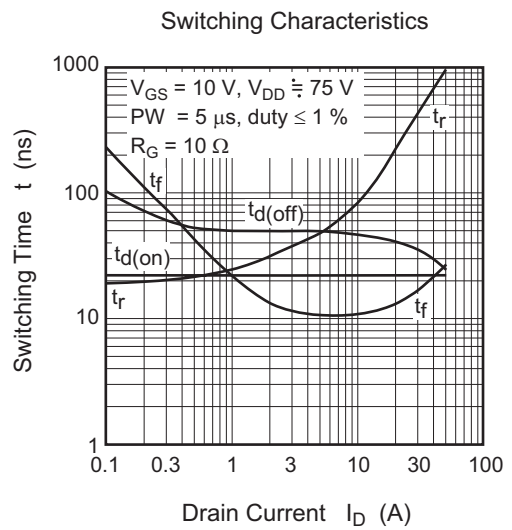
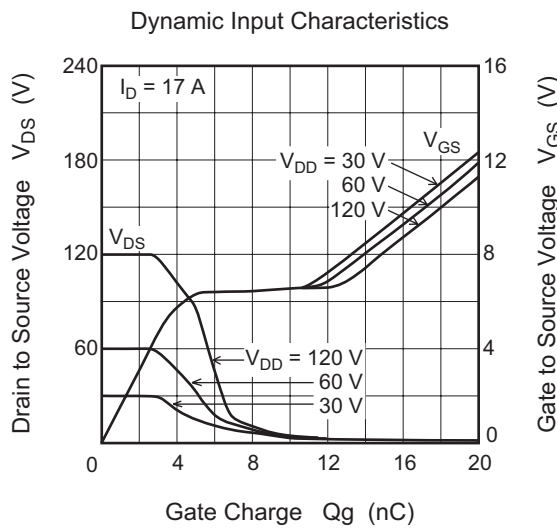
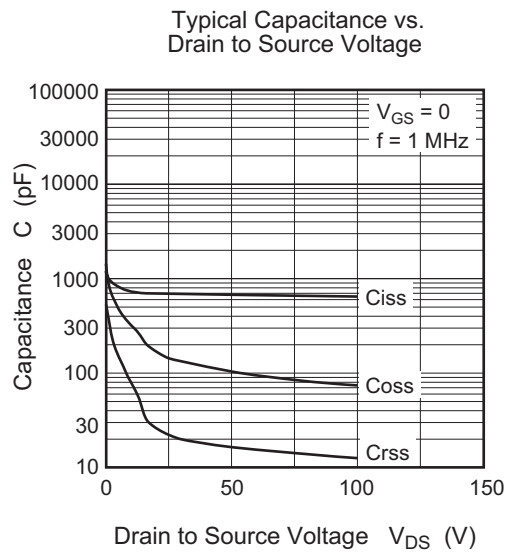
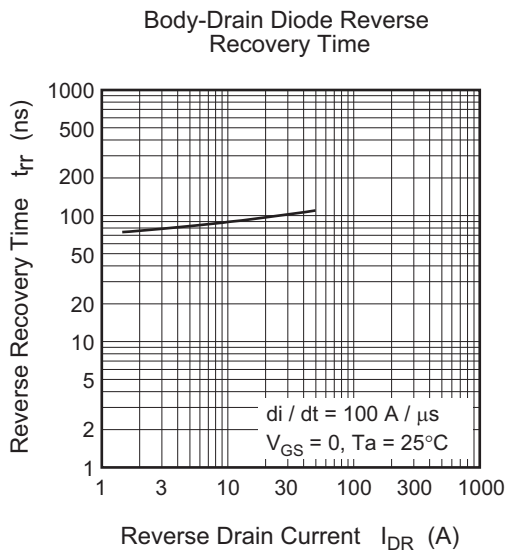
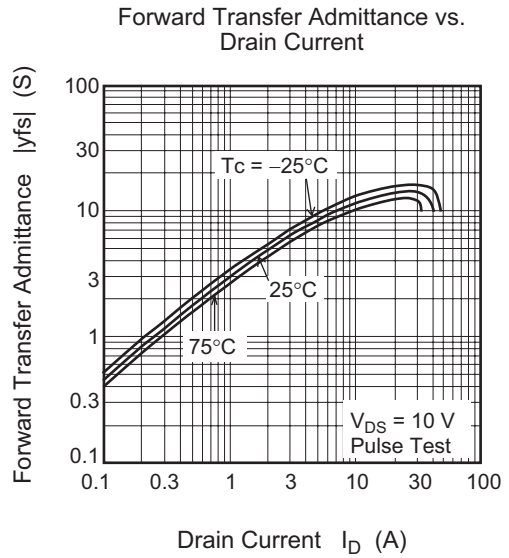
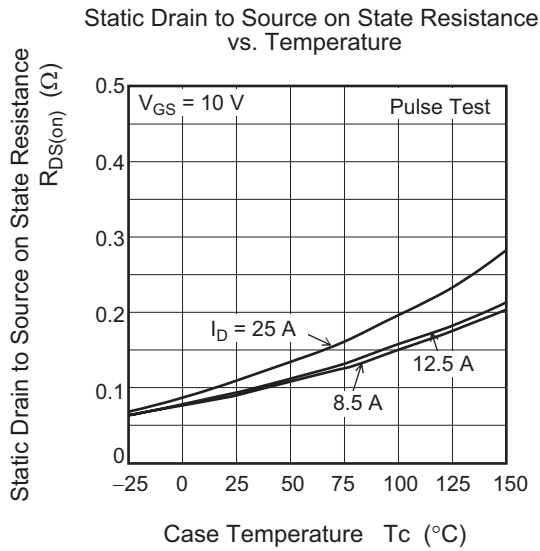
(Ta = 25°C)

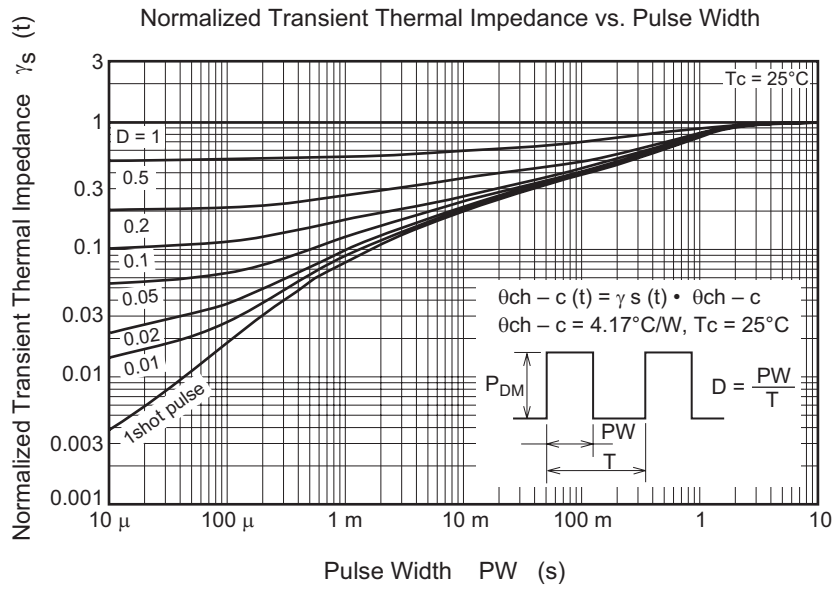
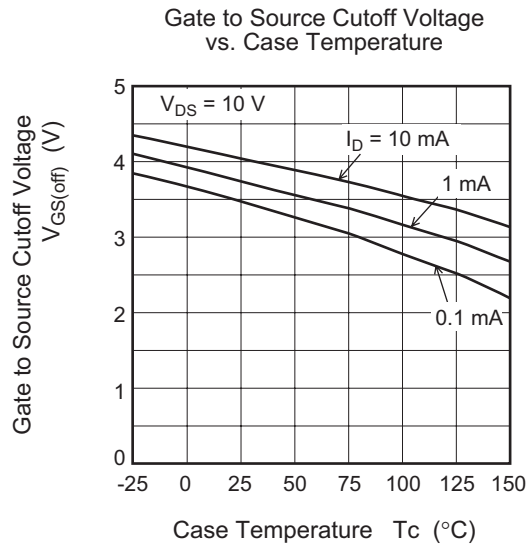
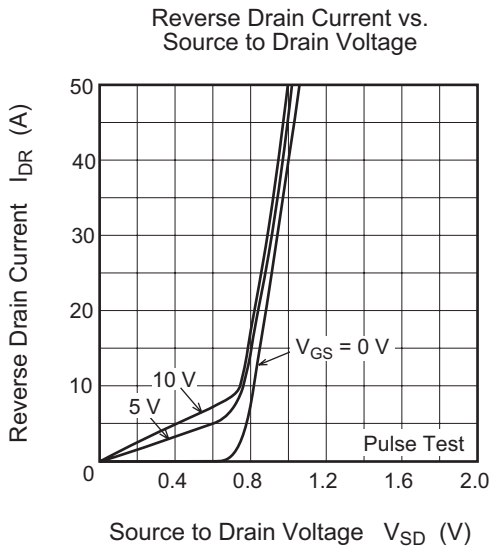
Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	150	—	—	V	$I_D = 10 \text{ mA}$, $V_{GS} = 0$
Zero gate voltage drain current	I_{DSS}	—	—	1	μA	$V_{DS} = 150 \text{ V}$, $V_{GS} = 0$
Gate to source leak current	I_{GSS}	—	—	± 0.1	μA	$V_{GS} = \pm 30 \text{ V}$, $V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	3.0	—	4.5	V	$V_{DS} = 10 \text{ V}$, $I_D = 1 \text{ mA}$
Forward transfer admittance	$ y_{fs} $	6	11	—	S	$I_D = 8.5 \text{ A}$, $V_{DS} = 10 \text{ V}$ ^{Note4}
Static drain to source on state resistance	$R_{DS(on)}$	—	0.089	0.110	Ω	$I_D = 8.5 \text{ A}$, $V_{GS} = 10 \text{ V}$ ^{Note4}
Input capacitance	C_{iss}	—	680	—	pF	$V_{DS} = 25 \text{ V}$ $V_{GS} = 0$ $f = 1 \text{ MHz}$
Output capacitance	C_{oss}	—	150	—	pF	
Reverse transfer capacitance	C_{rss}	—	22	—	pF	
Turn-on delay time	$t_{d(on)}$	—	22	—	ns	$I_D = 8.5 \text{ A}$ $V_{GS} = 10 \text{ V}$ $R_L = 8.8 \Omega$ $R_g = 10 \Omega$
Rise time	t_r	—	70	—	ns	
Turn-off delay time	$t_{d(off)}$	—	47	—	ns	
Fall time	t_f	—	11	—	ns	
Total gate charge	Q_g	—	18	—	nC	$V_{DD} = 120 \text{ V}$ $V_{GS} = 10 \text{ V}$ $I_D = 17 \text{ A}$
Gate to source charge	Q_{gs}	—	4.2	—	nC	
Gate to drain charge	Q_{gd}	—	8.3	—	nC	
Body-drain diode forward voltage	V_{DF}	—	0.88	1.40	V	$I_F = 17 \text{ A}$, $V_{GS} = 0$ ^{Note4}
Body-drain diode reverse recovery time	t_{rr}	—	95	—	ns	$I_F = 17 \text{ A}$, $V_{GS} = 0$ $di_F/dt = 100 \text{ A}/\mu\text{s}$
Body-drain diode reverse recovery charge	Q_{rr}	—	0.3	—	μC	

Notes: 4. Pulse test

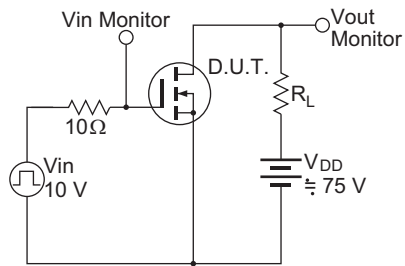
Main Characteristics



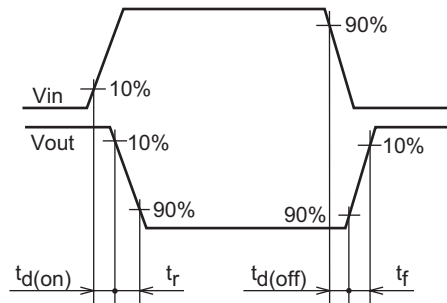




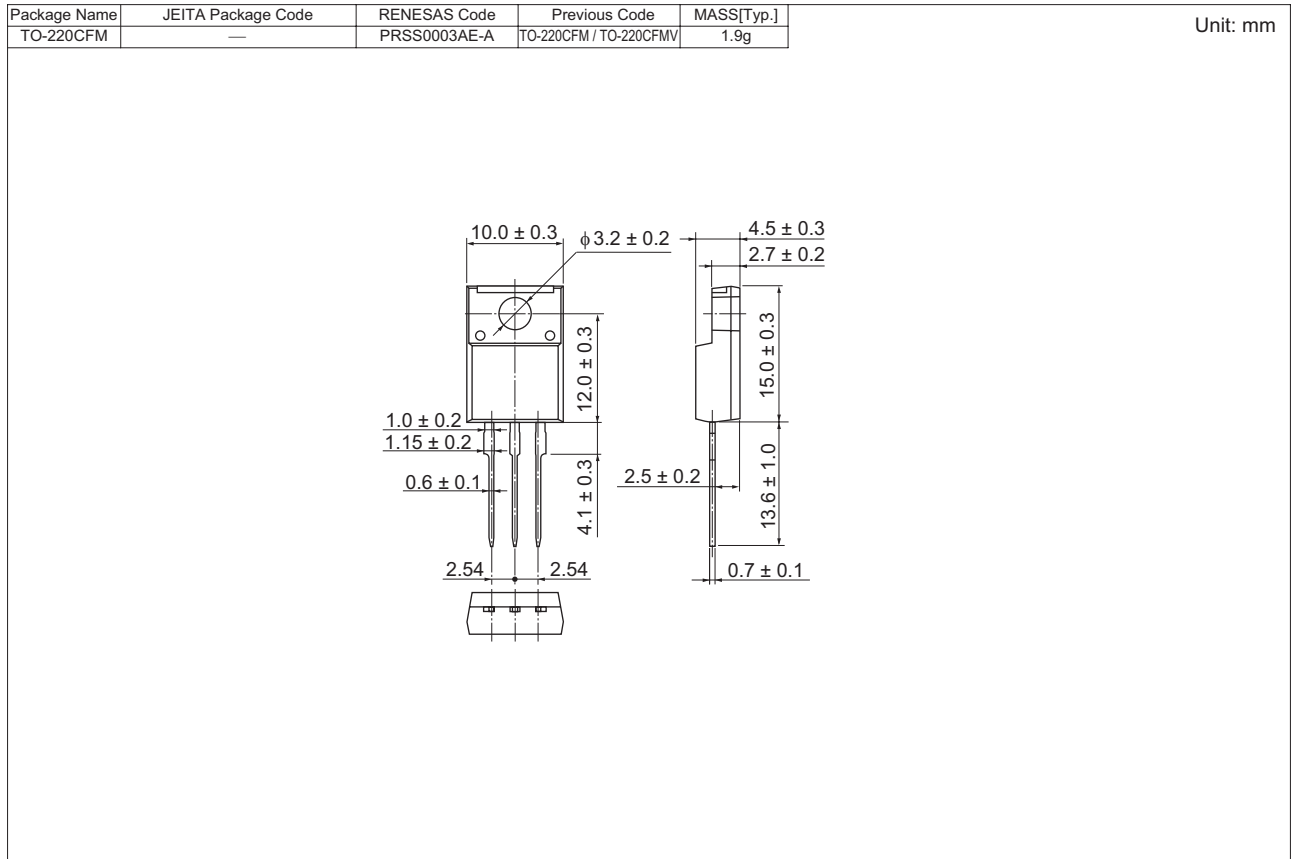
Switching Time Test Circuit



Waveform



Package Dimensions



Ordering Information

Part No.	Quantity	Shipping Container
RJK1525DPS-00-T2	600 pcs	Box (Tube)

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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