FAIRCHILD SEMICONDUCTOR™

August 2002
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NC7SP38
TinyLogic® ULP 2-Input NAND Gate (Open Drain Output)

General Description

The NC7SP38 is a single 2-Input NAND Gate with open drain output stage from Fairchild's Ultra Low Power (ULP) series of TinyLogic®. Ideal for applications where battery life is critical, this product is designed for ultra low power consumption within the V_{CC} operating range of 0.9V to 3.6V V_{CC}.

The internal circuit is composed of a minimum of inverter stages, including the output buffer, to enable ultra low static and dynamic power.

The NC7SP38, for lower drive requirements, is uniquely designed for optimized power and speed, and is fabricated with an advanced CMOS technology to achieve best in class speed operation while maintaining extremely low CMOS power dissipation.

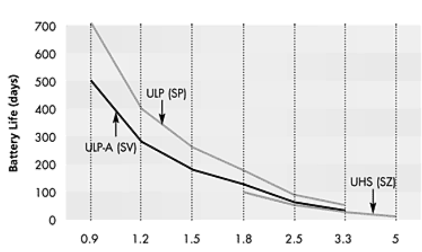
Features

- 0.9V to 3.6V V_{CC} supply operation
- 3.6V overvoltage tolerant I/O's at V_{CC} from 0.9V to 3.6V
- t_{PD}
  3.0 ns typ for 3.0V to 3.6V V_{CC}
  4.0 ns typ for 2.3V to 2.7V V_{CC}
  5.0 ns typ for 1.65V to 1.95V V_{CC}
  6.0 ns typ for 1.40V to 1.60V V_{CC}
  9.0 ns typ for 1.10V to 1.30V V_{CC}
  24.0 ns typ for 0.90V V_{CC}
- Power-Off high impedance inputs and outputs
- Static Drive (I_{OH}/I_{OL})
  ±2.6 mA @ 3.00V V_{CC}
  ±2.1 mA @ 2.30V V_{CC}
  ±1.5 mA @ 1.65V V_{CC}
  ±1.0 mA @ 1.40V V_{CC}
  ±0.5 mA @ 1.10V V_{CC}
  ±20 µA @ 0.9V V_{CC}
- Uses patented Quiet Series™ noise/EMI reduction circuitry
- Ultra small MicroPak™ leadfree package
- Ultra Low dynamic power

Ordering Code:

Order Number	Package Number	Product Code Top Mark	Package Description	Supplied As
NC7SP38P5X	MAA05A	938	5-Lead SC70, EIAJ SC-88a, 1.25mm Wide	3k Units on Tape and Reel
NC7SP38L6X	MAC06A	K7	6-Lead MicroPak, 1.0mm Wide	5k Units on Tape and Reel

Battery Life vs. V_{CC} Supply Voltage



TinyLogic ULP and ULP-A with up to 50% less power consumption can extend your battery life significantly.
Battery Life = ($V_{battery}$ *$I_{battery}$*.9)/(P_{device})/24hrs/day
Where, $P_{device} = (I_{CC} * V_{CC}) + (C_{PD} + C_L) * V_{CC}^2 * f$
Assumes ideal 3.6V Lithium Ion battery with current rating of 900mAH and derated 90% and device frequency at 10MHz, with C_L = 15 pF load

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Quiet Series™, and MicroPak™ are trademarks of Fairchild Semiconductor Corporation.

Logic Symbol

IEEE/IEC

Pin Descriptions

Pin Names	Description
A, B	Input
Y	Output
NC	No Connect

Function Table

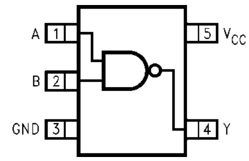
$Y = \overline{AB}$

Input		Output
A	**B**	**Y**
L	L	*H
L	H	*H
H	L	*H
H	H	L

H = HIGH Logic Level
L = LOW Logic Level
*H = HIGH Impedance Output State (Open Drain)

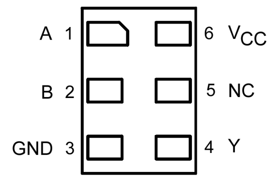
Connection Diagrams

Pin Assignments for SC70



(Top View)

Pad Assignments for MicroPak



(Top Thru View)

Absolute Maximum Ratings (Note 1)

Supply Voltage (V_{CC})	−0.5V to +4.6V
DC Input Voltage (V_{IN})	−0.5V to +4.6V
DC Output Voltage (V_{OUT})	
HIGH or LOW State (Note 2)	−0.5V to V_{CC} +0.5V
V_{CC} = 0V	−0.5V to 4.6V
DC Input Diode Current (I_{IK}) V_{IN} < 0V	±50 mA
DC Output Diode Current (I_{OK})	
V_{OUT} < 0V	−50 mA
V_{OUT} > V_{CC}	+50 mA
DC Output Source/Sink Current (I_{OH}/I_{OL})	± 50 mA
DC V_{CC} or Ground Current per	
Supply Pin (I_{CC} or Ground)	± 50 mA
Storage Temperature Range (T_{STG})	−65°C to +150°C

Recommended Operating Conditions (Note 3)

Supply Voltage	0.9V to 3.6V
Input Voltage (V_{IN})	0V to 3.6V
Output Voltage (V_{OUT})	
HIGH or LOW State	0V to V_{CC}
V_{CC} = 0V	0V to 3.6V
Output Current in I_{OH}/I_{OL}	
V_{CC} = 3.0V to 3.6V	±2.6 mA
V_{CC} = 2.3V to 2.7V	± 2.1 mA
V_{CC} = 1.65V to 1.95V	± 1.5 mA
V_{CC} = 1.40V to 1.60V	± 1 mA
V_{CC} = 1.10V to 1.30V	±0.5 mA
V_{CC} = 0.9V	±20 μA
Free Air Operating Temperature (T_A)	−40°C to +85°C
Minimum Input Edge Rate (Δt/ΔV)	
V_{IN} = 0.8V to 2.0V, V_{CC} = 3.0V	10 ns/V

Note 1: Absolute Maximum Ratings: are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The “Recommended Operating Conditions” table will define the conditions for actual device operation.

Note 2: I_O Absolute Maximum Rating must be observed.

Note 3: Unused inputs must be held HIGH or LOW. They may not float.

DC Electrical Characteristics

Symbol	Parameter	V_{CC} (V)	T_A = +25°C Min	Max	T_A = −40°C to +85°C Min	Max	Units	Conditions
V_{IH}	HIGH Level	0.90	0.65 x V_{CC}		0.65 x V_{CC}		V	
	Input Voltage	1.10 ≤ V_{CC} ≤ 1.30	0.65 x V_{CC}		0.65 x V_{CC}			
		1.40 ≤ V_{CC} ≤ 1.60	0.65 x V_{CC}		0.65 x V_{CC}			
		1.65 ≤ V_{CC} ≤ 1.95	0.65 x V_{CC}		0.65 x V_{CC}			
		2.30 ≤ V_{CC} ≤ 2.70	1.6		1.6			
		3.00 ≤ V_{CC} ≤ 3.60	2.1		2.1			
V_{IL}	LOW Level	0.90		0.35 x V_{CC}		0.35 x V_{CC}	V	
	Input Voltage	1.10 ≤ V_{CC} ≤ 1.30		0.35 x V_{CC}		0.35 x V_{CC}		
		1.40 ≤ V_{CC} ≤ 1.60		0.35 x V_{CC}		0.35 x V_{CC}		
		1.65 ≤ V_{CC} ≤ 1.95		0.35 x V_{CC}		0.35 x V_{CC}		
		2.30 ≤ V_{CC} ≤ 2.70		0.7		0.7		
		3.00 ≤ V_{CC} ≤ 3.60		0.9		0.9		
V_{OL}	LOW Level	0.90		0.1		0.1	V	I_{OL} = 20 μA
	Output Voltage	1.10 ≤ V_{CC} ≤ 1.30		0.1		0.1		
		1.40 ≤ V_{CC} ≤ 1.60		0.1		0.1		
		1.65 ≤ V_{CC} ≤ 1.95		0.1		0.1		
		2.30 ≤ V_{CC} ≤ 2.70		0.1		0.1		
		3.00 ≤ V_{CC} ≤ 3.60		0.1		0.1		
		1.10 ≤ V_{CC} ≤ 1.30		0.30 x V_{CC}		0.30 x V_{CC}		I_{OL} = 0.5 mA
		1.40 ≤ V_{CC} ≤ 1.60		0.31		0.37		I_{OL} = 1 mA
		1.65 ≤ V_{CC} ≤ 1.95		0.31		0.35		I_{OL} = 1.5 mA
		2.30 ≤ V_{CC} ≤ 2.70		0.31		0.33		I_{OL} = 2.1 mA
		3.00 ≤ V_{CC} ≤ 3.60		0.31		0.33		I_{OL} = 2.6 mA
I_{IN}	Input Leakage Current	0.90 to 3.60		±0.1		±0.5	μA	0 ≤ V_I ≤ 3.6V
I_{OFF}	Power Off Leakage Current	0		0.5		0.5	μA	0 ≤ (V_I, V_O) ≤ 3.6V
I_{CC}	Quiescent Supply Current	0.90 to 3.60		0.9		0.9	μA	V_I = V_{CC} or GND

AC Electrical Characteristics

Symbol	Parameter	V_{CC} (V)	$T_A = +25°C$ Min	Typ	Max	$T_A = -40°C$ to $+85°C$ Min	Max	Units	Conditions	Figure Number
t_{PZL}	Propagation Delay	0.90		24						
t_{PLZ}		$1.10 \le V_{CC} \le 1.30$	4.0	9	18.7	3.5	30.9			
		$1.40 \le V_{CC} \le 1.60$	2.0	6	12.4	1.5	13.9	ns	$C_L = 10$ pF	Figures 1, 2
		$1.65 \le V_{CC} \le 1.95$	1.5	5	9.6	1.0	12.1		$R_U = 5000\Omega$	
		$2.30 \le V_{CC} \le 2.70$	1.0	4	9.0	0.8	10.0		$R_D = 5000\Omega$	
		$3.00 \le V_{CC} \le 3.60$	1.0	3	8.7	0.5	9.0			
t_{PZL}	Propagation Delay	0.90		27						
t_{PLZ}		$1.10 \le V_{CC} \le 1.30$	5.0	10	20.0	4.5	33.9			
		$1.40 \le V_{CC} \le 1.60$	3.0	7	13.3	2.5	16.0	ns	$C_L = 10$ pF	Figures 1, 2
		$1.65 \le V_{CC} \le 1.95$	2.0	5	10.3	2.0	12.6		$R_U = 5000\Omega$	
		$2.30 \le V_{CC} \le 2.70$	1.5	4	9.4	1.0	10.2		$R_D = 5000\Omega$	
		$3.00 \le V_{CC} \le 3.60$	1.0	3	9.1	0.5	9.7			
t_{PZL}	Propagation Delay	0.90		34						
t_{PLZ}		$1.10 \le V_{CC} \le 1.30$	6.0	12	24.0	5.0	43.0			
		$1.40 \le V_{CC} \le 1.60$	4.0	8	16.0	3.0	18.0	ns	$C_L = 10$ pF	Figures 1, 2
		$1.65 \le V_{CC} \le 1.95$	2.0	6	12.0	2.0	14.0		$R_U = 5000\Omega$	
		$2.30 \le V_{CC} \le 2.70$	1.0	5	11.0	1.0	12.0		$R_D = 5000\Omega$	
		$3.00 \le V_{CC} \le 3.60$	0.8	4	10.0	0.5	11.0			
C_{IN}	Input Capacitance	0		2				pF		
C_{OUT}	Output Capacitance	0		4.0				pF		
C_{PD}	Power Dissipation Capacitance	0.9 to 3.60		6				pF	$V_I = 0V$ or V_{CC}, f = 10 MHz	

AC Loading and Waveforms

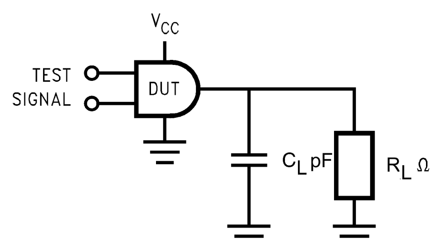


FIGURE 1. AC Test Circuit

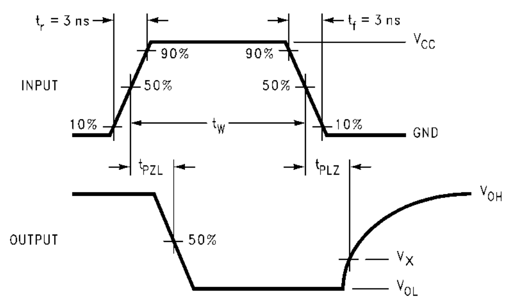


FIGURE 2. Waveform for Inverting and Non-Inverting Functions

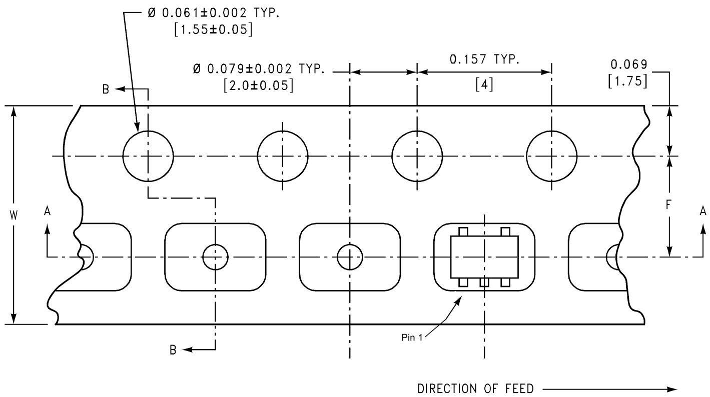
Symbol	V_{CC}					
	3.3V ± 0.3V	**2.5V ± 0.2V**	**1.8V ± 0.15V**	**1.5V ± 0.10V**	**1.2V ± 0.10V**	**0.9V**
V_{mi}	1.5V	$V_{CC}/2$	$V_{CC}/2$	$V_{CC}/2$	$V_{CC}/2$	$V_{CC}/2$
V_X	$V_{OL} + 0.3V$	$V_{OL} + 0.15V$	$V_{OL} + 0.15V$	$V_{OL} + 0.1V$	$V_{OL} + 0.1V$	$V_{OL} + 0.1V$

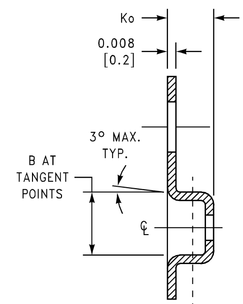
Tape and Reel Specification

TAPE FORMAT for SC70

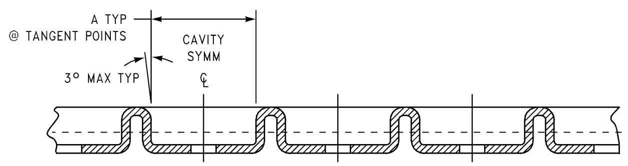
Package Designator	Tape Section	Number Cavities	Cavity Status	Cover Tape Status
P5X	Leader (Start End)	125 (typ)	Empty	Sealed
	Carrier	3000	Filled	Sealed
	Trailer (Hub End)	75 (typ)	Empty	Sealed

TAPE DIMENSIONS inches (millimeters)

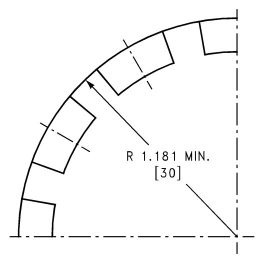




SECTION B-B



SECTION A-A



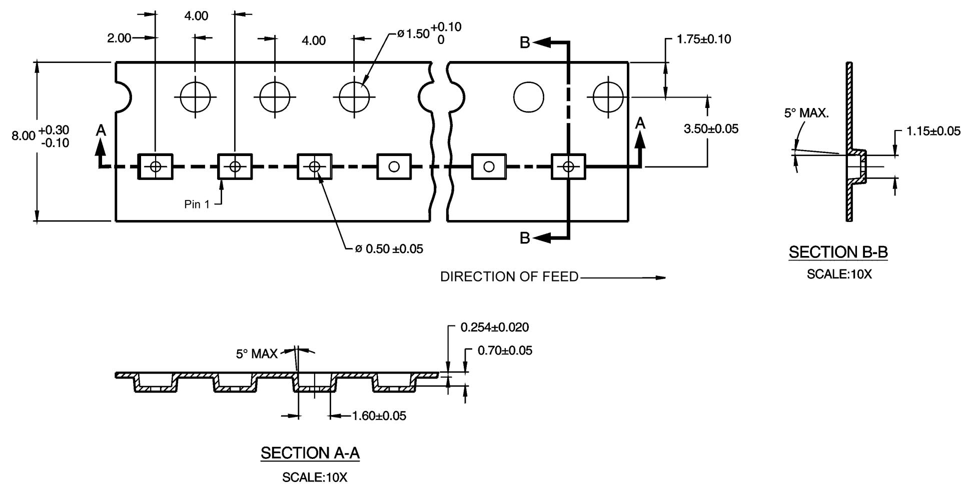
BEND RADIUS NOT TO SCALE

Tape and Reel Specification (Continued)

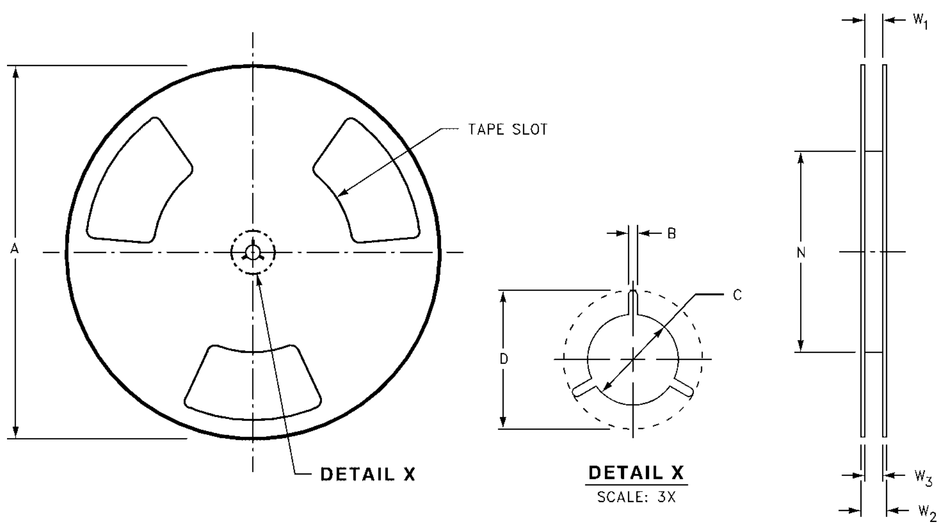
TAPE FORMAT for MicroPak

Package Designator	Tape Section	Number Cavities	Cavity Status	Cover Tape Status
L6X	Leader (Start End)	125 (typ)	Empty	Sealed
	Carrier	5000	Filled	Sealed
	Trailer (Hub End)	75 (typ)	Empty	Sealed

TAPE DIMENSIONS inches (millimeters)

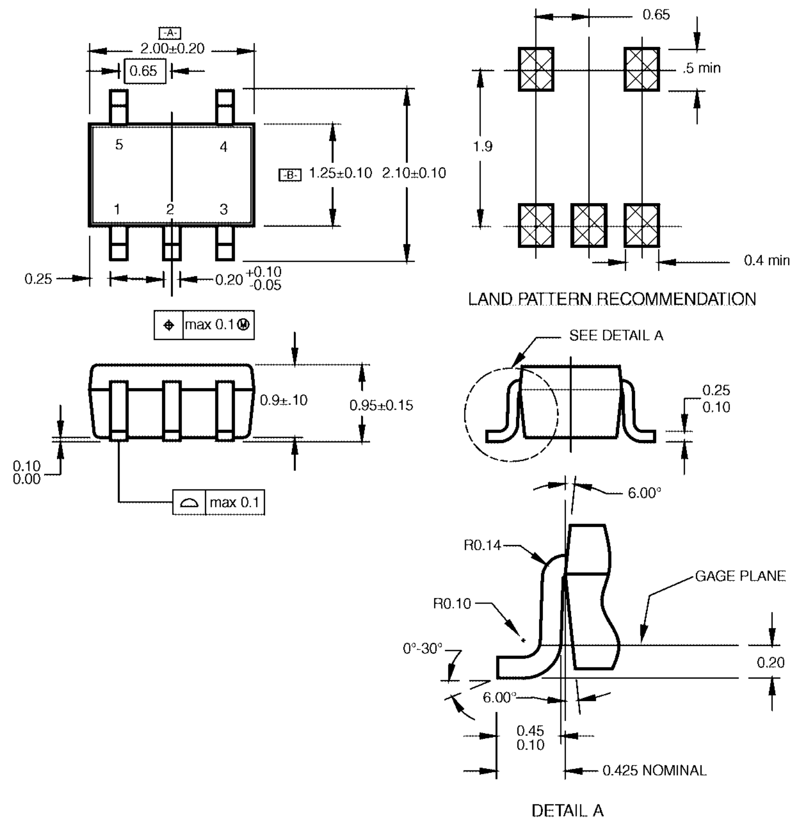


REEL DIMENSIONS inches (millimeters)



Tape Size	A	B	C	D	N	W1	W2	W3
8 mm	7.0 (177.8)	0.059 (1.50)	0.512 (13.00)	0.795 (20.20)	2.165 (55.00)	0.331 + 0.059/–0.000 (8.40 + 1.50/–0.00)	0.567 (14.40)	W1 + 0.078/–0.039 (W1 + 2.00/–1.00)

Physical Dimensions inches (millimeters) unless otherwise noted



NOTES:

A. CONFORMS TO EIAJ REGISTERED OUTLINE DRAWING SC88A.

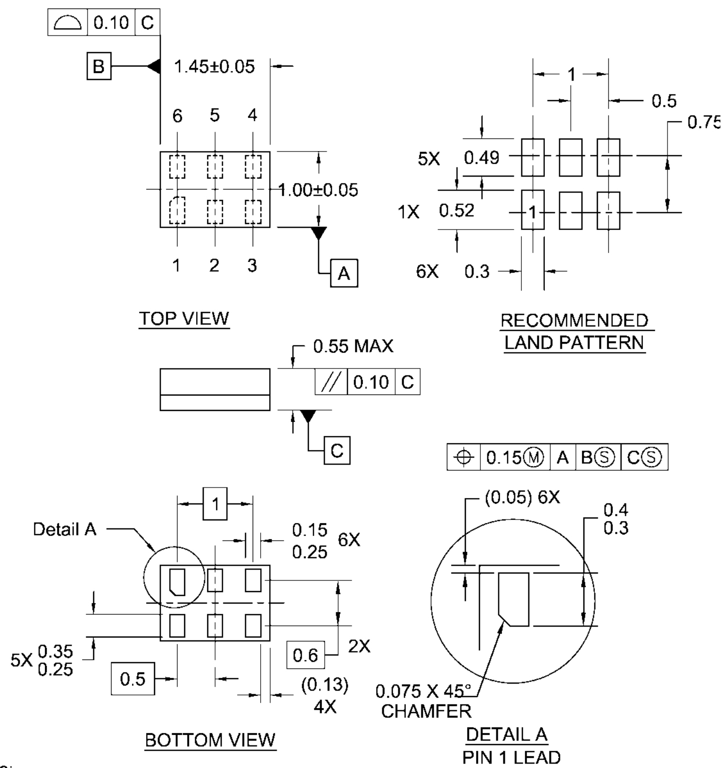
B. DIMENSIONS DO NOT INCLUDE BURRS OR MOLD FLASH.

C. DIMENSIONS ARE IN MILLIMETERS.

MAA05ARevC

5-Lead SC70, EIAJ SC-88a, 1.25mm Wide
Package Number MAA05A

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



Notes:

1. JEDEC PACKAGE REGISTRATION IS ANTICIPATED
2. DIMENSIONS ARE IN MILLIMETERS
3. DRAWING CONFORMS TO ASME Y14.5M-1994

MAC06ARevB

6-Lead MicroPak, 1.0mm Wide
Package Number MAC06A