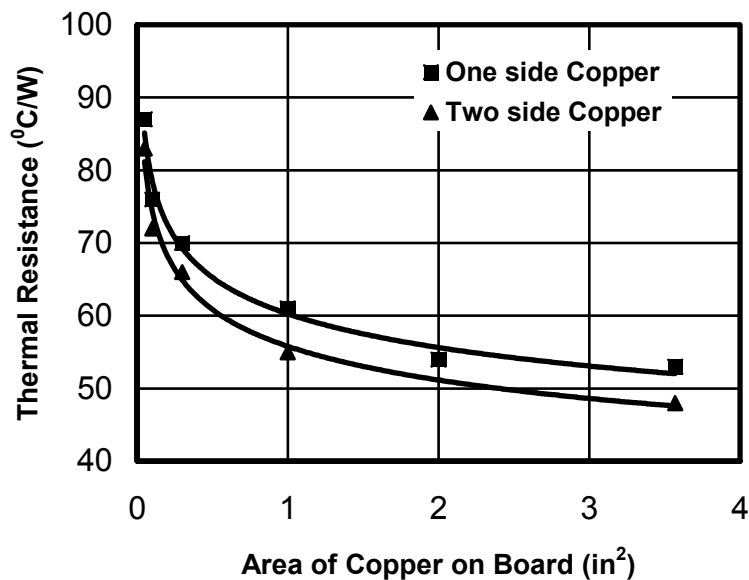


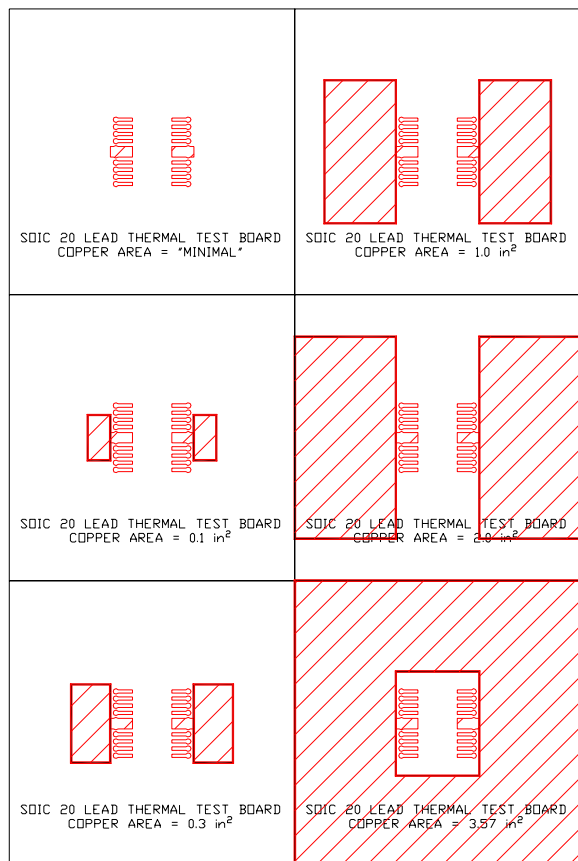
Effect of PWB Copper Area on Thermal Performance of 20-Lead SOIC LB Package

Thermal Resistance ($R_{\theta JA}$) vs. Copper Area on PWB



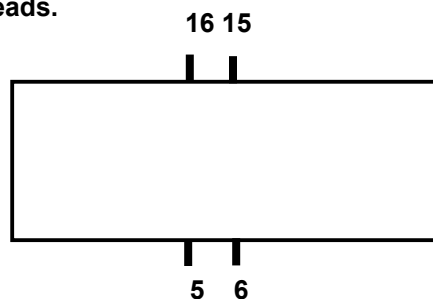
Variable Copper Area Test Board

Red-hatched area is copper on the surface of the PWB



Copper Area Must Be Connected to Package Ground Leads

The 20 lead LB package has 4 ground leads as shown. Additional copper area must be connected to these leads.



Using a Double-Layer PWB

For the double-layer copper board, the copper area on the bottom is identical to the area on the top. Both the top and bottom layers are thermally connected through vias placed on each ground lead.