## **HSD278**

## Silicon Schottky Barrier Diode

# **HITACHI**

ADE-208-1015A (Z) Rev.1 Jan. 2001

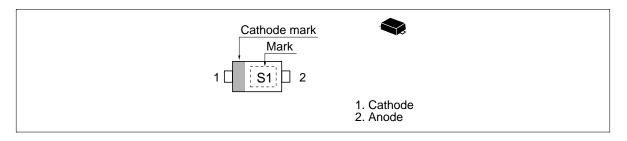
#### **Features**

- Low forward voltage, Low capacitance.
- Super small Flat Package (SFP) is suitable for surface mount design.

### **Ordering Information**

Type No.	Laser Mark	Package Code
HSD278	S1	SFP

#### **Outline**





#### **HSD278**

### **Absolute Maximum Ratings** (Ta = 25°C)

Item	Symbol	Value	Unit
Repetitive peak reverse voltage	$V_{RRM}$	30	V
Reverse voltage	$V_R$	30	V
Non-Repetitive peak forward surge current	I <sub>FSM</sub> *	200	mA
Peak forward current	I <sub>FM</sub>	150	mA
Average rectified current	I <sub>o</sub>	30	mA
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

Note: 10 msec sine wave 1 pulse

### **Electrical Characteristics** (Ta = 25°C)

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Forward voltage	$V_{F1}$	_	_	0.30	V	I <sub>F</sub> = 1 mA
	$V_{F2}$	_	_	0.95		I <sub>F</sub> = 30 mA
Reverse current	I <sub>R</sub>	_	_	700	nA	V <sub>R</sub> = 10 V
Capacitance	С	_	_	1.50	pF	V <sub>R</sub> = 1 V, f = 1 MHz
ESD-Capability *1	<u> </u>	100		_	V	$C = 200 \text{ pF}, R_L = 0 \Omega$ , Both forward and reverse direction 1 pulse.

Notes: 1. Failure criterion ;  $I_{R} \geq 1.4~\mu\text{A}$  at  $V_{R}$  = 10 V

2. Please do not use the soldering iron due to avoid high stress to the SFP package.

#### **Main Characteristic**

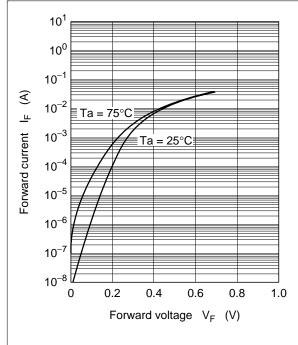


Fig.1 Forward current Vs. Forward voltage

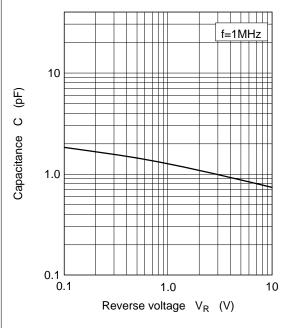


Fig.3 Capacitance Vs. Reverse voltage

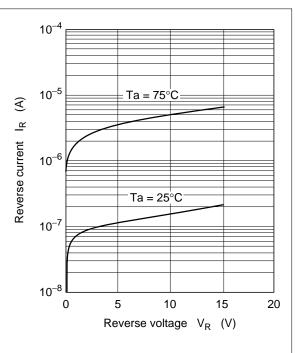
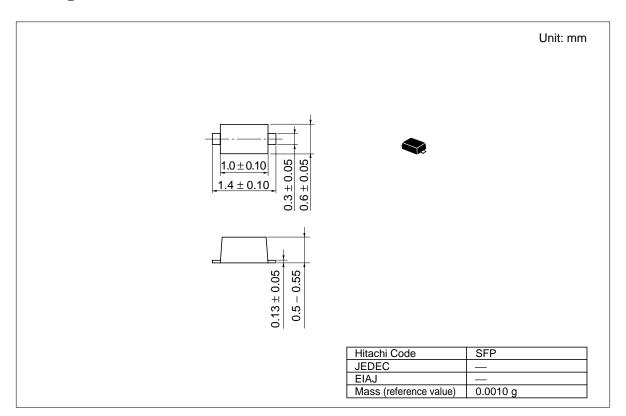


Fig.2 Reverse current Vs. Reverse voltage

## **HSD278**

## **Package Dimensions**



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