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HSB88WA

Silicon Schottky Barrier Diode for High Speed Switching



ADE-208-965 (Z)

Rev.0 Aug. 2000

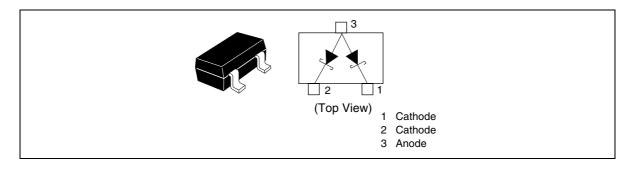
Features

- Low reverse current, Low capacitance.
- CMPAK package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Code
HSB88WA	C7	CMPAK

Pin Arrangement



HSB88WA

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Reverse voltage	V _R	10	V
Average rectified current	I ₀ *	15	mA
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

Note: Per one device.

Electrical Characteristics

 $(Ta = 25^{\circ}C) *^{1}$

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Forward voltage	V _{F1}	0.350	o —	0.420) V	I _F = 1 mA
	V_{F2}	0.500	o —	0.580)	I _F = 10 mA
Reverse current	I _{R1}	_	_	0.2	μΑ	V _R = 2 V
	I _{R2}	_	_	10	-	V _R = 10 V
Capacitance	С	_	_	0.80	pF	V _R = 0 V, f = 1 MHz
Capacitance deviation	ΔC	_	_	0.10	pF	V _R = 0 V, f = 1 MHz
Forward voltage deviation	$\Delta V_{_{F}}$	_	_	10	mV	I _F = 10 mA
ESD-Capability *2	_	30	_	_	V	$C = 200 \text{ pF}, R = 0 \Omega$, Both forward and reverse direction 1 pulse.

Notes: 1. Per one device.

2. Failure criterion ; $\rm I_{_{R}} \! > 0.4~\mu A$ at $\rm V_{_{R}} \! = 2~V$

Main Characteristic

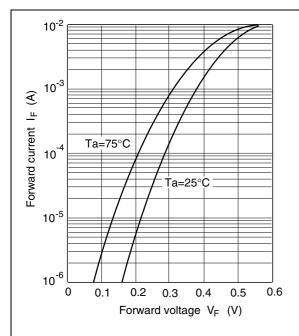


Fig.1 Forward current Vs. Forward voltage

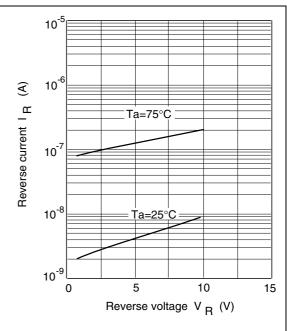
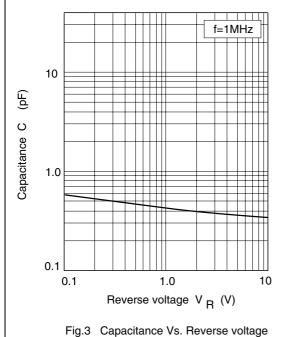
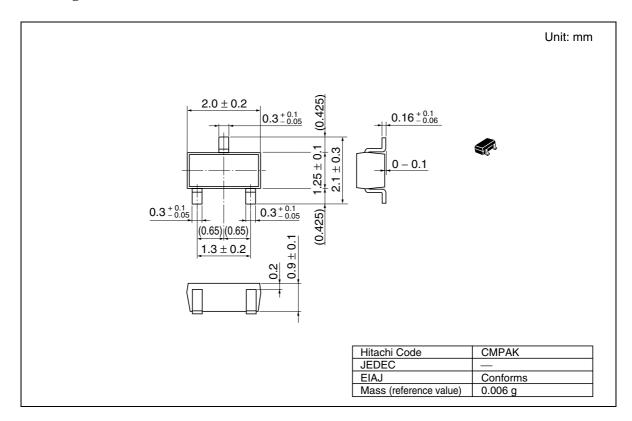


Fig.2 Reverse current Vs. Reverse voltage



HSB88WA

Package Dimensions



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