Preferred Device

Small Signal MOSFET 250 mAmps, 200 Volts

N-Channel TO-92

Features

• Pb-Free Package is Available*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain-Source Voltage	V _{DS}	200	Vdc
Gate–Source Voltage – Continuous – Non–repetitive (t _p ≤ 50 μs)	V _{GS} V _{GSM}	±20 ±30	Vdc Vpk
Drain Current Continuous (Note 1) Pulsed (Note 2)	I _D	250 500	mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	P _D	350	mW
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to 150	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

- The Power Dissipation of the package may result in a lower continuous drain current.
- 2. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.

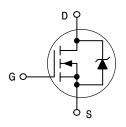


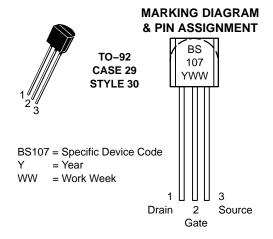
ON Semiconductor®

http://onsemi.com

250 mA, 200 V $R_{DS(on)} = 14~\Omega~(BS107)$ $R_{DS(on)} = 6.4~\Omega~(BS107A)$

N-Channel





ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 4 of this data sheet.

Preferred devices are recommended choices for future use and best overall value.

^{*}For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Zero-Gate-Voltage Drain Current (V _{DS} = 130 Vdc, V _{GS} = 0)	I _{DSS}	-	_	30	nAdc
Drain–Source Breakdown Voltage (V _{GS} = 0, I _D = 100 μAdc)	V _{(BR)DSX}	200	-	-	Vdc
Gate Reverse Current (V _{GS} = 15 Vdc, V _{DS} = 0)	I _{GSS}	_	0.01	10	nAdc
ON CHARACTERISTICS (Note 3)					
Gate Threshold Voltage (I _D = 1.0 mAdc, V _{DS} = V _{GS})	V _{GS(Th)}	1.0	_	3.0	Vdc
Static Drain–Source On Resistance BS107 ($V_{GS} = 2.6 \text{ Vdc}$, $I_D = 20 \text{ mAdc}$) ($V_{GS} = 10 \text{ Vdc}$, $I_D = 200 \text{ mAdc}$) BS107A ($V_{GS} = 10 \text{ Vdc}$) ($I_D = 100 \text{ mAdc}$)	r _{DS(on)}		- - 4.5	28 14 6.0	Ω
(I _D = 250 mAdc)		-	4.8	6.4	
SMALL-SIGNAL CHARACTERISTICS Input Capacitance (V _{DS} = 25 Vdc, V _{GS} = 0, f = 1.0 MHz)	C _{iss}	-	60	_	pF
Reverse Transfer Capacitance (V _{DS} = 25 Vdc, V _{GS} = 0, f = 1.0 MHz)	C _{rss}	-	6.0	_	pF
Output Capacitance $(V_{DS} = 25 \text{ Vdc}, V_{GS} = 0, f = 1.0 \text{ MHz})$	C _{oss}	ı	30	_	pF
Forward Transconductance (V _{DS} = 25 Vdc, I _D = 250 mAdc)	9fs	200	400	_	mmhos
SWITCHING CHARACTERISTICS					
Turn-On Time	t _{on}	-	6.0	15	ns
Turn-Off Time	t _{off}	_	12	15	ns

^{3.} Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.

RESISTIVE SWITCHING

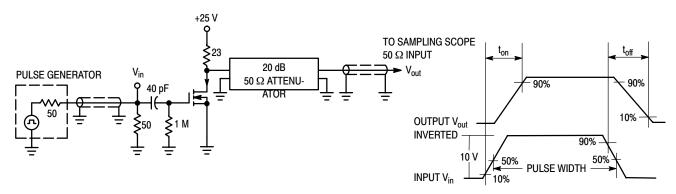


Figure 1. Switching Test Circuit

Figure 2. Switching Waveforms

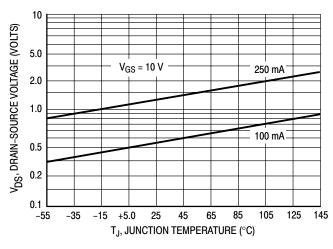


Figure 3. On Voltage versus Temperature

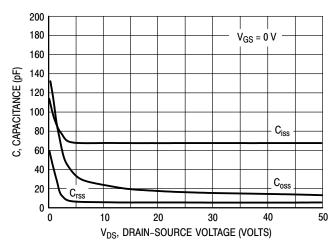


Figure 4. Capacitance Variation

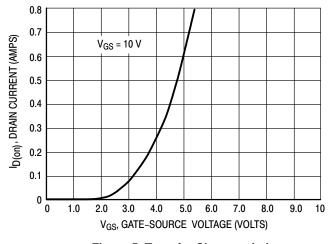


Figure 5. Transfer Characteristic

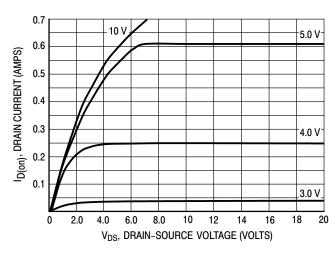


Figure 6. Output Characteristic

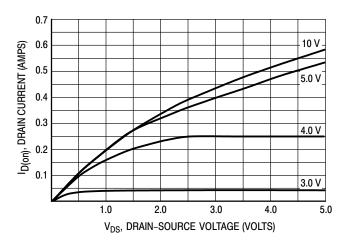


Figure 7. Saturation Characteristic

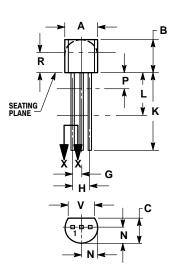
ORDERING INFORMATION

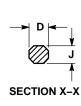
Device	Package	Shipping [†]	
BS107	TO-92	1000 Unit / Box	
BS107G	TO-92 (Pb-Free)		
BS107RLRA	TO-92	2000 / Tape & Reel	
BS107RL1	TO-92	2000 / Tape & Reel	
BS107A	TO-92	1000 Units / Box	
BS107AG	TO-92 (Pb-Free)		
BS107ARLRM	TO-92	2000 Ammo Pack	
BS107ARLRP	TO-92	2000 Ammo Pack	
BS107ARL1	TO-92		
BS107ARL1G	TO-92 (Pb-Free)	2000 / Tape & Reel	

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 **ISSUE AL**





- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
 4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.175	0.205	4.45	5.20	
В	0.170	0.210	4.32	5.33	
С	0.125	0.165	3.18	4.19	
D	0.016	0.021	0.407	0.533	
G	0.045	0.055	1.15	1.39	
Н	0.095	0.105	2.42	2.66	
J	0.015	0.020	0.39	0.50	
K	0.500		12.70		
L	0.250		6.35		
N	0.080	0.105	2.04	2.66	
P		0.100		2.54	
R	0.115		2.93		
v	0.135		3 43		

STYLE 30:
PIN 1. DRAIN
2. GATE
3. SOURCE

ON Semiconductor and are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 61312, Phoenix, Arizona 85082–1312 USA Phone: 480–829–7710 or 800–344–3860 Toll Free USA/Canada Fax: 480–829–7709 or 800–344–3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800–282–9855 Toll Free USA/Canada

Japan: ON Semiconductor, Japan Customer Focus Center 2–9–1 Kamimeguro, Meguro–ku, Tokyo, Japan 153–0051 Phone: 81–3–5773–3850

ON Semiconductor Website: http://onsemi.com

Order Literature: http://www.onsemi.com/litorder

For additional information, please contact your local Sales Representative.