

Schottky Barrier Diodes

These Schottky barrier diodes are designed for high speed switching applications, circuit protection, and voltage clamping. Extremely low forward voltage reduces conduction loss. Miniature surface mount package is excellent for hand held and portable applications where space is limited.

- Extremely Fast Switching Speed
- Low Forward Voltage — 0.35 Volts (Typ) @ $I_F = 10$ mAdc

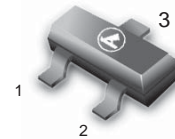
ORDERING INFORMATION

Device	Package	Shipping
BAT54LT1	SOT-23	3000/Tape & Reel

Preferred: devices are recommended choices for future use and best overall value.

BAT54LT1

30 VOLTS SILICON HOT-CARRIER DETECTOR AND SWITCHING DIODES



CASE 318, STYLE 8
SOT-23 (TO-236AB)



DEVICE MARKING

BAT54LT1 = JV3

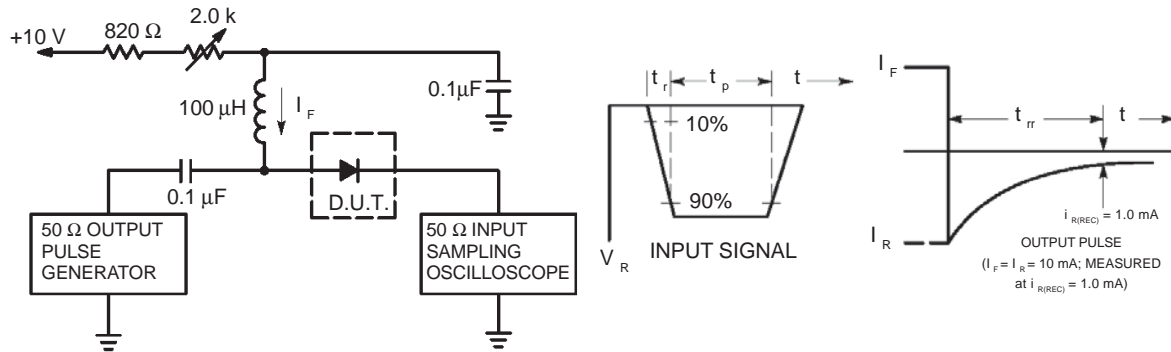
MAXIMUM RATINGS (T_J = 125°C unless otherwise noted)

Rating	Symbol	Max	Unit
Reverse Voltage	V _R	30	Volts
Forward Power Dissipation @ T _A = 25°C	P _F	200	mW
Derate above 25°C		2.0	mW/°C
Forward Current(DC)	I _F	200Max	mA
Junction Temperature	T _J	125Max	°C
Storage Temperature Range	T _{stg}	-55 to +150	°C

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
Reverse Breakdown Voltage (I _R = 10 μA)	V _{(BR)R}	30	—	—	Volts
Total Capacitance (V _R = 1.0 V, f = 1.0 MHz)	C _T	—	7.6	10	pF
Reverse Leakage (V _R = 25 V)	I _R	—	0.5	2.0	μAdc
Forward Voltage (I _F = 0.1 mAdc)	V _F	—	0.22	0.24	Vdc
Forward Voltage (I _F = 30 mAdc)	V _F	—	0.41	0.5	Vdc
Forward Voltage (I _F = 100 mAdc)	V _F	—	0.52	0.8	Vdc
Reverse Recovery Time (I _F = I _R = 10 mAdc, I _{R(REC)} = 1.0 mAdc, Figure 1)	t _{rr}	—	—	5.0	ns
Forward Voltage (I _F = 1.0 mAdc)	V _F	—	0.29	0.32	Vdc
Forward Voltage (I _F = 10 mAdc)	V _F	—	0.35	0.40	Vdc
Forward Current (DC)	I _F	—	—	200	mAdc
Repetitive Peak Forward Current	I _{FRM}	—	—	300	mAdc
Non-Repetitive Peak Forward Current (t < 1.0 s)	I _{FSM}	—	—	600	mAdc

BAT54LT1



- Notes: 1. A 2.0 kΩ variable resistor adjusted for a Forward Current (I_F) of 10mA.
 2. Input pulse is adjusted so $I_{R(\text{peak})}$ is equal to 10mA.
 3. $t_p \gg t_{rr}$

Figure 1. Recovery Time Equivalent Test Circuit

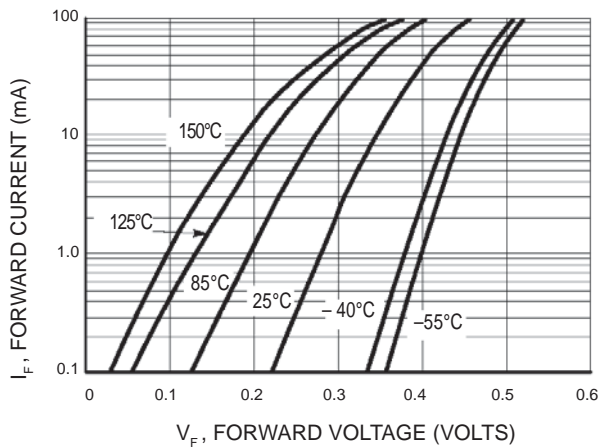


Figure 2. Forward Voltage

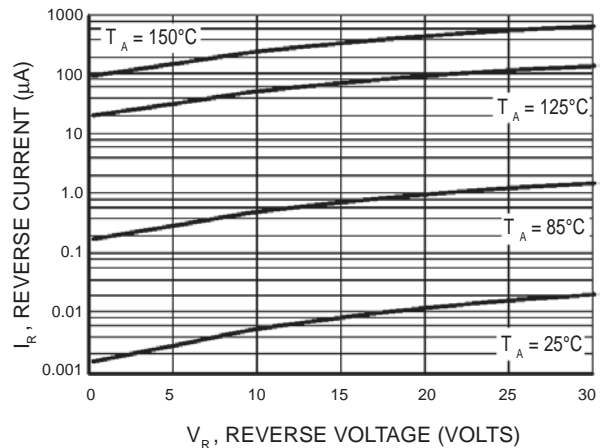


Figure 3. Leakage Current

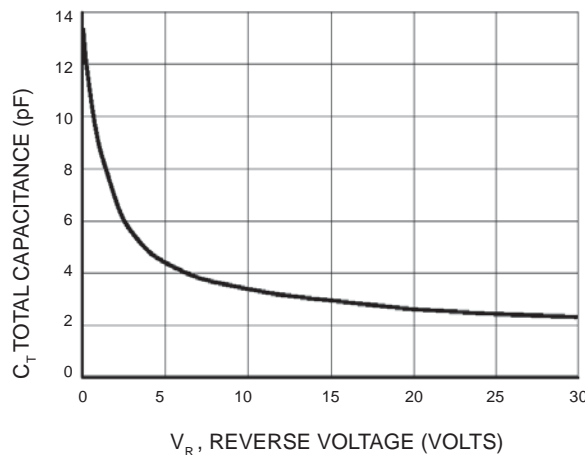


Figure 4. Total Capacitance