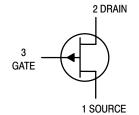


JFET Amplifiers P-Channel — Depletion



2N5460 2N5461 2N5462

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain-Gate Voltage	V _{DG}	40	Vdc
Reverse Gate-Source Voltage	VGSR	40	Vdc
Forward Gate Current	IG(f)	10	mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	PD	350 2.8	mW mW/°C
Junction Temperature Range	TJ	-65 to +135	°C
Storage Channel Temperature Range	T _{stg}	-65 to +150	°C



ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic		Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS						
Gate–Source Breakdown Voltage (I _G = 10 μ Adc, V _{DS} = 0)	2N5460, 2N5461, 2N5462	V(BR)GSS	40	_	_	Vdc
Gate Reverse Current (VGS = 20 Vdc, VDS = 0) (VGS = 30 Vdc, VDS = 0) (VGS = 20 Vdc, VDS = 0, TA = 100°C) (VGS = 30 Vdc, VDS = 0, TA = 100°C)	2N5460, 2N5461, 2N5462 2N5460, 2N5461, 2N5462	I _{GSS}	_ _		5.0 1.0	nAdc μAdc
Gate–Source Cutoff Voltage (V _{DS} = 15 Vdc, I _D = 1.0 μAdc)	2N5460 2N5461 2N5462	VGS(off)	0.75 1.0 1.8	_ _ _	6.0 7.5 9.0	Vdc
$\label{eq:Gate-Source Voltage} \begin{tabular}{ll} Gate-Source Voltage \\ (V_{DS} = 15 \mbox{ Vdc, } I_{D} = 0.1 \mbox{ mAdc)} \\ (V_{DS} = 15 \mbox{ Vdc, } I_{D} = 0.2 \mbox{ mAdc)} \\ (V_{DS} = 15 \mbox{ Vdc, } I_{D} = 0.4 \mbox{ mAdc)} \\ \end{tabular}$	2N5460 2N5461 2N5462	VGS	0.5 0.8 1.5	_ _ _	4.0 4.5 6.0	Vdc

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Тур	Max	Unit	
ON CHARACTERISTICS							
Zero-Gate-Voltage Drain Current (V _{DS} = 15 Vdc, V _{GS} = 0, f = 1.0 kHz)	2N5460 2N5461 2N5462	IDSS	-1.0 -2.0 -4.0		-5.0 -9.0 -16	mAdc	
SMALL-SIGNAL CHARACTERISTICS							
Forward Transfer Admittance (V _{DS} = 15 Vdc, V _{GS} = 0, f = 1.0 kHz)	2N5460 2N5461 2N5462	lyfs	1000 1500 2000		4000 5000 6000	μmhos	
Output Admittance (V _{DS} = 15 Vdc, V _{GS} = 0, f = 1.0 kHz)		y _{os}	_	_	75	μmhos	
Input Capacitance (V _{DS} = 15 Vdc, V _{GS} = 0, f = 1.0 MHz)		C _{iss}	_	5.0	7.0	pF	
Reverse Transfer Capacitance (V _{DS} = 15 Vdc, V _{GS} = 0, f = 1.0 MHz)		C _{rss}	_	1.0	2.0	pF	
FUNCTIONAL CHARACTERISTICS							
Equivalent Short–Circuit Input Noise Voltage (VDS = 15 Vdc, VGS = 0, f = 100 Hz, BW = 1.0 Hz)		e _n	_	60	115	nV/√Hz	

DRAIN CURRENT versus GATE SOURCE VOLTAGE

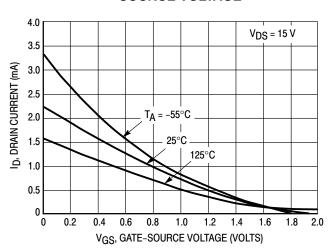


Figure 1. $V_{GS(off)} = 2.0 \text{ Volts}$

FORWARD TRANSFER ADMITTANCE versus DRAIN CURRENT

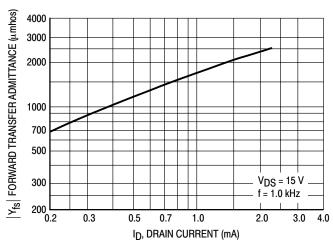


Figure 4. V_{GS(off)} = 2.0 Volts

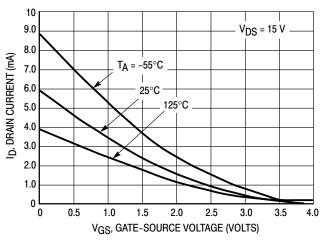


Figure 2. V_{GS(off)} = 4.0 Volts

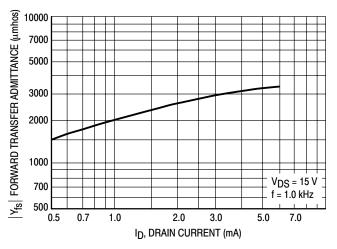


Figure 5. VGS(off) = 4.0 Volts

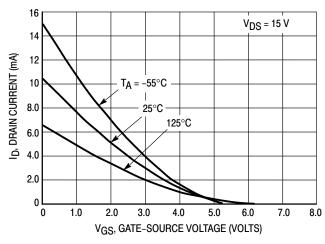


Figure 3. V_{GS(off)} = 5.0 Volts

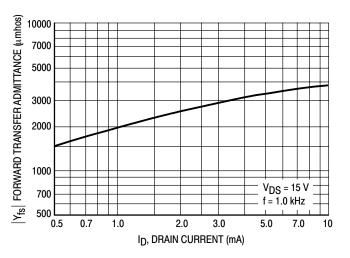


Figure 6. $V_{GS(off)} = 5.0 \text{ Volts}$

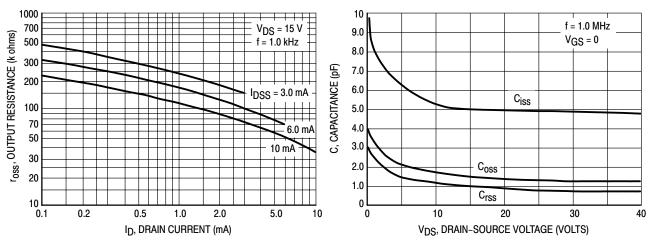


Figure 7. Output Resistance versus Drain Current

Figure 8. Capacitance versus Drain-Source Voltage

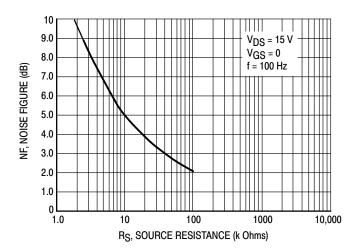
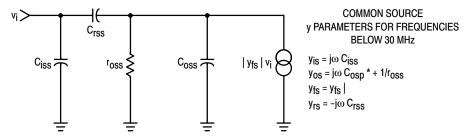


Figure 9. Noise Figure versus Source Resistance



*Cosp is Coss in parallel with Series Combination of Ciss and Crss.

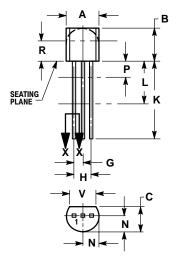
NOTE:

 Graphical data is presented for dc conditions. Tabular data is given for pulsed conditions (Pulse Width = 630 ms, Duty Cycle = 10%).

Figure 10. Equivalent Low Frequency Circuit

PACKAGE DIMENSIONS

TO-92 (TO-226AA) CASE 29-11 ISSUE AL





STYLE 7: PIN 1. SOURCE 2. DRAIN 3. GATE

- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
 4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.175	0.205	4.45	5.20	
В	0.170	0.210	4.32	5.33	
C	0.125	0.165	3.18	4.19	
D	0.016	0.021	0.407	0.533	
G	0.045	0.055	1.15	1.39	
Н	0.095	0.105	2.42	2.66	
J	0.015	0.020	0.39	0.50	
K	0.500		12.70		
L	0.250		6.35		
N	0.080	0.105	2.04	2.66	
Р		0.100		2.54	
R	0.115		2.93		
٧	0.135		3.43		





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