



# SPN2054

## N-Channel Enhancement Mode MOSFET

### DESCRIPTION

The SPN2054 is the N-Channel logic enhancement mode power field effect transistors are produced using high cell density , DMOS trench technology.

This high density process is especially tailored to minimize on-state resistance. These devices are particularly suited for low voltage application, such as DC/DC converter and Desktop computer power management.

The package is universally preferred for commercial industrial surface mount applications

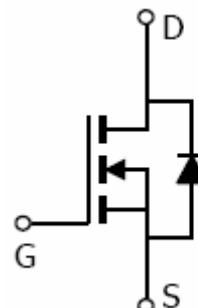
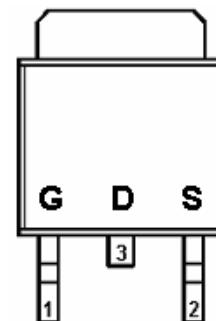
### FEATURES

- ◆ 20V/12A,R<sub>DS(ON)</sub>=40mΩ@V<sub>GS</sub>=10V
- ◆ 20V/ 7A,R<sub>DS(ON)</sub>=45mΩ@V<sub>GS</sub>=4.5V
- ◆ 20V/ 4A,R<sub>DS(ON)</sub>=50mΩ@V<sub>GS</sub>=2.5V
- ◆ 20V/ 2A,R<sub>DS(ON)</sub>=60mΩ@V<sub>GS</sub>=1.8V
- ◆ Super high density cell design for extremely low RDS (ON)
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ TO-252-2L package design

### APPLICATIONS

- Power Management in Desktop Computer
- DC/DC Converter
- LCD Display inverter

### PIN CONFIGURATION(TO-252-2L)



### PART MARKING



A : Lot Code  
B : Date Code



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### PIN DESCRIPTION

Pin	Symbol	Description
1	G	Gate
2	S	Source
3	D	Drain

### ORDERING INFORMATION

Part Number	Package	Part Marking
SPN2054T252RG	TO-252-2L	SPN2054

※ Week Code : A ~ Z( 1 ~ 26 ) ; a ~ z( 27 ~ 52 )

※ SPN2054T252RG : Tape Reel ; Pb – Free

### ABSOULTE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Typical	Unit
Drain-Source Voltage	V <sub>DSS</sub>	20	V
Gate –Source Voltage	V <sub>GSS</sub>	±12	V
Continuous Drain Current(T <sub>J</sub> =150°C)	T <sub>A</sub> =25°C	12	A
	T <sub>A</sub> =70°C	8	
Pulsed Drain Current	I <sub>DM</sub>	20	A
Continuous Source Current(Diode Conduction)	I <sub>S</sub>	12	A
Power Dissipation	T <sub>A</sub> =25°C	40	W
	T <sub>A</sub> =70°C	20	
Operating Junction Temperature	T <sub>J</sub>	-55/150	°C
Storage Temperature Range	T <sub>STG</sub>	-55/150	°C
Thermal Resistance-Junction to Ambient	R <sub>θJA</sub>	105	°C/W



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### ELECTRICAL CHARACTERISTICS

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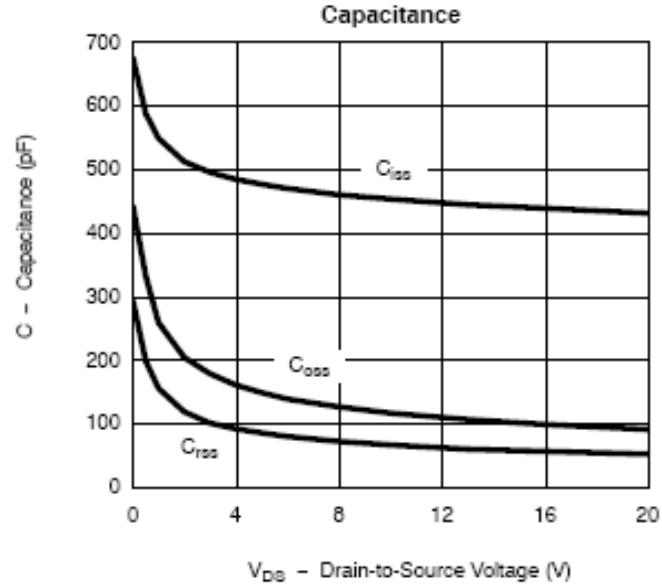
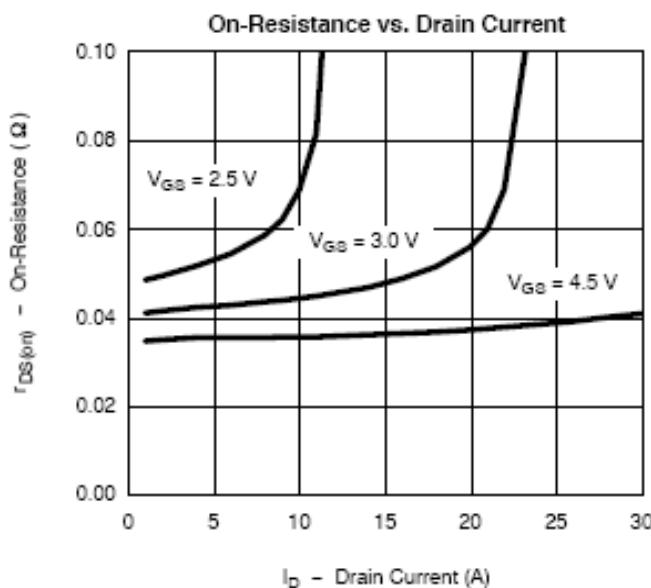
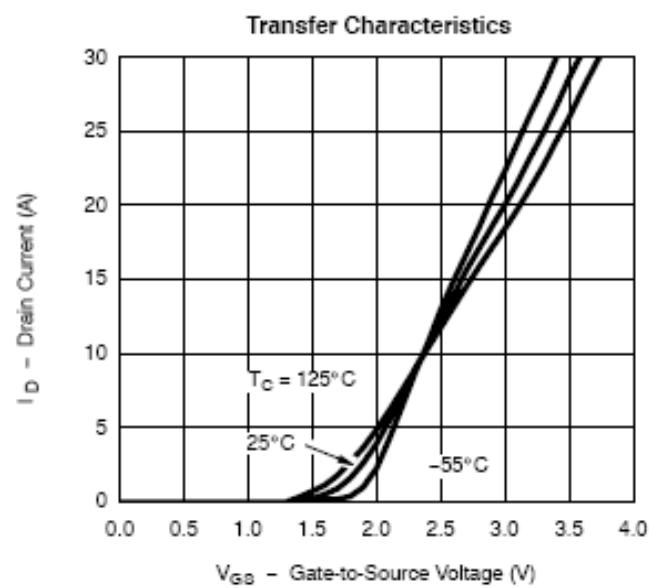
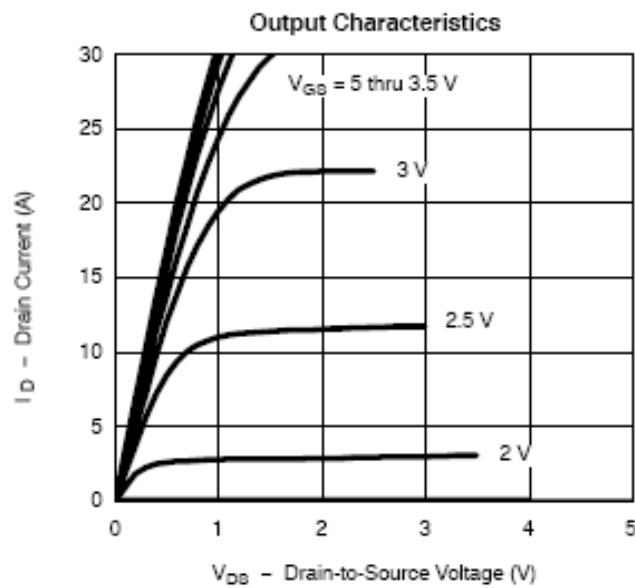
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	V(BR)DSS	VGS=0V, ID=250uA	20			V
Gate Threshold Voltage	VGS(th)	VDS=VGS, ID=250uA	0.36		1.0	
Gate Leakage Current	IGSS	VDS=0V, VGS=±12V			±100	nA
Zero Gate Voltage Drain Current	IDSS	VDS=20V, VGS=0V			1	uA
		VDS=20V, VGS=0V TJ=55°C			5	
Drain-Source On-Resistance	RDS(on)	VGS=10V, ID=12A		0.031	0.040	Ω
		VGS=4.5V, ID=7A		0.035	0.045	
		VGS=2.5V, ID=4A		0.040	0.050	
		VGS=1.8V, ID=2A		0.048	0.060	
Forward Transconductance	gfs	VDS=5V, ID=-3.6A		10		S
Diode Forward Voltage	VSD	Is=7A, VGS=0V		0.95	1.2	V
<b>Dynamic</b>						
Total Gate Charge	Qg	VDS=10V, VGS=4.5V ID=12A		4.8	8	nC
Gate-Source Charge	Qgs			1.0		
Gate-Drain Charge	Qgd			1.0		
Input Capacitance	Ciss	VDS=10V, VGS=0V f=1MHz		485		pF
Output Capacitance	Coss			85		
Reverse Transfer Capacitance	Crss			40		
Turn-On Time	td(on)	VDD=10V, RL=6Ω ID=1.0A, VGEN=4.5V RG=6Ω		8	14	ns
	tr			12	18	
Turn-Off Time	td(off)			30	35	
	tf			12	16	



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### TYPICAL CHARACTERISTICS

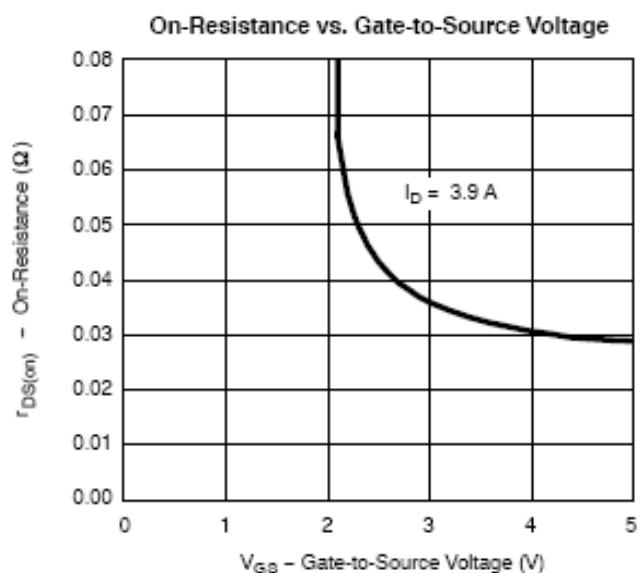
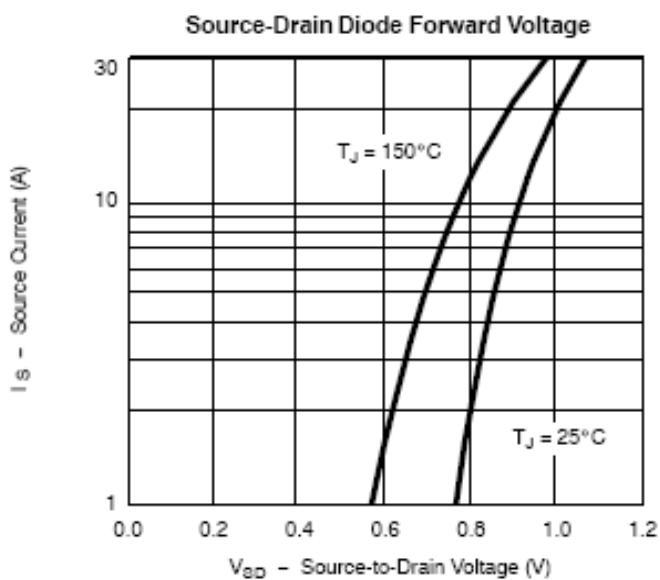
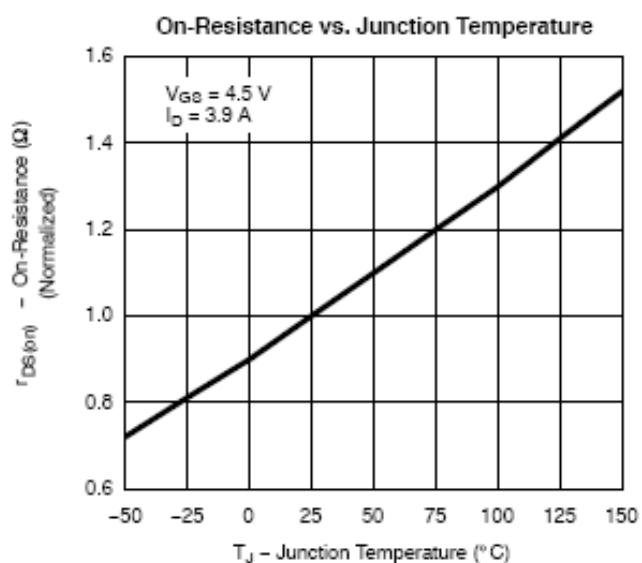
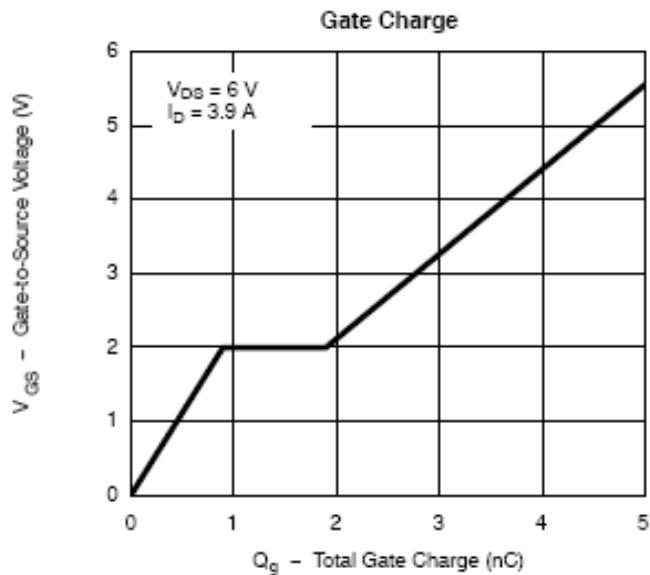




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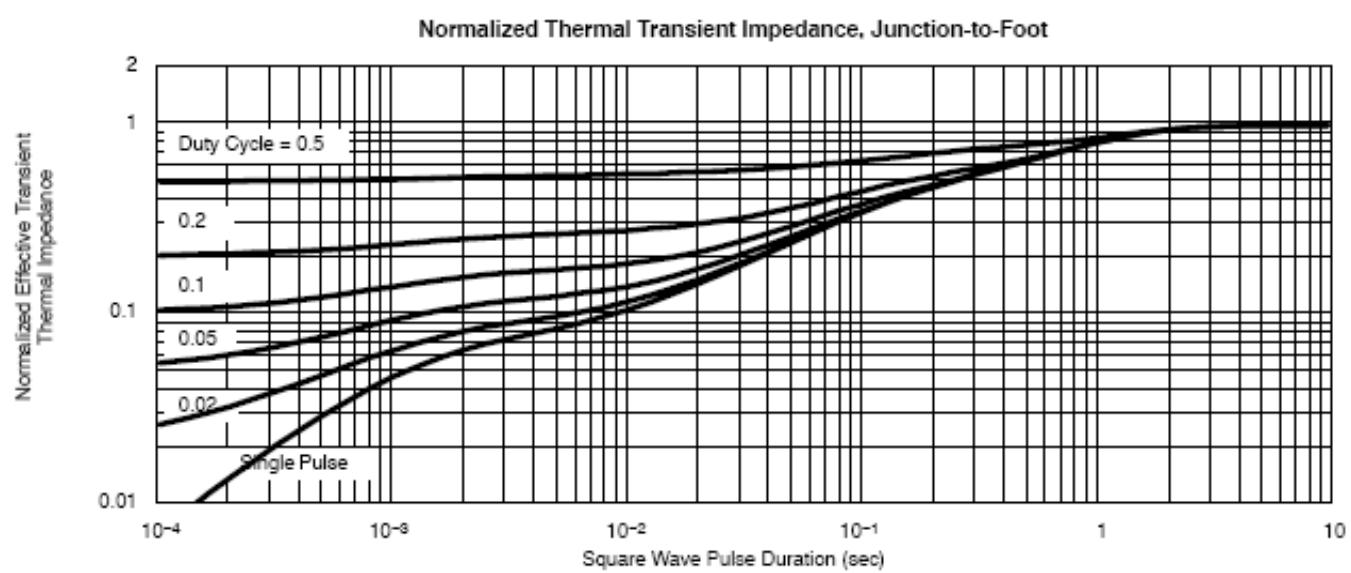
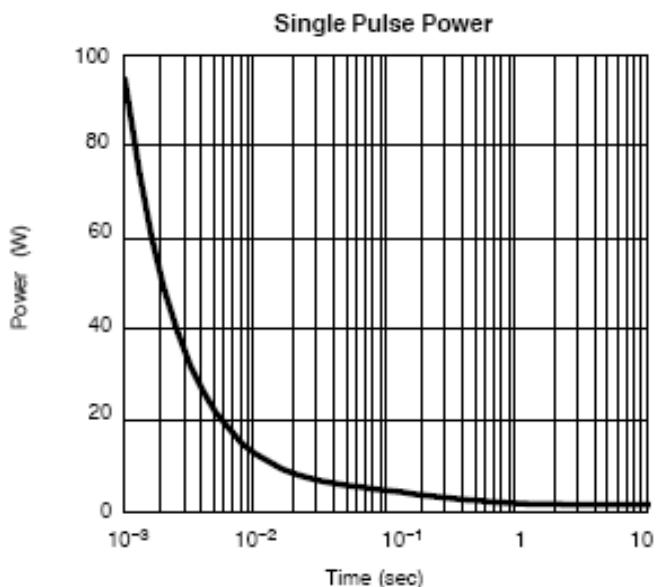
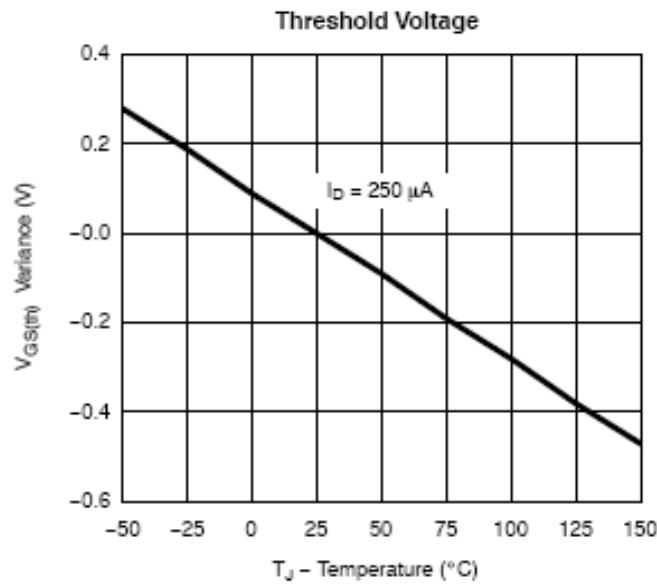




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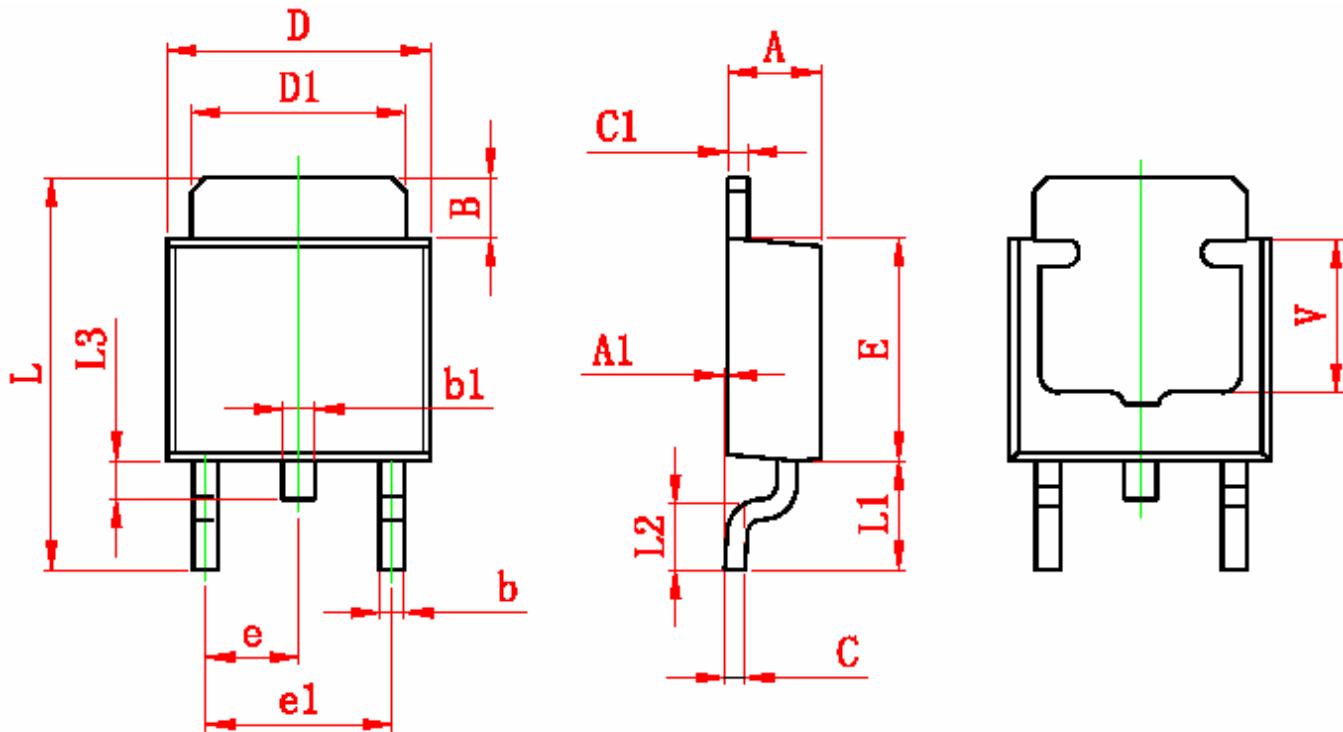




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### TO-252-2L PACKAGE OUTLINE



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
B	1.350	1.650	0.053	0.065
b	0.500	0.700	0.020	0.028
b1	0.700	0.900	0.028	0.035
c	0.430	0.580	0.017	0.023
c1	0.430	0.580	0.017	0.023
D	6.350	6.650	0.250	0.262
D1	5.200	5.400	0.205	0.213
E	5.400	5.700	0.213	0.224
e	2.300 TYP.		0.091 TYP.	
e1	4.500	4.700	0.177	0.185
L	9.500	9.900	0.374	0.390
L1	2.550	2.900	0.100	0.114
L2	1.400	1.780	0.055	0.070
L3	0.600	0.900	0.024	0.035
V	3.800 REF.		0.150 REF.	



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