

ELECTRICAL SPECIFICATIONS:

1.0 TURNS RATIO: (P5-P6-P4) : (J6-J3) : 1CT : 1CT $\pm 2\%$ (P10-P12-P11) : (J2-J1) : 1CT : 1CT $\pm 2\%$

2.0 INDUCTANCE: (P10-P11); (P5-P4) : 350 uH MIN. @ 0.01V, 100KHz, 8 mA DC BIAS

3.0 LEAKAGE INDUCTANCE: P10-P11 (WITH J1 AND J2 SHORT) : 0.3uH MAX. @ 1MHz P5-P4 (WITH J6 AND J3 SHORT) : 0.3uH MAX. @ 1MHz

4.0 INTERWINDING CAPACITANCE: (P10-P11) : (J1-J2) : 30pf MAX @ 1MHz (P5-P4) : (J6-J3) : 30pf MAX @ 1MHz

5.0 DC RESISTANCE: (J6-J3); (J2-J1) : 1.2 ohms Max.

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SHEET

1 OF 4

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6.0 RETURN LOSS: 1MHz TO 30MHz : -18dB MIN. 60MHz TO 80MHz : -12dB MIN.

7.0 DIELECTRIC WITHSTAND: (J1,J2) TO (P10,P11); (J5,J4) TO (P4-P5) : 1500 VAC (J3,J6) TO (P5,P4); (J8,J7) TO (P10, P11) : 1500 VAC

8.0 INSERTION LOSS: RS=RL=100 ohms 100KHz TO 125MHz : -1.1 dB TYP

9.0 RISE TIME: RS=100 OHMS AND RL = 100 OHMS

OUTPUT VOLTAGE = 1 V peak : 3.0 nS MAX PULSE WIDTH= 112nS : 3.0 nS MAX

10.0 CROSS TALK: 1-100 MHz : -35dB TYP

11.0 COMMON TO COMMON MODE ATTENUATION: 1MHz TO 100MHz : -35dB TYP

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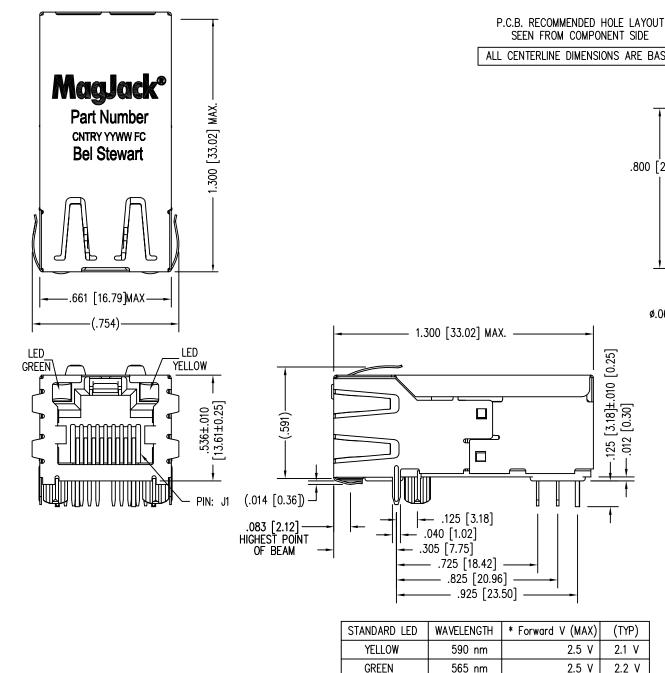
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LE LAYOUT NT SIDE S ARE BASIC.	.390 [9.9] .330 [8.38] .100 [2.54] (2) .010 [0.254] Ø.040 [1.02](4)(LED)
.800 [20.32] .700	0.035 [Ø0.89]
ø.062 [1.	57] (2) (SHIELD)050 [1.27] .068 [1.71]050 [1.27]
	500 [12.70] - 568 [14.41]

NOTES:

- 1. CONNECTOR MATERIALS:
 - HOUSING: THERMOPLASTIC UL94 V-0 CONTACT/SHIELD: COPPER ALLOY SHIELD PLATING: NICKEL OR TIN CONTACT PLATING: SELECTIVE GOLD.

50 MICRO-INCHES MIN. IN CONTACT AREA.

- 2. PIN NOT ELECTRICALLY CONNECTED ARE NOT OMITTED. ALL PINS ARE PRESENT.
- 3. TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS.
- 4. THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE USER THE ABILITY TO HAVE REASONABLE JACK/PANEL CLEARANCES, YET MAINTAIN GROUNDING CAPABILITY.
- 5. SOLDER REFLOW COMPATABLE 230°C/90 SEC MAX.

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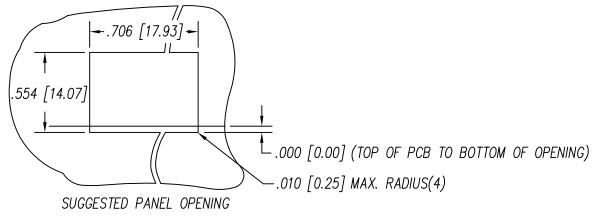
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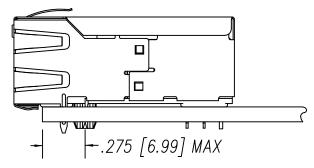
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* WITH A FORWARD CURRENT OF 20 mA





THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY.

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