

3875081 G E SOLID STATE

01E 11063 D

U1897-U1899



T-35-25

U1897-U1899

ELECTRICAL CHARACTERISTICS (Continued) ($T_A = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Conditions	U1897		U1898		U1899		Units	
			Min	Max	Min	Max	Min	Max		
C_{dg}	Drain-Gate Capacitance	$V_{DG} = 20\text{V}, I_S = 0$		5		5		5	pF	
C_{gg}	Source-Gate Capacitance	$V_{SG} = 20\text{V}, I_D = 0$		5		5		5		
C_{iss}	Common-Source Input Capacitance	$V_{DS} = 20\text{V}, V_{GS} = 0$ $f = 1\text{MHz}$ (Note 2)		16		16		16		
C_{rss}	Common-Source Reverse Transfer Capacitance			3.5		3.5		3.5		
$t_{d(on)}$	Turn ON Delay Time (Note 2)	Switching Time Test Conditions				15		15	20	ns
t_r	Rise Time (Note 2)					10		20	40	
t_{off}	Turn OFF Time (Note 2)	V_{DD}	3V	3V	3V					
		$V_{GS(on)}$	0	0	0					
		$V_{GS(off)}$	-12V	-8V	-6V	40		60	80	
		R_L	425Ω	770Ω	1120Ω					
		$I_{D(on)}$	6.6mA	4mA	2.5mA					

NOTES: 1. Pulse test pulsewidth = 300μs; duty cycle < 3%.
 2. For design reference only, not 100% tested.

INTERSIL'S SOLE AND EXCLUSIVE WARRANTY OBLIGATION WITH RESPECT TO THIS PRODUCT SHALL BE THAT STATED IN THE WARRANTY ARTICLE OF THE CONDITION OF SALE. THE WARRANTY SHALL BE EXCLUSIVE AND SHALL BE IN LIEU OF ALL OTHER WARRANTIES, EXPRESS, IMPLIED OR STATUTORY, INCLUDING THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR USE.

NOTE: All typical values have been characterized but are not tested.