



CX24142/3/6/7 Features

- MPEG-2 MP@ML video decoder supporting NTSC and PAL CCIR601 image resolution
- MPEG-1/MPEG-2 and Dolby Digital (AC-3) audio decoder
- MPEG-2/DVB transport stream demultiplexing
- Integrated 1-45 Msym/sec QPSK/BPSK demodulator/FEC with DiSEqC 2.x signaling support (CX24142/3 only)
- 32-bit 146 MIPS ARM920 CPU with 16KB I & D caches
- Dual-slot Common Interface support (CX24142/6 only)
- Dual SmartCard interface for flexible CAS support
- 16-bit unified memory controller architecture
- Integrated NTSC/PAL/SECAM TV encoder supporting simultaneous CVBS, Y/C & RGB/YPrPb analog video output signals
- High-performance 2D graphics rendering acceleration
- Multiplane video/graphics compositing and display engine
- Integrated DVB common descrambler & DES ECB descrambler
- Optional NDS Videoguard conditional access hardware support
- Flicker filtering, aspect ratio conversion and hardware cursor
- 4/8-bit per pixel palletized AYCrCb/YCrCb graphics

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The CX24142/3/6/7 IC family includes four part numbers defined by different functionality combinations.

Part Number	QPSK Demod	VDACs	DRAM	DVB-CI
CX24142	Yes	6	16-bit	Yes
CX24143	Yes	6	16-bit	No
CX24146	No	6	16-bit	Yes
CX24147	No	6	16-bit	No

License-based feature configurations are identified using a part number suffix or dash number.

CX24142/3/6/7-mn

	"-1n"	"-2n"	"-3n"	"-4n"
Macrovision		✓		✓
Dolby Digital			✓	✓

m = License based feature (0 thru 4)

n = die revision (e.g. n=1 for die rev A, 2 for die rev B etc.)

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