

# Advance Information

# Low Dropout 300 mA Voltage Regulator with ON/OFF Control

The MC33375 series are micropower low dropout voltage regulators available in a wide variety of output voltages as well as packages, SOT–223, and SOP–8 surface mount packages. These devices feature a very low quiescent current and are capable of supplying output currents up to 300 mA. Internal current and thermal limiting protection are provided by the presence of a short circuit at the output and an internal thermal shutdown circuit.

The MC33375 has a control pin that allows a logic level signal to turn–off or turn–on the regulator output.

Due to the low input-to-output voltage differential and bias current specifications, these devices are ideally suited for battery powered computer, consumer, and industrial equipment where an extension of useful battery life is desirable.

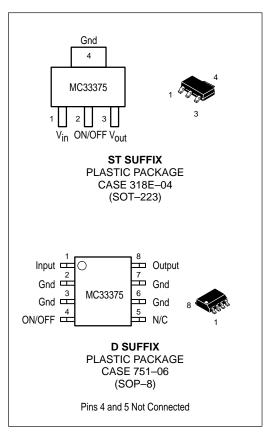
#### Features:

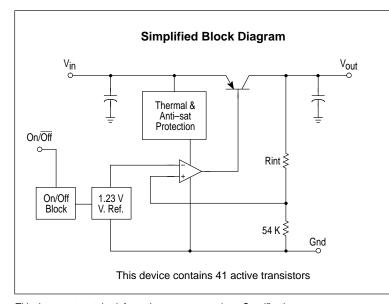
- Low Quiescent Current (0.3 μA in OFF mode; 125 μA in ON mode)
- Low Input–to–Output Voltage Differential of 25 mV at I<sub>O</sub> = 10 mA, and 260 mV at I<sub>O</sub> = 300 mA
- Extremely Tight Line and Load Regulation
- Stable with Output Capacitance of only 0.33  $\mu$ F for 2.5 V Output Voltage
- Internal Current and Thermal Limiting
- Logic Level ON/OFF Control



# LOW DROPOUT MICROPOWER VOLTAGE REGULATOR SEMICONDUCTOR

TECHNICAL DATA





This document contains information on a new product. Specifications and information herein are subject to change without notice.

### ORDERING INFORMATION

Device	Туре	Operating Temperature Range, Tolerance	Case	Package
MC33375ST-2.5	2.5 V		318E	SOT-223
MC33375D-2.5	$(Fixed Voltage)$ $3.0 V$ $(Fixed Voltage)$ $1\% Tolerance$ $at T_A = 25^{\circ}C$ $3.3 V$ $(Fixed Voltage)$ $2\% Tolerance at$ $T_J from -40 to +125^{\circ}C$ $5.0 V$ $(Fixed Voltage)$		751–5	SOP-8
MC33375ST-3.0		1% Tolerance	318E	SOT-223
MC33375D-3.0			751–5	SOP-8
MC33375ST-3.3			318E	SOT-223
MC33375D-3.3		Т <sub>Ј</sub> from –40 to +125°С	751–5	SOP-8
MC33375ST-5.0			318E	SOT-223
MC33375D-5.0			751–5	SOP-8

#### DEVICE MARKING

Device	Version	Marking (1st line)	
MC33375	2.5V	37525	
MC33375	3.0V	37530	
MC33375	3.3V	37533	
MC33375	5.0V	37550	

#### TAPE AND REEL SPECIFICATIONS

Device	Reel Size Tape Width		Quantity
MC33375D	13"	12mm embossed tape	2500 units
MC33375ST	13"	8mm embossed tape	4000 units

# **MAXIMUM RATINGS** (T<sub>A</sub> = 25°C, for min/max values T<sub>J</sub> = -40°C to +125°C)

Rating	Symbol	Value	Unit
Input Voltage	VCC	13	Vdc
Power Dissipation and Thermal Characteristics $T_A = 25^{\circ}C$			
Maximum Power Dissipation Case 751 (SOP–8) D Suffix	PD	Internally Limited	W
Thermal Resistance, Junction-to-Ambient	R <sub>θJA</sub>	160	°C/W
Thermal Resistance, Junction–to–Case Case 318E (SOT–223) ST Suffix	R <sub>θJC</sub>	25	°C/W
Thermal Resistance, Junction-to-Air	R <sub>θJA</sub>	245	°C/W
Thermal Resistance, Junction-to-Case	R <sub>0JC</sub>	15	°C/W
Output Current	IO	300	mA
Maximum Junction Temperature	Тj	150	°C
Operating Junction Temperature Range	Тj	- 40 to +125	°C
Storage Temperature Range	T <sub>stg</sub>	– 65 to +150	°C

# **ELECTRICAL CHARACTERISTICS** (C<sub>L</sub> = 1.0 $\mu$ F, T<sub>A</sub> = 25°C, for min/max values T<sub>J</sub> = -40°C to +125°C, Note 1)

	Characteristic	Symbol	Min	Тур	Max	Unit
Output Voltage 2.5 V Suffix 3.0 V Suffix 3.3 V Suffix 5.0 V Suffix	$I_{O} = 0 \text{ mA to } 250 \text{ mA}$ $T_{A} = 25^{\circ}\text{C}, V_{in} = [V_{O} + 1] \text{ V}$	Vo	2.475 2.970 3.267 4.950	2.50 3.00 3.30 5.00	2.525 3.030 3.333 5.05	Vdc
2.5 V Suffix 3.0 V Suffix 3.3 V Suffix 5.0 V Suffix	$V_{in} = [V_O + 1] V, 0 < I_O < 100 mA$ 2% Tolerance from $T_J = -40$ to +125°C		2.450 2.940 3.234 4.900		2.550 3.060 3.366 5.100	
Line Regulation	$V_{in} = [V_O + 1] V \text{ to } V_{max}, I_O = 250 \text{ mA},$ All Suffixes $T_A = 25^{\circ}C$	Reg <sub>line</sub>	-	2.0	10	mV
Load Regulation	$V_{in} = [V_O + 1] V$ , $I_O = 0$ mA to 250 mA, All Suffixes $T_A = 25^{\circ}C$	Regload	-	5.0	25	mV
Dropout Voltage $I_O = 10 \text{ mA}$ $I_O = 100 \text{ mA}$ $I_O = 250 \text{ mA}$ $I_O = 300 \text{ mA}$	$T_{J} = -40^{\circ}C$ to +125°C	V <sub>in</sub> – V <sub>O</sub>	 	25 115 220 260	100 200 400 500	mV
Ripple Rejection (	(120 Hz) $V_{in(peak-peak)} = [V_O + 1.5] V \text{ to } [V_O + 12] V$	_	65	75	—	dB
Output Noise Volt $C_L = 1 \ \mu F$ $C_L = 200 \ \mu F$	age I <sub>O</sub> = 50 mA (10 Hz to 100 kHz)	Vn		160 46		μVrms
CURRENT PAR	AMETERS	1				
		I .	1	1		1 .

Quiescent Curren	t	lQ				μΑ
On Mode	V <sub>in</sub> = [V <sub>O</sub> + 1] V, I <sub>O</sub> = 0 mA		-	125	200	
Off Mode			_	0.3	4.0	
On Mode SAT	$V_{in} = [V_O - 0.5] V$ , $I_O = 0$ mA, Note 2		-	1100	1500	
Current Limit	$V_{in} = [V_O + 1], V_O$ shorted	ILIMIT	-	450	—	mA
ON/OFF INPUTS						
		Ma iau				<u>۷</u>

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	Logic "0" Regulator Off) $V_{out} < 0.03V$		<u> </u>	_	0.5		
	Logic "1" (Regulator On) $V_{out} = V_O \pm 2\%$	01/01	24				
	On/Off Input Voltage	VOn/Off				V	

#### THERMAL SHUTDOWN

Thermal Shutdown

**NOTE:** 1. Low duty pulse techniques are used during test to maintain junction temperature as close to ambient as possible. 2. Quiescent Current is measured where the PNP pass transistor is in saturation.  $V_{in} = [V_O - 0.5] V$  guarantees this condition.

°C

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150

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## MC33375 DEFINITIONS

**Load Regulation** – The change in output voltage for a change in load current at constant chip temperature.

**Dropout Voltage** – The input/output differential at which the regulator output no longer maintains regulation against further reductions in input voltage. Measured when the output drops 100 mV below its nominal value (which is measured at 1.0 V differential), dropout voltage is affected by junction temperature, load current and minimum input supply requirements.

**Output Noise Voltage** – The RMS AC voltage at the output with a constant load and no input ripple, measured over a specified frequency range.

**Maximum Power Dissipation** – The maximum total dissipation for which the regulator will operate within specifications.

**Quiescent Current** – Current which is used to operate the regulator chip and is not delivered to the load.

Line Regulation – The change in output voltage for a change in the input voltage. The measurement is made under conditions of low dissipation or by using pulse techniques such that the average chip temperature is not significantly affected.

**Maximum Package Power Dissipation** – The maximum package power dissipation is the power dissipation level at which the junction temperature reaches its maximum value i.e. 150°C. The junction temperature is rising while the difference between the input power ( $V_{CC} \times I_{CC}$ ) and the output power ( $V_{out} \times I_{out}$ ) is increasing.

Depending on ambient temperature, it is possible to calculate the maximum power dissipation and so the maximum current as following:

$$\mathsf{Pd} = \frac{\mathsf{T}_{J} - \mathsf{T}_{A}}{\mathsf{R}_{\theta JA}}$$

The maximum operating junction temperature  $T_J$  is specified at 150°C, if  $T_A = 25$ °C, then P<sub>D</sub> can be found. By neglecting the quiescent current, the maximum power dissipation can be expressed as:

$$I_{out} = \frac{P_D}{V_{CC} - V_{out}}$$

The thermal resistance of the whole circuit can be evaluated by deliberately activating the thermal shutdown of the circuit (by increasing the output current or raising the input voltage for example).

Then you can calculate the power dissipation by subtracting the output power from the input power. All variables are then well known: power dissipation, thermal shutdown temperature (150°C for MC33375) and ambient temperature.

$$\mathsf{R}_{\theta \mathsf{J} \mathsf{A}} = \frac{\mathsf{T}_{\mathsf{J}} - \mathsf{T}_{\mathsf{A}}}{\mathsf{P}_{\mathsf{D}}}$$

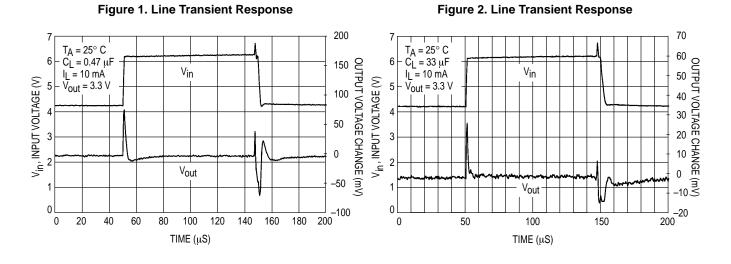


Figure 3. Load Transient Response



LOAD CURRENT

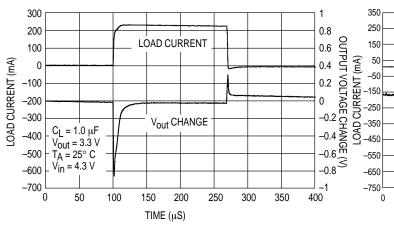
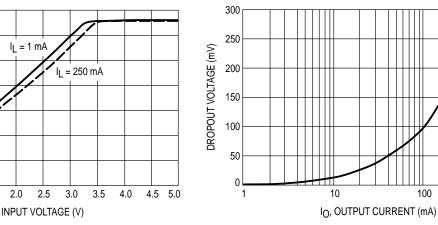


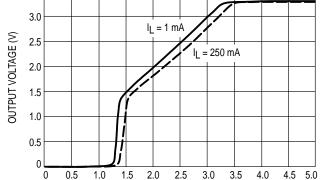
Figure 5. Output Voltage versus Input Voltage



Vout CHANGE

Figure 6. Dropout Voltage versus Output Current





3.5



1000

0.14

0.09

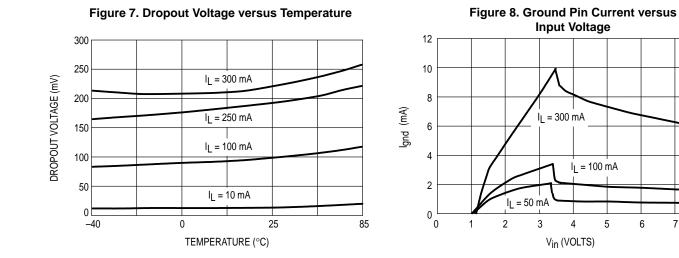
0.04

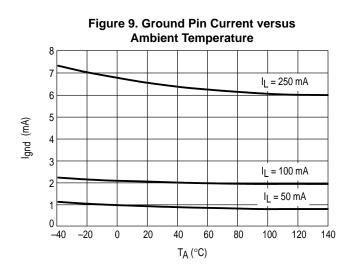
CL = 33.0 μF

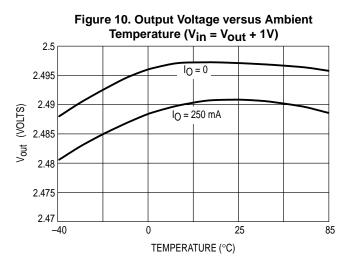
 $V_{out} = 3.3 V$ T<sub>A</sub> = 25° C

Vin = 4.3 V

-0.16







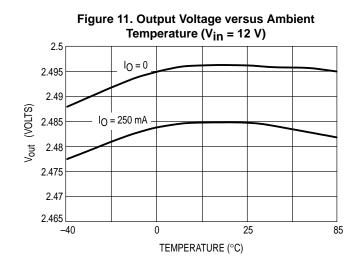


Figure 12. Ripple Rejection

1

 $I_L = 1 \text{ mA}$ 

FREQUENCY (kHz)

IL = 10 mA

10

70

60

50

40

30

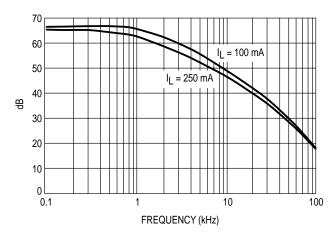
20 10

0

0.1

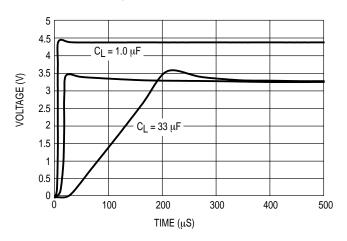
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Figure 13. Ripple Rejection

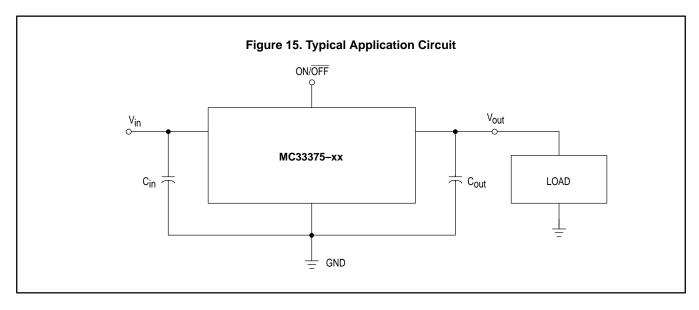




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## MC33375 APPLICATIONS INFORMATION



The MC33375 regulators are designed with internal current limiting and thermal shutdown making them user-friendly. Figure 15 is a typical application circuit. The output capability of the regulator is in excess of 300 mA, with a typical dropout voltage of less than 260 mV. Internal protective features include current and thermal limiting.

#### **EXTERNAL CAPACITORS**

These regulators require only a 0.33 µF (or greater) capacitance between the output and ground for stability for 2.5 V, 3.0 V, and 3.3 V output voltage options. Output voltage options of 5.0 V require only 0.22 µF for stability. The output capacitor must be mounted as close as possible to the MC33375. If the output capacitor must be mounted further than two centimeters away from the MC33375, then a larger value of output capacitor may be required for stability. A value of 0.68 µF or larger is recommended. Most type of aluminum, tantalum, or multilayer ceramic will perform adequately. Solid tantalums or appropriate multilayer ceramic capacitors are recommended for operation below 25°C. An input bypass capacitor is recommended to improve transient response or if the regulator is connected to the supply input filter with long wire lengths, more than 4 inches. This will reduce the circuit's sensitivity to the input line impedance at high frequencies. A 0.33 µF or larger tantalum, mylar, ceramic, or other capacitor having low internal impedance at high frequencies should be chosen. The bypass capacitor should be mounted with shortest possible lead or track length directly across the regulator's input terminals.

Applications should be tested over all operating conditions to insure stability.

#### THERMAL PROTECTION

Internal thermal limiting circuitry is provided to protect the integrated circuit in the event that the maximum junction temperature is exceeded. When activated, typically at 150°C, the output is disabled. There is no hysteresis built into the thermal protection. As a result the output will appear to be oscillating during thermal limit. The output will turn off until the temperature drops below the 150°C then the output turns on again. The process will repeat if the junction increases above the threshold. This will continue until the existing conditions allow the junction to operate below the temperature threshold.

# Thermal limit is not a substitute for proper heatsinking.

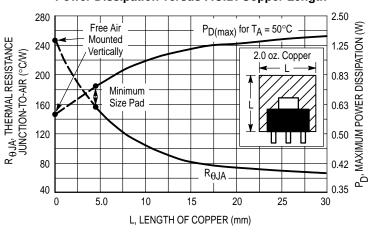
The internal current limit will typically limit current to 450 mA. If during current limit the junction exceeds 150°C, the thermal protection will protect the device also. **Current limit is not a substitute for proper heatsinking.** 

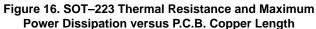
#### **OUTPUT NOISE**

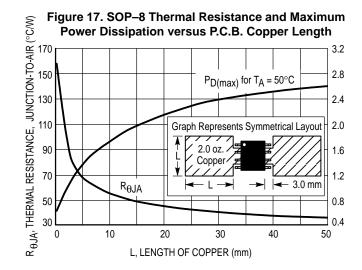
In many applications it is desirable to reduce the noise present at the output. Reducing the regulator bandwidth by increasing the size of the output capacitor will reduce the noise on the MC33375.

#### **ON/OFF PIN**

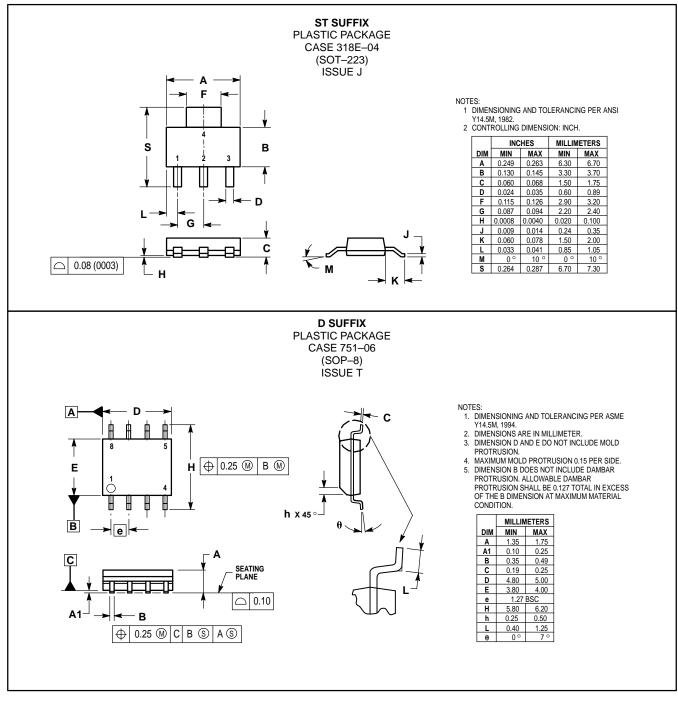
When this pin is pulled low, the MC33375 is off. This pin should not be left floating. The pin should be pulled high for the MC33375 to operate.







### **OUTLINE DIMENSIONS**





MC33375 **NOTES** 

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