

MNLM611AM-X REV 0BL

 Original Creation Date: 08/02/95
 Last Update Date: 11/12/98
 Last Major Revision Date: 08/02/95

OPERATIONAL AMPLIFIER AND ADJUSTABLE REFERENCE
Industry Part Number

LM611

NS Part Numbers

LM611AMJ/883

Prime Die

LM611

Processing

MIL-STD-883, Method 5004

Quality Conformance Inspection

MIL-STD-883, Method 5005

Subgrp	Description	Temp (°C)
1	Static tests at	+25
2	Static tests at	+125
3	Static tests at	-55
4	Dynamic tests at	+25
5	Dynamic tests at	+125
6	Dynamic tests at	-55
7	Functional tests at	+25
8A	Functional tests at	+125
8B	Functional tests at	-55
9	Switching tests at	+25
10	Switching tests at	+125
11	Switching tests at	-55

Electrical Characteristics

DC PARAMETERS

(The following conditions apply to all the following parameters, unless otherwise specified.)

DC: $V_- = \text{Gnd} = 0\text{V}$, $V_+ = 5\text{V}$, $V_{\text{cm}} = V_{\text{out}} = V_+/2$, $I_r = 100\mu\text{A}$ Feedback Pin Shorted to Gnd

SYMBOL	PARAMETER	CONDITIONS	NOTES	PIN-NAME	MIN	MAX	UNIT	SUB-GROUPS
I _{cc}	Total Supply Current	R _{load} = Infinity, $4\text{V} \leq V_+ \leq 36\text{V}$				300	uA	1
						320	uA	2, 3
V _s	Supply Voltage Range				2.8	36	V	1
					3.0	36	V	2, 3

DC PARAMETERS: Operational Amplifier

(The following conditions apply to all the following parameters, unless otherwise specified.)

DC: $V_- = \text{Gnd} = 0\text{V}$, $V_+ = 5\text{V}$, $V_{\text{cm}} = V_{\text{out}} = V_+/2$, $I_r = 100\mu\text{A}$ Feedback Pin Shorted to Gnd

V _{io1}	Offset Voltage Over V ₊ Range	$4\text{V} \leq V_+ \leq 36\text{V}$			-3.5	3.5	mV	1
					-6.0	6.0	mV	2, 3
V _{io2}	Offset Voltage Over V _{cm} Range	$V_{\text{cm}} = 0\text{V}$ through $V_{\text{cm}} = (V_+ - 1.4\text{V})$ Room, $(V_+ - 1.8\text{V})$ Hot and Cold.			-3.5	3.5	mV	1
					-6.0	6.0	mV	2, 3
I _{ib}	Input bias Current				-25	25	nA	1
					-30	30	nA	2, 3
I _{io}	Input Offset Current				-4	4	nA	1
					-5	5	nA	2, 3
CMRR	Common-Mode Rejection Ratio	$V_+ = 30\text{V}$, $0\text{V} \leq V_{\text{cm}} \leq 1.4\text{V}(\text{Room})$ and $\leq 1.8\text{V}(\text{Over Temp})$ $\text{CMRR} = 20\log(\Delta V_{\text{cm}}/\Delta V_{\text{io}})$			80		dB	1
					75		dB	2, 3
PSRR	Power Supply Rejection Ratio	$4\text{V} \leq V_+ \leq 30\text{V}$, $V_{\text{cm}} = V_+/2$ $\text{PSRR} = 20\log(\Delta V_+/\Delta V_{\text{io}})$			80		dB	1
					75		dB	2, 3
I _{out}	Output Source Current	$V_{\text{out}} = V_+ - 2.5\text{V}$, $V_{\text{+in}} = 0\text{V}$, $V_{\text{-in}} = -0.3\text{V}$				-20	mA	1
						-13	mA	2, 3
I _{sink}	Output Sink Current	$V_{\text{out}} = 1.6\text{V}$, $V_{\text{+in}} = 0\text{V}$, $V_{\text{-in}} = 0.3\text{V}$			14		mA	1
					8		mA	2, 3
I _{short}	Short Circuit Current	$V_{\text{out}} = 0\text{V}$, $V_{\text{+in}} = 3\text{V}$, $V_{\text{-in}} = 2\text{V}$, Source				50	mA	1
						60	mA	2, 3
		$V_{\text{out}} = 5\text{V}$, $V_{\text{+in}} = 2\text{V}$, $V_{\text{-in}} = 3\text{V}$, Sink				60	mA	1
						80	mA	2, 3

Electrical Characteristics

DC PARAMETERS: Voltage Reference

(The following conditions apply to all the following parameters, unless otherwise specified.)

DC: $V^- = \text{Gnd} = 0\text{V}$, $V^+ = 5\text{V}$, $V_{\text{cm}} = V_{\text{out}} = V^+/2$, $I_r = 100\mu\text{A}$ Feedback Pin Shorted to Gnd

SYMBOL	PARAMETER	CONDITIONS	NOTES	PIN-NAME	MIN	MAX	UNIT	SUB-GROUPS
V_r	Voltage Reference		1		1.2365	1.2515	V	1
$\Delta V_r / \Delta I_r$	Vr Change with Current	$V_r(100\mu\text{A}) - V_r(17\mu\text{A})$			-1	1	mV	1
					-1.1	1.1	mV	2, 3
		$V_r(10\text{mA}) - V_r(100\mu\text{A})$	2		-5	5	mV	1
			2		-5.5	5.5	mV	2, 3
R	Resistance	$\Delta V_r(10\text{mA to } 100\mu\text{A})/9.9\text{mA}$	3			0.51	Ohms	1
			3			0.56	Ohms	2, 3
		$\Delta V_r(100\mu\text{A to } 17\mu\text{A})/83\mu\text{A}$	3			12	Ohms	1
			3			13	Ohms	2, 3
$\Delta V_r / \Delta V_{ro}$	Vr Change with High V_{ro}	$V_r(V_{ro}=V_r) - V_r(V_{ro}=6.3\text{V})$ (5.06V between Anode and FEEDBACK)				7	mV	1
						10	mV	2, 3
$\Delta V_r / \Delta V^+$	Vr Change with V^+ Change	$V_r(V^+=5\text{V}) - V_r(V^+=36\text{V})$			-1.2	1.2	mV	1
					-1.3	1.3	mV	2, 3
		$V_r(V^+=5\text{V}) - V_r(V^+=3\text{V})$			-1	1	mV	1
					-1.5	1.5	mV	2, 3
$\Delta V_r / \Delta V_{a\text{node}}$	Vr Change with Vanode Change	$V^+ = V^+_{\text{max}}$, $\Delta V_r = V_r(\text{at } V_{\text{anode}} = V^- = \text{Gnd})$ $- V_r(\text{at } V_{\text{anode}} = V^+ - 1.0\text{V})$				1.5	mV	1
						3.0	mV	2, 3
$I_b(I_f)$	FEEDBACK Bias Current	I_{fb} ; $V_{\text{anode}} \leq V_{fb} \leq 5.06\text{V}$				35	nA	1
						40	nA	2, 3

DC PARAMETERS: Operational Amplifiers

(The following conditions apply to all the following parameters, unless otherwise specified.)

DC: $V^- = \text{Gnd} = 0\text{V}$, $V^+ = 5\text{V}$, $V_{\text{cm}} = V_{\text{out}} = V^+/2$, $I_r = 100\mu\text{A}$ Feedback Pin Shorted to Gnd

A_v	Open Loop Voltage Gain	$R_l = 10\text{K}\Omega$, $V^+ = 30\text{V}$, $5\text{V} \leq V_{\text{out}} \leq 25\text{V}$			100		V/mV	4
					40		V/mV	5, 6
V_{o1}	Output Voltage Swing High	$R_l = 10\text{K}\Omega$, $V^+ = 36\text{V}$			$V^+ - 1.7$		V	4
					$V^+ - 1.9$		V	5, 6
V_{o2}	Output Voltage Swing Low	$R_l = 10\text{K}\Omega$ to V^+ , $V^+ = 36\text{V}$				$V^- + 0.9$	V	4
						$V^- + 1.0$	V	5, 6

Electrical Characteristics

AC PARAMETERS

(The following conditions apply to all the following parameters, unless otherwise specified.)

AC: $V_- = \text{Gnd} = 0\text{V}$, $V_+ = 5\text{V}$, $V_{\text{cm}} = V_{\text{out}} = V_+/2$, $I_r = 100\mu\text{A}$ Feedback Pin Shorted to Gnd

SYMBOL	PARAMETER	CONDITIONS	NOTES	PIN-NAME	MIN	MAX	UNIT	SUB-GROUPS
Sr	Slew Rate	$V_+ = 30\text{V}$	4		0.55		V/ μS	7
			4		0.45		V/ μS	8A, 8B

Note 1: V_{ro} is the reference output voltage, which may be set for 1.2V to 6.3V. V_r is the V_{ro} -to Feedback voltage (nominally 1.244V).

Note 2: Low contact resistance is required for accurate measurement.

Note 3: Guaranteed by V_r change with current.

Note 4: Slew rate is measured with the op amp in a voltage follower configuration. For rising slew rate, the input voltage is driven from 5V to 25V, and the output voltage transition is sampled at 10V and 20V. For falling slew rate, the input voltage is driven from 25V to 5V, and the output voltage transition is sampled at 20V and 10V.

Revision History

Rev	ECN #	Rel Date	Originator	Changes
0BL	M0001697	11/12/98	Barbara Lopez	Changed: MNLM611AM-X Rev. 0AL to MNLM611AM-X Rev. 0BL.