

# 2-INPUT 1-OUTPUT VIDEO SWITCH

## **GENERAL DESCRIPTION**

The NJM2533 is a video switch for VCR, TV, and others. It contains two bias-type inputs and one buffer-type output.

## **FEATURES**

Operating Voltage

Low Operating Current

(MAX:3.7mA)(-70dB)

 $(+4.75V \sim +13V)$ 

Crosstalk

2-Input, 1-Output

Bipolar Technology

Package Outline

DIP8, DMP8, SIP8, SSOP8

### **■ PACKAGE OUTLINE**





NJM2533D

NJM2533M

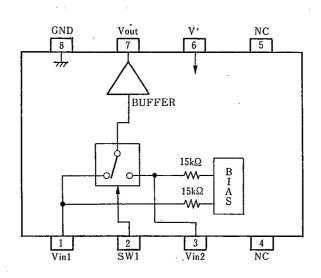




NJM2533L

NJM2533V

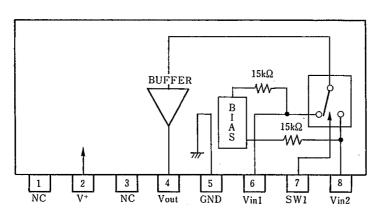
## **■ PIN CONFIGURATION**



PIN FUNCTION 1: Vin1 2: SW1 3: Vin2

3 . Vin2 4 : NC 5 : NC 6 : V<sup>+</sup> 7 : V<sub>OUT</sub> 8 : GND

NJM2533D NJM2533M NJM2533V



### PIN FUNCTION

PIN FUN

1 : NC

2 : V\*

3 : NC

4 : Vout

5 : GND

6 : Vin1

7 : SW1

8: Vin2

NJM2533L

## **■ ABSOLUTE MAXIMUM RATINGS**

(Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT	
Supply Voltage	V+	+15	V	
Power Dissipation		(DIP-8) 500		
	PD	(DMP-8) 300	mW	
		(SIP-8) 800		
		(SSOP-8) 250		
Operating Temperature Range	T <sub>opr</sub>	-20~+75	°C	
Storage Temperature Range	T <sub>stg</sub>	-40~+125	°C	

## **■ ELECTRICAL CHARACTERISTICS**

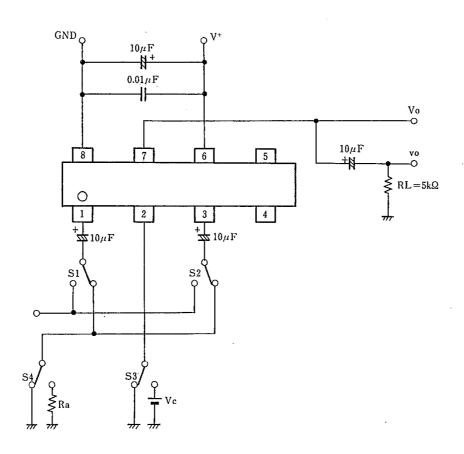
(V\*=5V, Ta=25°C)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Operating Voltage	V+		+4.5	_	+13.0	v
Operating Current	I <sub>cc</sub>			2.7	3.7	mA
Frequency Characteristics	Gf	V <sub>IN</sub> =2Vpp, Vo=10MHz/100kHz	-1.0	0	+1.0	dB
Voltage Gain	Gv	V <sub>IN</sub> =2Vpp, 100kHz	-0.5	0	+0.5	dB
Total Harmonic Distortion	THD	V <sub>IN</sub> =2.5Vpp, 1kHz	1	0.05	0.1	%
Differential Gain	DG	V <sub>IN</sub> =2Vpp, Standard staircase signal, APL=50%	_	0	3.0	%
Differential Phase	DP	V <sub>IN</sub> =2Vpp, Standard staircase signal, APL=50%		0	3.0	deg
Output Offset Voltage	V <sub>off</sub>		-15	0	+15	mV
Crosstalk	СТ	V <sub>IN</sub> =2Vpp, 4.3MHz		-70	-60	dB
Switching Voltage	V <sub>CH</sub>		2.4	_		V
	V <sub>CL</sub>				0.8	V
Input Impedance	RI		_	30	_	kΩ
Output Impedance	Ro		_	25		Ω
Input Bias Voltage	ViN		_	2.5		٧

## **■ CONTROL SIGNAL-OUTPUT SIGNAL**

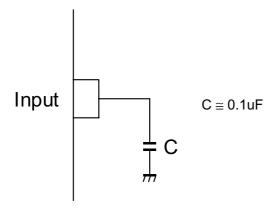
SW1	OUTPUT SIGNAL
L	V <sub>IN</sub> 1
Н	V <sub>IN</sub> 2

## **■ TEST CIRCUIT**



## **■**APPLICATION

This IC requires 0.1uF capacitor between INPUT and GND for bias type input at mute mode.



[CAUTION]
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