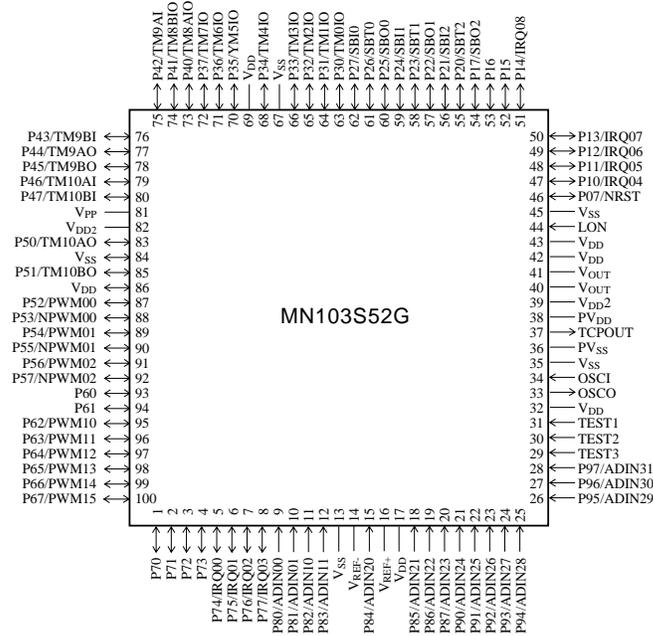


# □ MN103S52G

<b>Type</b>		MN103S52G (under development)	
<b>Internal ROM</b>		128 K-byte	
<b>Internal RAM</b>		4 K-byte SRAM	
<b>Package</b>		QFP100-P-1818B *Lead-free	
<b>Minimum Instruction Execution Time</b>		25 ns (at 3.0 V to 3.6 V, 40 MHz)	
<b>Interrupts</b>		<ul style="list-style-type: none"> <li>• 9 external interrupts</li> <li>• 35 internal interrupts (watch dog timer, timer, serial I/F, PWM, system error)</li> </ul>	
<b>Timer Counter</b>		<p>Eight 8-bit timers Interval timer, Event counter, Cascading</p> <p>Four 16-bit timers Interval timer, Event counter, PWM output, Double buffer</p>	
<b>Serial Interface</b>		UART / synchronous interfaces selective: 3	
<b>I/O Pins</b>	<b>I/O</b>	56	exclusive: 6, selective: 50
	<b>Input</b>	16	selective: 16
<b>A/D Inputs</b>		10-bit, 2 inputs: 2, 12 inputs: 1	
<b>PWM</b>		<p>3-phase PWM output</p> <p>16-bit counter, triangular waveform or jigsaw waveform dead time setup</p>	
<b>Electrical Characteristics</b>		T.B.D.	

Pin Assignment



QFP100-P-1818B \*Lead-free

Support Tool

In-circuit Emulator	PX-ICE101C / D + PX-PRB101D08-QFP100-P-1818B-M	
Flash Memory Built-in Type	Type	MN103SF52G (under development)
	ROM	128 K-byte
	RAM	4 K-byte SRAM
	Minimum instruction execution time	25 ns (at 3.0 V to 3.6 V, 40 MHz)
	Package	QFP100-P-1818B *Lead-free

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