



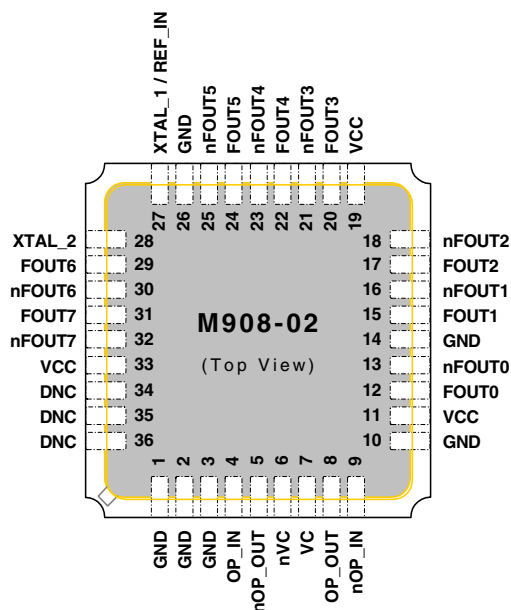
GENERAL DESCRIPTION

The M908-02 is a PLL (Phase Locked Loop) based clock generator that uses an internal VCSO (Voltage Controlled SAW Oscillator) to produce a very low jitter output clock. Eight LVPECL clock output pairs are provided. Any output frequency between 75MHz and 175MHz can be supported (nominal output frequency is specified at time of device order). The output frequency accuracy is assured by the internal PLL that phase-locks the internal VCSO to an external frequency reference. The input reference can either be an external crystal, utilizing the internal crystal oscillator, or a stable external clock source such as a canned crystal oscillator.

FEATURES

- Output clock frequency from 75MHz to 175MHz (Consult factory for frequency availability)
- Eight identical LVPECL output pairs
- Low jitter 0.7ps rms (over 12kHz-20MHz)
- Ideal for OC-12/48 clock reference
- Output-to-output skew < 250ps
- XTAL or LVCMOS reference input
- Integrated SAW (surface acoustic wave) delay line
- Single 3.3V power supply
- Small 9 x 9 mm SMT (surface mount) package

PIN ASSIGNMENT (9 x 9 mm SMT)



Example Output Frequency Configurations

Ref Clock Frequency (MHz)	PLL Multiplication Ratio	VCSO and Output Frequency ¹ (MHz)	Application
19.44	8	155.52	OC-12/48

1. Specify VCSO center frequency at time of order

BLOCK DIAGRAM

